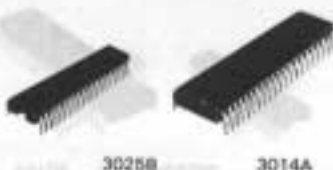


LM6402A, 6405A



3025B

3014A

LM6402A 6405A

N-Channel E/D MOS LSI

Single-Chip 4-Bit Microcomputer

Basic Type, On-Chip 24-Character PLA, High-Current Driver

©1980

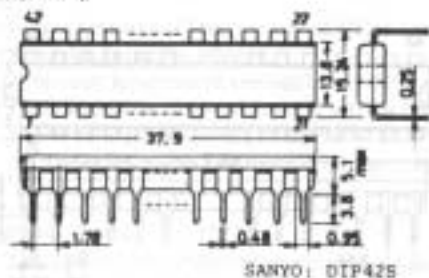
General Description

LM6402A and 6405A are N-channel MOS 4-bit microcomputers with ROM, RAM, ALU, I/O ports, timer, and clock generator, built on a single chip. The microcomputer, for control applications, has a ROM memory with a maximum capacity of 2K bytes, a RAM with 128 x 4 bits, I/O ports with 35 terminals, and internal PLA.

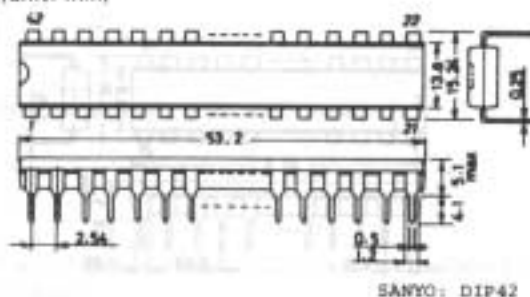
- 1) Hardware features (reduction in externally connected component parts)
 - An N-channel E/D MOS for a single 5 V power supply
 - C MOS and TTL compatible
 - LED driver output ports (2 ports/8 pins)
 - 24 character PLA
 - Dual purpose I/O ports (7 ports/27 pins)
 - Flexible output (open drain/with pull-up resistors)
 - Clock oscillator (with externally connected ceramic oscillating element)
 - Cycle time 10 μ s
 - 1K byte/2K byte pin compatible
 - Programmable input threshold voltages (normal/high thresholds)
 - Initial reset and external interrupt input terminals containing Schmitt gate at external.
- 2) Software features (effective utilization of ROM capacity)
 - 4-level subroutine nesting feasible
 - Interrupt functions (external/internal)
 - Built-in programmable timer capable of a wide range of designations (timer, pulse count, and pulse width measure)
 - A rich variety of instructions of 82 kinds
 - Automatic page select
 - 1K byte/2K byte instruction compatible

For details, refer to LM6402, 6405 Series User's Manual.

Case Outline 3025B-D42SIC
(unit: mm)



Case Outline 3014A-D421C
(unit: mm)



* The rest is omitted. For further details, refer to the separate catalog.