

LM2406 Monolithic 9 ns Triple CRT Driver

Features

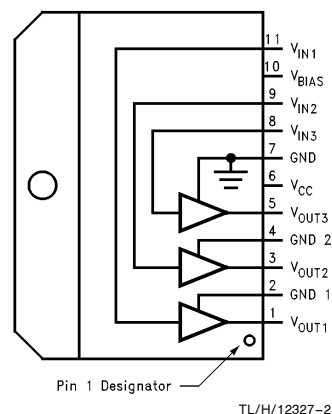
- Output swing capability: 50 V_{PP} for V_{CC} = 80
40 V_{PP} for V_{CC} = 70
30 V_{PP} for V_{CC} = 60
- Pinout designed for easy PCB layout
- 1V to 7V input range
- Stable with 0 pF–20 pF capacitive loads
- Convenient TO-220 staggered lead style package

Applications

- CRT driver for 1024 x 768 (Non-interlaced) and SVGA display resolution color monitors
- Pixel clock frequency up to 80 MHz

[illegible]

FIGURE 1. Simplified Schematic Diagram (One Channel)



Note: Tab is at GND

Top View

Absolute Maximum Ratings (Notes 1, 3)

Supply Voltage, V_{CC}	+95V
Bias Voltage, V_{BIAS}	+16V
Input Voltage, V_{IN}	−0.5V to V_{BIAS} + 0.5V
Storage Temperature Range, T_{STG}	−65°C to +150°C
Lead Temperature (Soldering, <10 sec.)	300°C
ESD Tolerance	2 kV

Operating Range (Note 2)

V_{CC}	+60V to +85V
V_{BIAS}	+8V to +15V
V_{IN}	+1V to +7V
Case Temperature, T_{CASE}	−20°C to +100°C
Do not operate the part without a heat sink	

Electrical Characteristics

Unless otherwise noted: $V_{CC} = +80V$, $V_{BIAS} = +12V$, $V_{IN} = +3.3V$, $C_L = 8\text{ pF}$, Output = 40 V_{PP} at 1 MHz, $T_A = 25^\circ\text{C}$.

Symbol	Parameter	Conditions	LM2406			Units
			Min	Typical	Max	
I_{CC}	Supply Current (Per Channel)	No Output Load		18	30	mA
V_{OUT}	Output Voltage	No Input Signal	45	50	55	V _{DC}
t_R	Rise Time	10%–90%, $f = 1\text{ MHz}$		9		nS
t_F	Fall Time	90%–10%, $f = 1\text{ MHz}$		9		nS
A_V	Voltage Gain		−13	−14.5	−16	V/V
LE	Linearity Error	(Note 4)		8		%
ΔA_V	Gain Matching	(Note 5)		1.0		dB

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.

Note 2: Operating ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may change when the device is not operated under the listed test conditions.

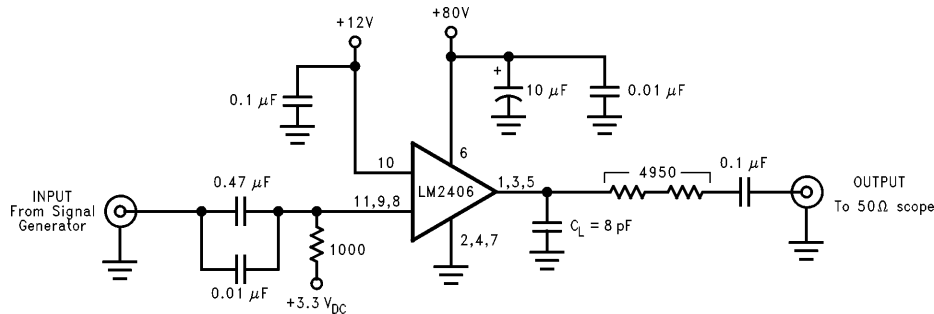
Note 3: All voltages are measured with respect to GND, unless otherwise specified.

Note 4: Linearity Error is defined as the variation in small signal gain from +30V to +70V output with a 100 mV AC, 10 kHz input signal.

Note 5: Calculated value from Voltage Gain test on each channel.

Note 6: Input signal, V_{IN} ; t_r , $t_f < 2\text{ nS}$.

AC Test Circuit



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Note: 8 pF is total load including parasitic capacitance.

FIGURE 2. Test Circuit (One Channel)

Figure 2 shows a typical test circuit for evaluation of the LM2406. This circuit is designed to allow testing of the LM2406 in a 50 Ω environment, such as a pulse generator, oscilloscope or network analyzer. The two series resistors at the output form a 100:1 voltage divider when connected to a 50 Ω load.

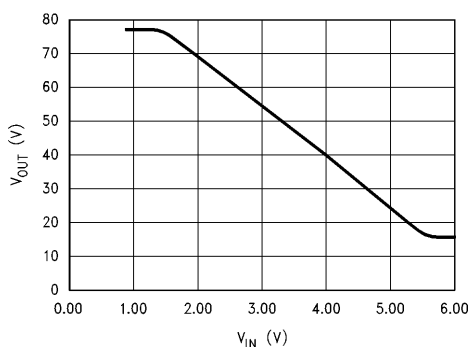


FIGURE 3. V_{OUT} vs V_{IN}

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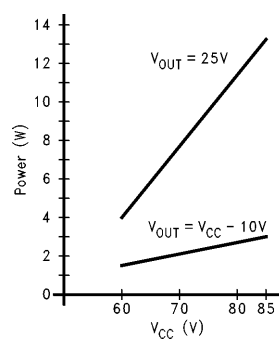


FIGURE 4. Power Dissipation vs V_{CC}

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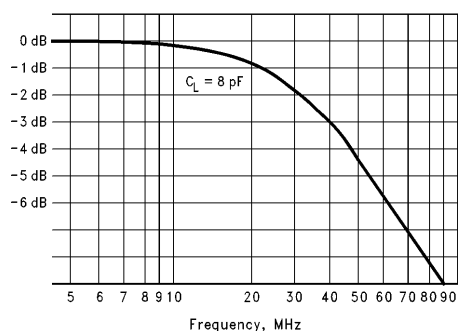


FIGURE 5. Large Signal Frequency Response

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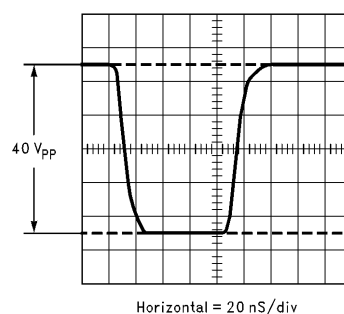


FIGURE 6. Pulse Response

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Theory of Operation

The LM2406 is a high voltage monolithic triple CRT driver suitable for VGA and SVGA display applications. The LM2406 features +80V operation and low power dissipation. The part is housed in the industry standard 11-lead TO-220 molded plastic power package.

The circuit diagram of the LM2406 is shown in *Figure 1*. Q1 and R2 provide a conversion of input voltage to current, while Q2 acts as a common base or cascode amplifier stage to drive the load resistor R1. Emitter followers Q3 and Q4 isolate the impedance of R1 from the capacitance of the CRT cathode, and make the circuit relative insensitive to load capacitance. The gain of this circuit is $-R1/(R2||R3)$ and is fixed at -14.5 . The bandwidth of the circuit is set by the collector time constant formed by the resistor R1 and

associated capacitance of Q2, Q3, Q4 and stray layout capacitance. Transistor Q6 and resistors R7 and R8 provide biasing to the output emitter-follower stage to reduce crossover distortion at low signal levels, while R3 provides a DC bias offset to match the output level characteristics of the preamplifier stage.

Figure 2 shows a typical test circuit for evaluation of the LM2406. This circuit is designed to allow testing of the LM2406 in a 50Ω environment, such as a pulse generator and a scope, or a network analyzer. In this test circuit, two resistors in series totaling $4.95\text{ k}\Omega$ form a 100:1 wideband low capacitance probe when connected to a 50Ω cable and load. The input signal from the generator is AC coupled to the base of Q5, while a DC bias of +12V is applied to the base of Q2 (See *Figure 2*).

Application Hints

POWER SUPPLY BYPASS

Since the LM2406 is a wide bandwidth amplifier, proper power supply bypassing is critical for optimum performance. Improper power supply bypassing can result in large overshoot, ringing and oscillation. A 0.01 μF capacitor should be connected as close to the supply pin, $V+$, as is practical (preferably less than $\frac{1}{4}$ " from the supply pin). Additionally, a 10 μF –100 μF electrolytic capacitor should be connected from the supply pin to ground. The electrolytic capacitor should also be placed reasonably close to the LM2406's supply pin. A 0.1 μF capacitor should be connected from the bias pin to ground, as close as is practical to the LM2406.

The LM2406 is short circuit proof to momentary shorts to ground (< 1 sec.).

ARC PROTECTION

During normal CRT operation, internal arcing may occasionally occur. Spark gaps of 200V–300V at the cathodes will limit the maximum voltage, but to a value that is much higher than allowable on the LM2406. This fast, high voltage, high energy pulse can damage the LM2406 output stage. The addition of clamp diodes D1 and D2 (as shown in Figure 7) will help clamp the voltage at the output of the LM2406 to a safe level. The clamp diodes should have a fast transient response, high peak current rating, low series impedance and low shunt capacitance. FDH400 or equivalent diodes are recommended. Resistor R2 in Figure 7 limits the arcover current while R1 limits the current into the LM2406 and reduces the power dissipation of the output transistors when the output is stressed beyond the supply voltage. Having large value resistors for R1 and R2 would be desirable, but this has the effect of increasing rise and fall times.

IMPROVING RISE AND FALL TIMES

Because of an emitter follower output stage, the rise and fall times of the LM2406 are relatively insensitive to capacitive loading. However, the series resistors R1 and R2 (see Figure 7) will increase the rise and fall times when driving the CRT's cathode which appears as a capacitive load. The capacitance at the cathode typically ranges from 8 pF–12 pF.

To improve the rise and fall times at the cathode, a small inductor is often used in series with the output of the amplifier. The inductor L_p in Figure 7 peaks the amplifier's frequency response at the cathode, thus improving rise and fall times. The inductor value is empirically determined and is dependent on the load. An inductor value of 0.1 μH is a good starting value. Note that peaking the amplifier's frequency response will increase the overshoot.

THERMAL CONSIDERATIONS

Power supply current increases as the input signal increases and consequently power dissipation also increases.

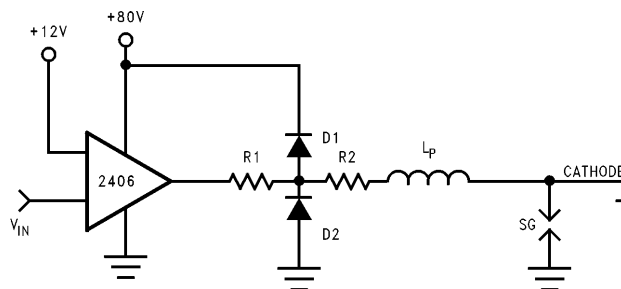
The LM2406 cannot be used without heat sinking. Typical "average" power dissipation with the device output voltage at one half the supply voltage is 2.4W per channel for a total dissipation of 7.2W package dissipation. Under white screen conditions, i.e., 25V output, dissipation increases to 3.5W per channel or 10.5W total. The LM2406 case temperature must be maintained below 100°C. If the maximum expected ambient temperature is 50°C, then a maximum heat sink thermal resistance can be calculated:

$$R_{th} = \frac{100^\circ\text{C} - 50^\circ\text{C}}{10.5\text{W}} = 4.76^\circ\text{C/W.}$$

This example assumes a typical CRT capacitive load and is without a resistive load.

TYPICAL APPLICATION

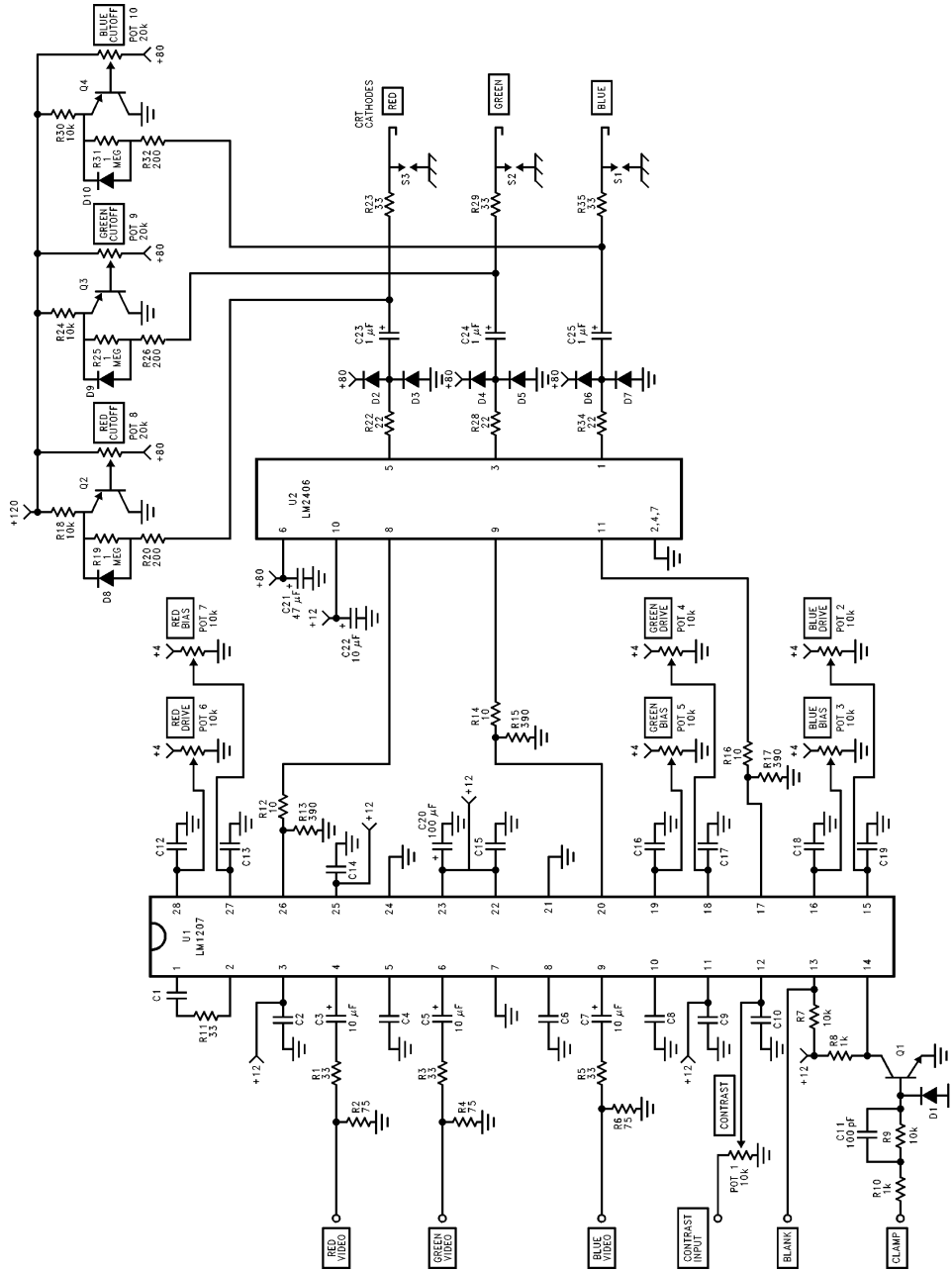
A typical application of the LM2406 is shown in Figure 8. Used in conjunction with an LM1207, a complete video channel from monitor input to CRT cathode can be achieved. Performance is satisfactory for all applications up to 1024 x 768 non-interlaced.



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FIGURE 7. One Section of the LM2406 with Arc Protection and Peaking Inductor L_p

Typical Applications

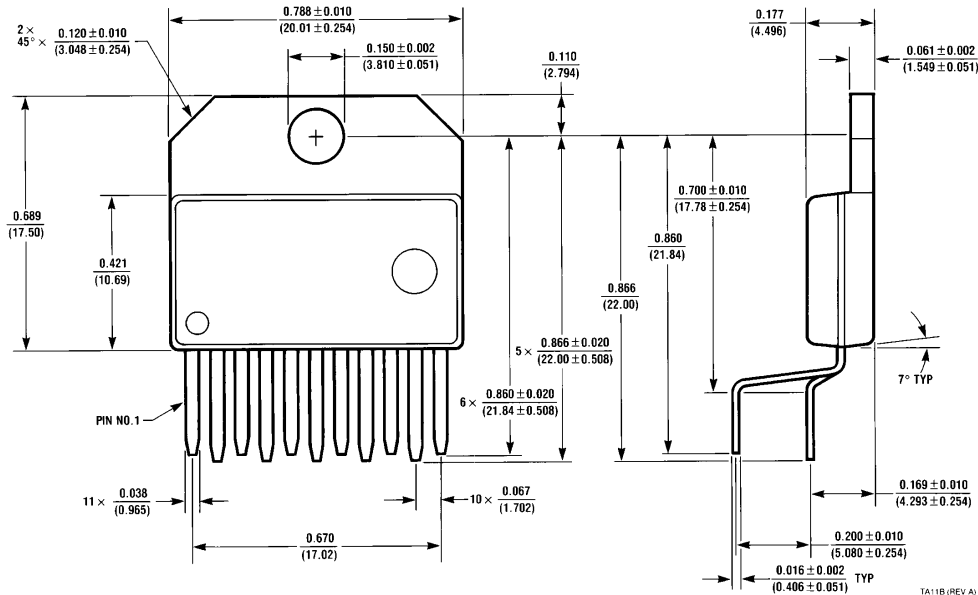


Note: Unmarked capacitors 0.1 μ F

FIGURE 8. Typical Application LM1207 – LM2406

LM2406 Monolithic 9 ns Triple CRT Driver

Physical Dimensions inches (millimeters)



11-Lead Molded TO-220
Order Number LM2406
NS Package Number TA11B

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