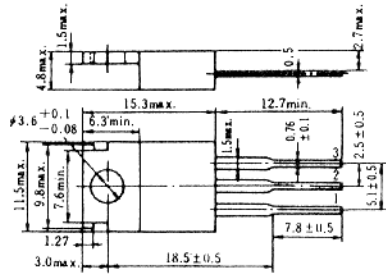


## 2SD1137

SILICON NPN TRIPLE DIFFUSED  
 LOW FREQUENCY POWER AMPLIFIER  
 TV VERTICAL DEFLECTION OUTPUT  
 COMPLEMENTARY PAIR WITH 2SB860



1. Base
2. Collector  
(Flange)
3. Emitter  
(Dimensions in mm)

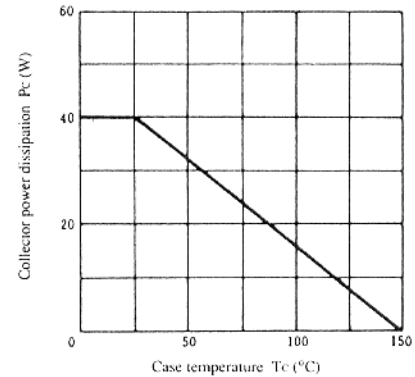
(JEDEC TO-220 AB)

### ■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Item	Symbol	2SD1137	Unit
Collector to base voltage	V <sub>CB0</sub>	100	V
Collector to emitter voltage	V <sub>CE0</sub>	100	V
Emitter to base voltage	V <sub>EB0</sub>	4	V
Collector current	I <sub>C</sub>	4	A
Collector peak current	i <sub>C(peak)</sub>	5	A
Collector power dissipation	P <sub>C</sub>	1.8	W
	P <sub>C</sub> *	40	W
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-45 to +150	°C

\* Value at T<sub>C</sub> = 25°C.

### MAXIMUM COLLECTOR DISSIPATION CURVE



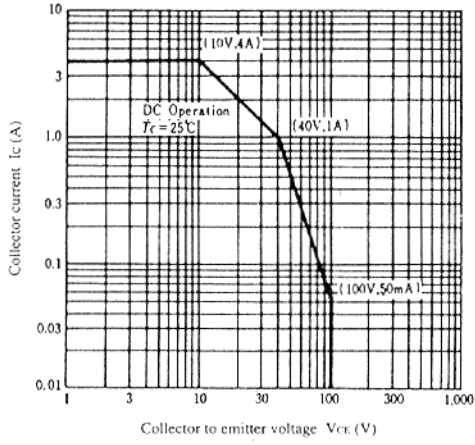
### ■ ELECTRICAL CHARACTERISTICS (Ta=25°C)

Item	Symbol	Test Condition	min.	typ.	max.	Unit	
Collector to emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 10mA, R <sub>BE</sub> = ∞	100	—	—	V	
Emitter to base breakdown voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 1mA, I <sub>C</sub> = 0	4	—	—	V	
Collector cutoff current	I <sub>CEO</sub>	V <sub>CE</sub> = 80V, R <sub>BE</sub> = ∞	—	—	100	μA	
Emitter cutoff current	I <sub>EBO</sub>	V <sub>EB</sub> = 3.5V, I <sub>C</sub> = 0	—	—	50	μA	
DC current transfer ratio	h <sub>FE</sub>	V <sub>CE</sub> = 4V	I <sub>C</sub> = 0.5A*	50	—	250	
			I <sub>C</sub> = 50mA	25	—	350	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.1A	—	—	1.0	V	

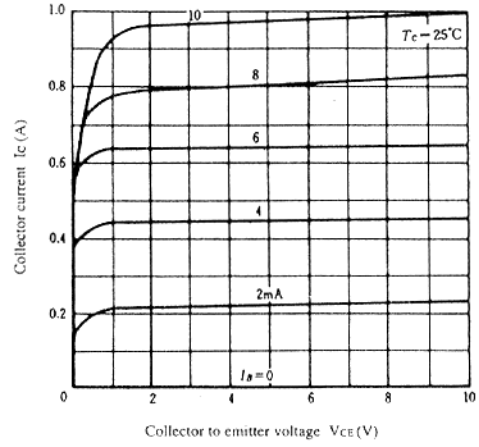
\* Pulse Test.

## 2SD1137

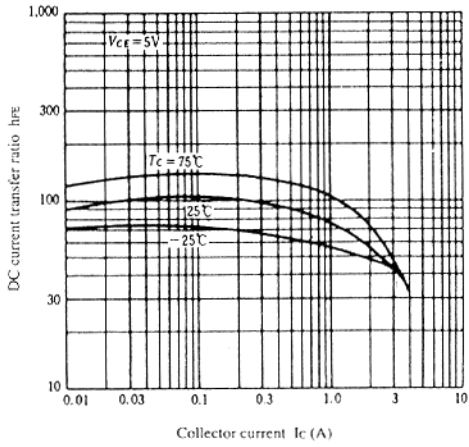
AREA OF SAFE OPERATION



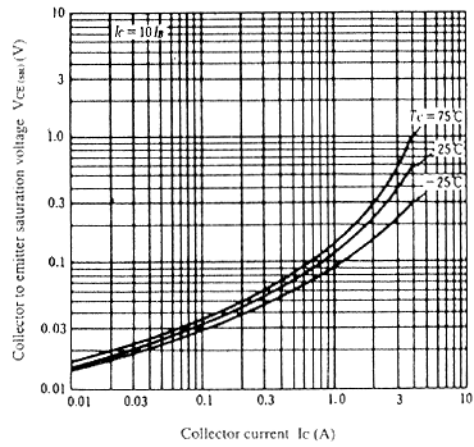
TYPICAL OUTPUT CHARACTERISTICS



DC CURRENT TRANSFER RATIO VS. COLLECTOR CURRENT



COLLECTOR TO EMITTER SATURATION VOLTAGE VS. COLLECTOR CURRENT



BASE TO EMITTER SATURATION VOLTAGE VS. COLLECTOR CURRENT

