DATA SHEET

Part No.	AN17883A
Package Code No.	QFP044-P-1010F

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AN17883A

Headphone amplifier IC with center amplifier and tuner amplifier

■ Applications

- ICs for low frequency amplifier

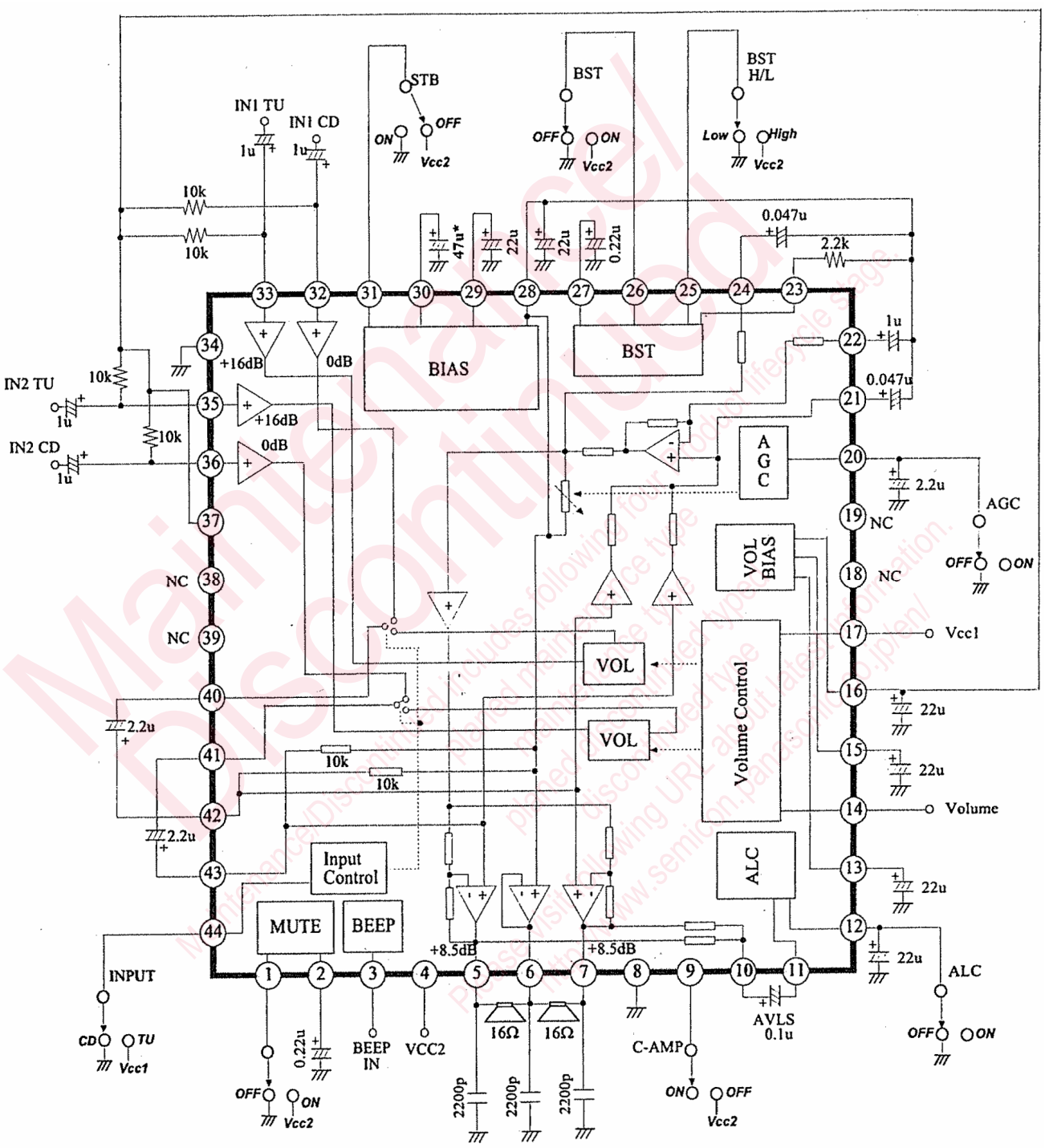
■ Package

- QFP 44-pin plastic package

■ Type

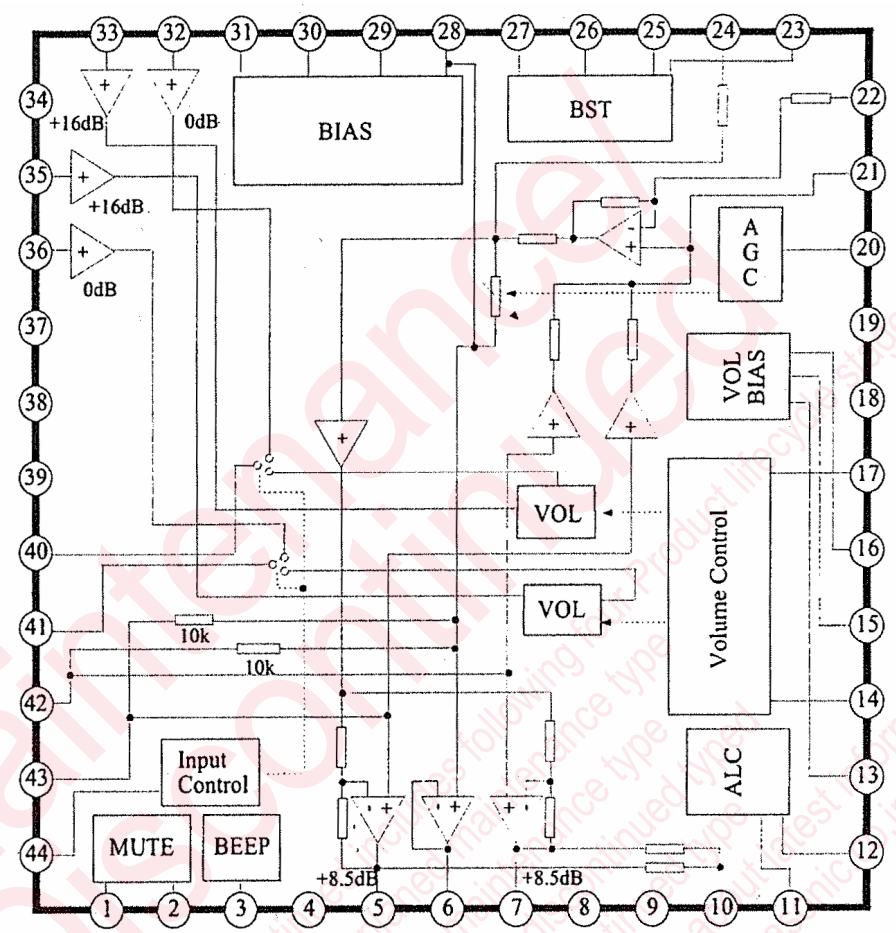
- Silicon monolithic bipolar IC

■ Application Circuit Example



Note) This value is used to have a better V_{CC} plug-in pop noise performance.

■ Block Diagram



■ Pin Descriptions

Pin No.	Description	Pin No.	Description
1	Mute switch	23	BST adjust
2	Mute timing control	24	Boost amplifier output
3	Beep sound input	25	Boost H/L switch
4	V_{CC2} supply	26	Boost control
5	Channel 2 power output	27	Boost timing control
6	Center amplifier output	28	Bias output
7	Channel 1 power output	29	Bias input
8	Power GND	30	Ripple filter
9	Center amplifier switch	31	Standby switch
10	Mixed output of power amplifier	32	Channel 1 CD input
11	ALC detection pin	33	Channel 1 tuner input
12	ALC switch	34	GND
13	VOLRF	35	Channel 2 tuner input
14	Volume control	36	Channel 2 CD input
15	VOLRFX	37	VOLVREFX
16	VOLVREF	38	N.C.
17	V_{CC1} supply	39	N.C.
18	N.C.	40	Channel 1 volume output
19	N.C.	41	Channel 2 volume output
20	AGC switch	42	Channel 1 power input
21	Channel 1, channel 2 mixed output	43	Channel 2 power input
22	Boost amplifier NF	44	Input switch

■ Absolute Maximum Ratings

A No.	Parameter	Symbol	Rating	Unit	Note
1	Supply voltage	V_{CC}	4.6	V	*1
2	Supply current	I_{CC}	200	mA	
3	Power dissipation	P_D	0.654	W	
4	Storage temperature	T_{stg}	–55 to +150	°C	*2
5	Operating ambient temperature	T_{opr}	–25 to +75	°C	*2
6	Operating ambient atmospheric pressure	P_{opr}	$1.013 \times 10^5 \pm 0.61 \times 10^5$	Pa	
7	Operating constant gravity	G_{opr}	9 810	m/S^2	
8	Operating shock	S_{opr}	4 900	m/S^2	

Note) *1: No signal input.

*2: Expect for the storage temperature and operating ambient temperature, all ratings are for Ta = 25°C.

■ Operating Supply Voltage Range

Parameter	Symbol	Range	Unit	Note
Supply voltage range	V_{CC1}	1.8 to 4.5	V	*1
	V_{CC2}	1.8 to 4.5		

Note) *1: V_{CC1} and V_{CC2} must be connected together.

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