

# AN6914UBS

## Industrial Surface Mounting Package Comparator

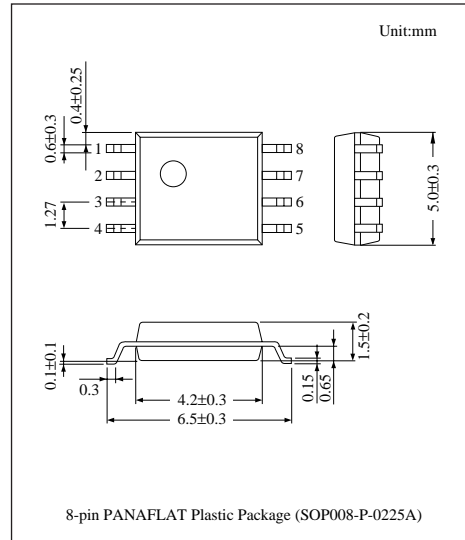
### ■ Overview

The AN6914UBS is available for automotive and industrial equipments which require high reliability through an enhanced screening process.

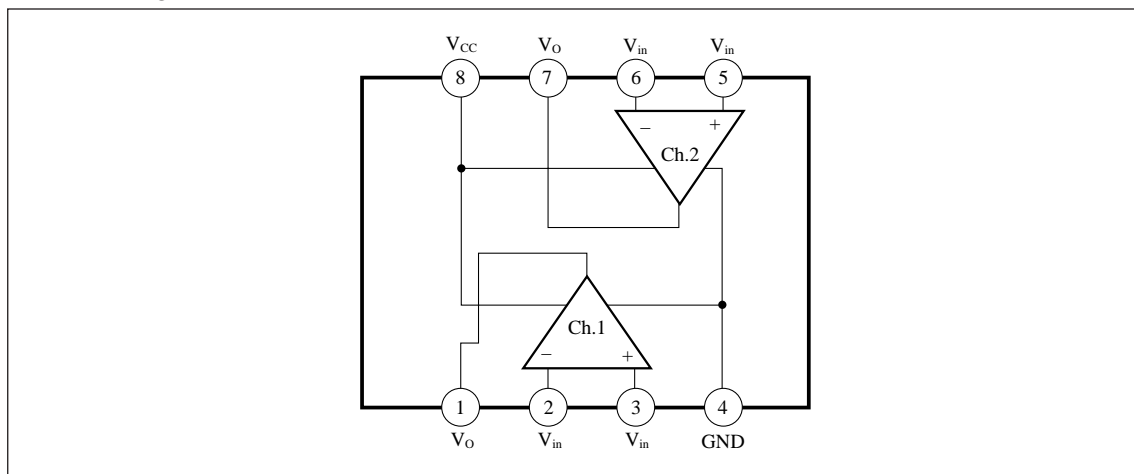
### ■ Features

- Temperature cycle test conducted for all the products
- All the products inspected at  $T_a=25^{\circ}\text{C}$  and  $=100^{\circ}\text{C}$  to assure the characteristics
- Operating temperature range:  $-40^{\circ}\text{C}$  to  $+100^{\circ}\text{C}$
- Either of the following types of packing is selectable:
  - Embossed taping.....AN6914UBS-(E1)
  - Magazine.....AN6914UBS
- Quantity of packed ICs
 

AN6914UBS-(E1)	2000 pcs./reel
AN6914UBS	90 pcs./magazine



### ■ Block Diagram



## ■ Absolute Maximum Ratings (Ta=25°)

Parameter	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	36	V
Common-mode input voltage	V <sub>ICM</sub>	- 0.3 to +36 *1	V
Differential input voltage	V <sub>ID</sub>	36 *2	V
Output applied voltage	V <sub>1</sub> , V <sub>7</sub>	24	V
Power dissipation	P <sub>D</sub>	360	mW
Operating ambient temperature	T <sub>opr</sub>	-40 to +100	°C
Storage temperature	T <sub>stg</sub>	-55 to +125	°C

\*1 The common-mode input voltage is applied to the non-inverting input pin and inverting input pin simultaneously.

\*2 Differential input corresponds to the potential difference between the non-inverting input pin and inverting input pin.

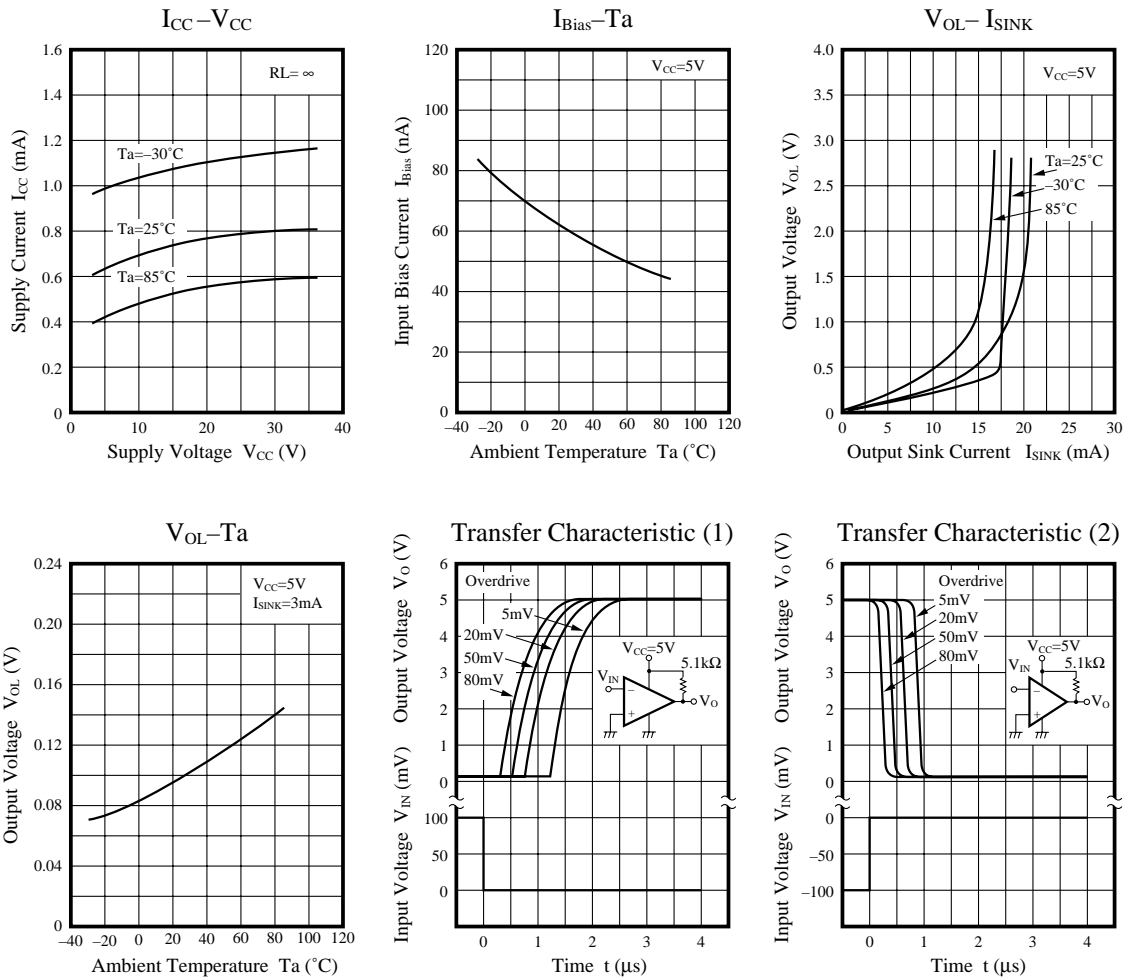
## ■ Electrical Characteristics (V<sub>CC</sub>=5V)

Parameter	Symbol	Condition	min *4	typ *3	max *4	Unit
Input offset voltage	V <sub>I (offset)</sub>		—	1	5	mV
Input offset current	I <sub>IO</sub>		—	—	50	nA
Input bias current	I <sub>Bias</sub>		—	—	250	nA
Voltage gain	G <sub>V</sub>	R <sub>L</sub> =15kΩ	—	200	—	V/mV
Common-mode input voltage width	V <sub>CM</sub>		0	—	V <sub>CC</sub> -1.5	V
Supply current	I <sub>CC</sub>	R <sub>L</sub> =∞	—	0.6	1.5	mA
Response time	t <sub>r</sub>	R <sub>L</sub> =5.1kΩ	—	1.3	—	μs
Output sink current	I <sub>SINK</sub>	V <sub>REF</sub> =0V, V <sub>IN</sub> =1V, V <sub>O</sub> ≤1.5V	10	—	—	mA
Output voltage low level	V <sub>OL</sub>	V <sub>REF</sub> =0V, V <sub>IN</sub> =1V, I <sub>SINK</sub> =3mA	—	0.2	0.4	V
Output pin leak current	I <sub>O (Leak)</sub>	V <sub>REF</sub> =1V, V <sub>IN</sub> =0V, V <sub>O</sub> =5V	—	0.1	—	nA

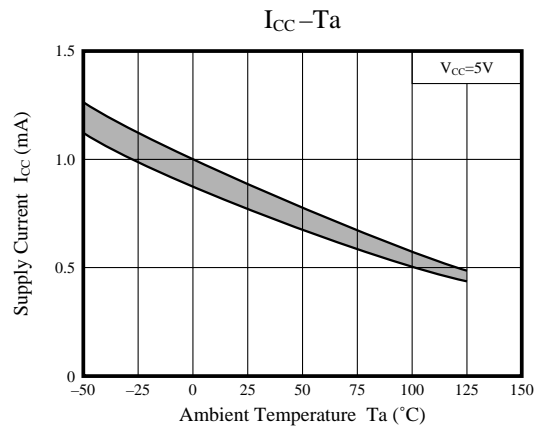
\*3 Typical value: Ta=25°C

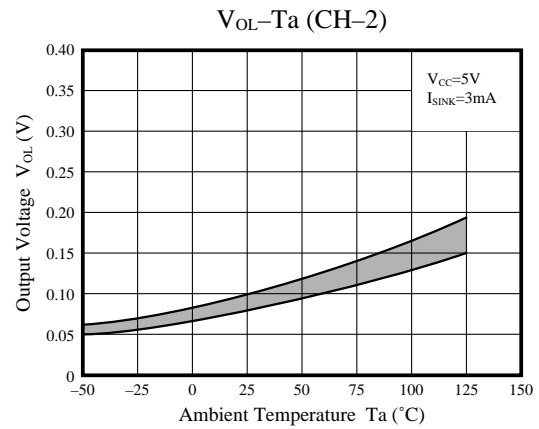
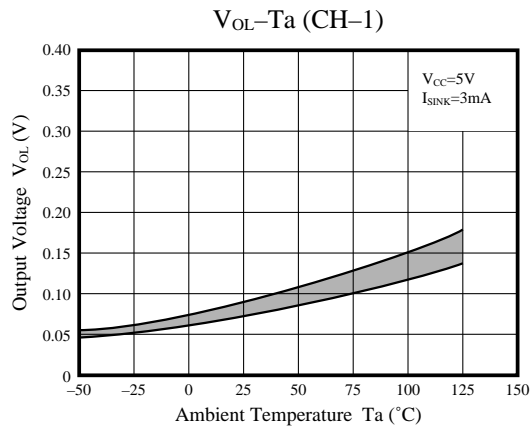
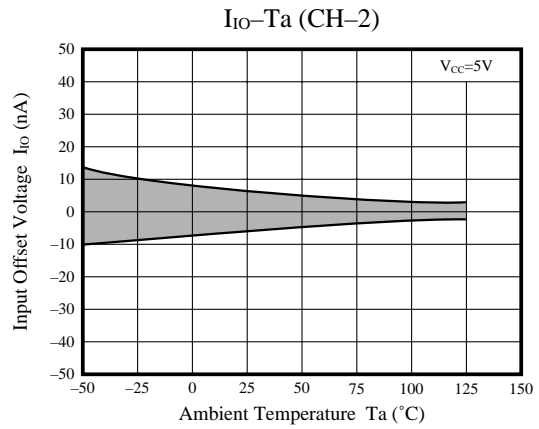
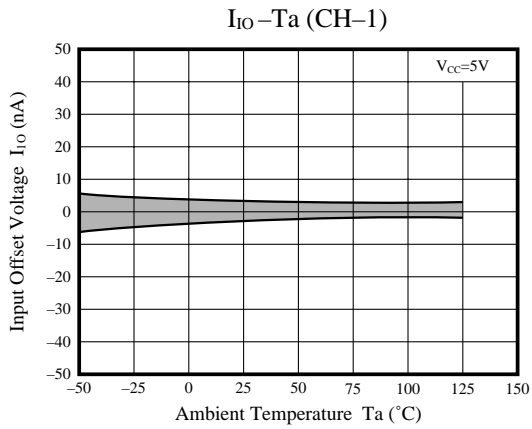
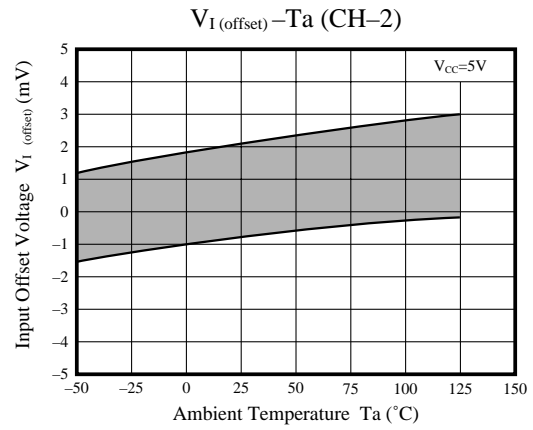
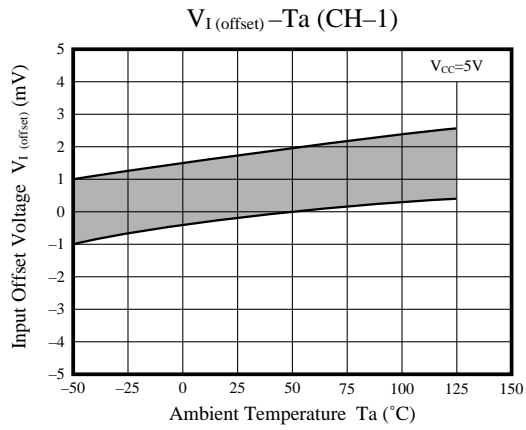
\*4 Maximum and minimum values: All the ICs are inspected at two conditional temperatures of Ta=25°C and Ta=100°C to assure the characteristics.

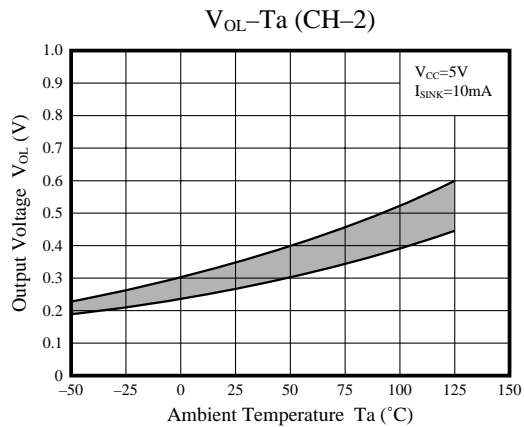
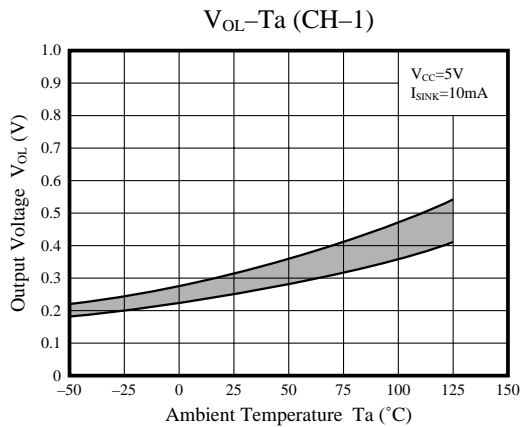
■ Characteristics Curve



■ Characteristics Dispersion Charts







## ■ Reliability of AN6914UBS

### 1. Results of Reliability Test

Test item	Test condition	No. of tested pieces	Total test time	No. of failures
High-temperature operation	$T_a=125^{\circ}\text{C}$ $V_{CC}=12\text{V}$	150	16000	0
Leaving under high temperature	$T_a=125^{\circ}\text{C}$	120	3600	0
Thermal shock	$+150^{\circ}\text{C} \leftrightarrow -65^{\circ}\text{C}$ 500 cycles	160		0
Temperature cycle	$+125 \leftrightarrow -40^{\circ}\text{C}$ 10000 cycles	50		0

### 2. Failure Rate (Refer to JIS-C-5003)

Assuming that the total test time =  $T(\text{No. of test pieces} \times \text{test time})$  and the No. of failures =  $r$ , the failure rate can be calculated by obtaining  $r/T$  and multiplying it by the numerical values commensurate with the reliability level and the number of defective ICs.

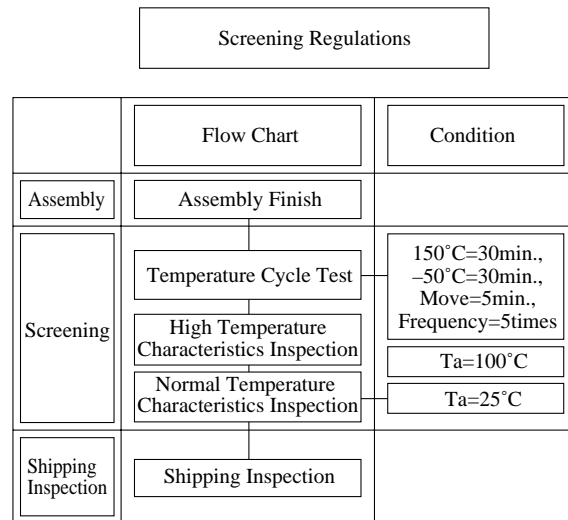
If the failure rate (FR) is calculated from the results of the above high temperature test of the AN6914UBS, assuming that the reliability level is 60%;

$$T=150 \times 16000=2.4 \times 10^6(\text{hours})$$

$$r=0 \text{ comes to } 0.92 \text{ at the level of } 60\%.$$

$$\text{FR}=0.92/2.4 \times 10^6=3.8 \times 10^{-7}=380\text{Fit}$$

Assuming that activation energy is 1.0 eV, FR will be 38 Fit at worst ( $T_a=100^{\circ}\text{C}$ ) because an acceleration factor differs by about 1 digit between at  $125^{\circ}\text{C}$  and  $100^{\circ}\text{C}$ .





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