

DMC505C0

Silicon NPN epitaxial planar type

For low frequency amplification
DMC205C0 in SMini6 type package

■ Features

- High forward current transfer ratio h_{FE} with excellent linearity
- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

■ Basic Part Number

Dual DSC2C01 (Individual)

■ Packaging

DMC505C00R Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	100	V
Collector-emitter voltage (Base open)	V_{CEO}	100	V
Emitter-base voltage (Collector open)	V_{EBO}	15	V
Collector current	I_C	20	mA
Peak collector current	I_{CP}	50	mA
Total power dissipation	P_T	150	mW
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

■ Electrical Characteristics $T_a = 25^\circ\text{C} \pm 3^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	100			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = 1 \text{mA}, I_B = 0$	100			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10 \mu\text{A}, I_C = 0$	15			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 60 \text{V}, I_E = 0$			0.1	μA
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = 60 \text{V}, I_B = 0$			1	μA
Forward current transfer ratio	h_{FE}	$V_{CE} = 10 \text{V}, I_C = 2 \text{mA}$	400		1200	—
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 10 \text{mA}, I_B = 1 \text{mA}$		0.05	0.20	V
Transition frequency	f_T	$V_{CE} = 10 \text{V}, I_C = 2 \text{mA}$		140		MHz

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

■ Package

• Code

SMini6-F3-B

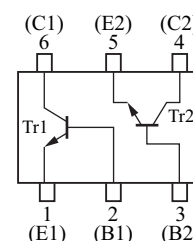
Package dimension clicks here.→

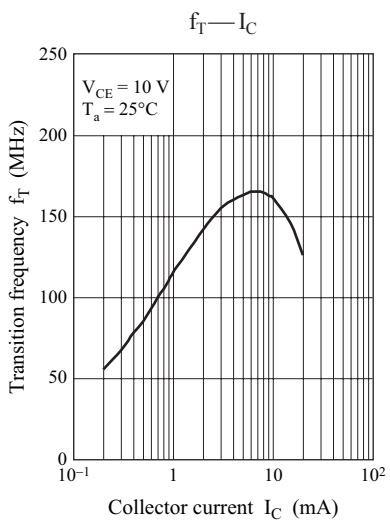
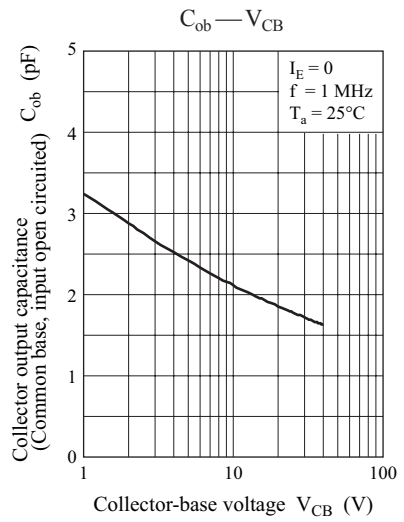
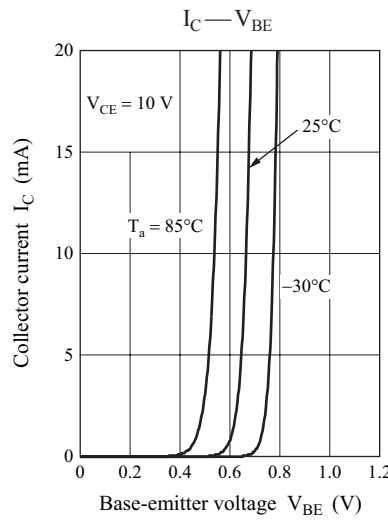
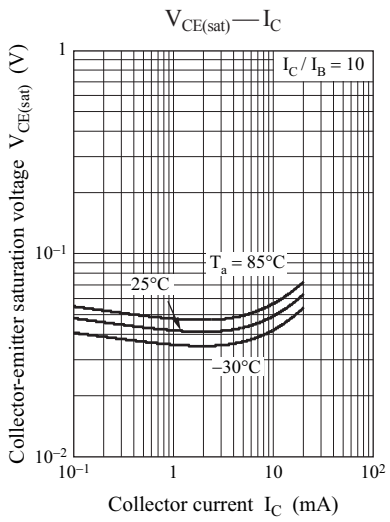
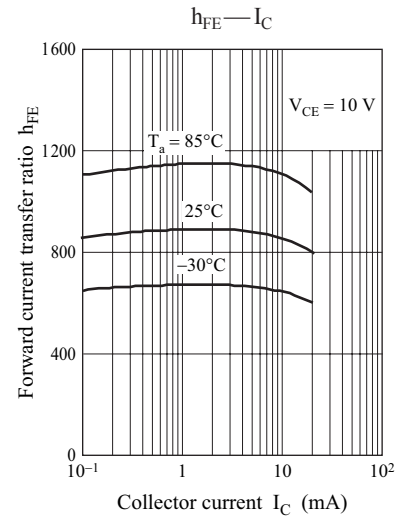
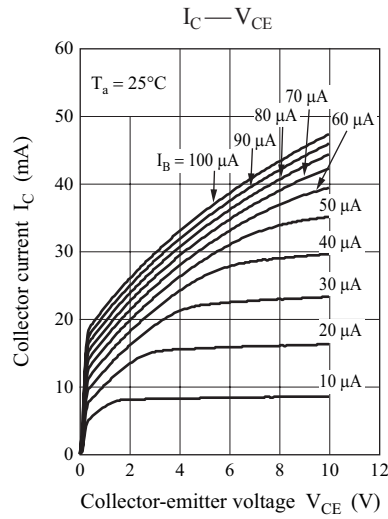
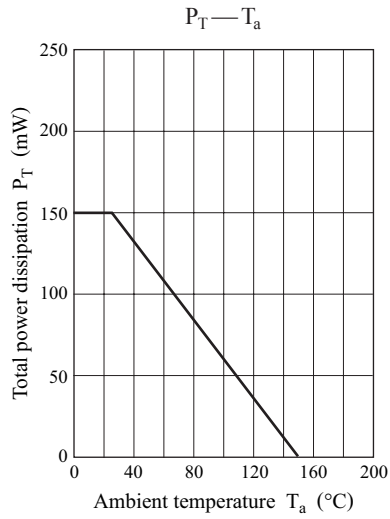
• Pin Name

- | | |
|------------------|--------------------|
| 1: Emitter (Tr1) | 4: Collector (Tr2) |
| 2: Base (Tr1) | 5: Emitter (Tr2) |
| 3: Base (Tr2) | 6: Collector (Tr1) |

■ Marking Symbol: D6

■ Internal Connection





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