

T-77-07-05

H/V PROCESSOR FOR TTL V.D.U

PRELIMINARY DATA

HORIZONTAL SECTION

- SYNCHRONIZATION INPUT : TTL COMPATIBLE, NEGATIVE EDGE TRIGGERED
- SYNCHRONIZATION INDEPENDENT FROM DUTY CYCLE TIME
- OSCILLATOR : FREQUENCY RANGE FROM 15kHz to 100kHz
- HORIZONTAL OUTPUT PULSE SHAPER AND SHIFTER
- PHASE COMPARATOR BETWEEN SYNCHRO AND OSCILLATOR (PLL1)
- PHASE COMPARATOR BETWEEN FLYBACK AND OSCILLATOR (PLL2)
- INTERNAL VOLTAGE REGULATOR
- DC COMPATIBLE CONTROLS FOR PHASE AND FREQUENCY
- DUTY CYCLE : 34% FOR TDA9102B
43% FOR TDA9102C

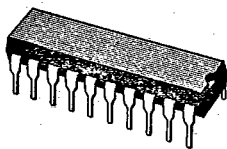
VERTICAL SECTION

- SYNCHRONIZATION INPUT : TTL COMPATIBLE, NEGATIVE EDGE TRIGGERED
- SYNCHRONIZATION INDEPENDENT FROM DUTY CYCLE TIME
- OSCILLATOR : FREQUENCY RANGE FROM 30Hz to 120Hz
- RAMP GENERATOR WITH VARIABLE GAIN STAGE
- VERTICAL RAMP VOLTAGE REFERENCE
- INTERNAL VOLTAGE REGULATOR

DESCRIPTION

The TDA9102B, C is a monolithic integrated circuit for horizontal and vertical sync processing in monochrome and color video displays driven by input TTL compatible signals.

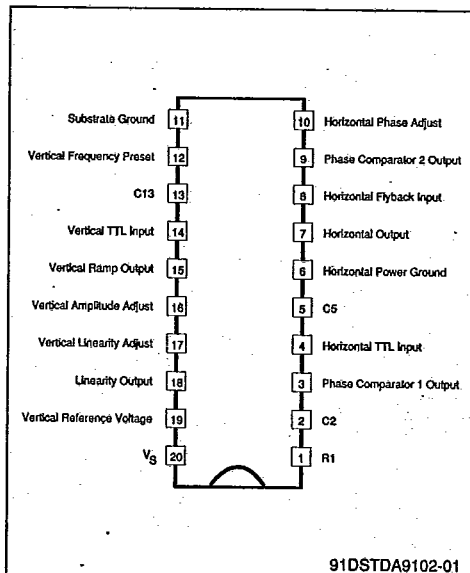
The TDA9102B, C is supplied in a 20 pin dual in line package with pin 11 connected to ground and used for heatsinking.



DIP20
(Plastic package)

ORDER CODES : TDA9102B / TDA9102C

PIN CONNECTIONS

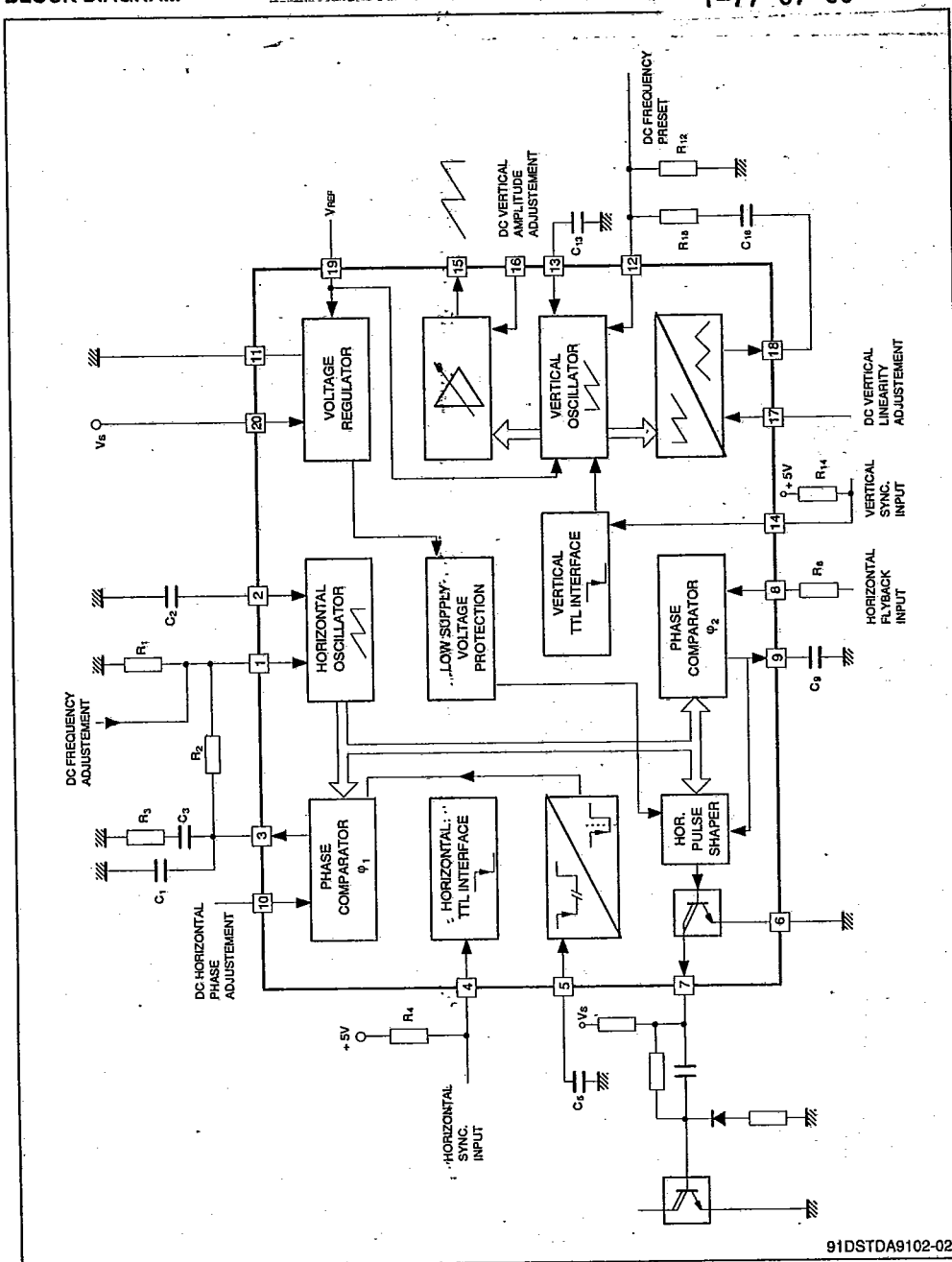


91DSTD9102-01

BLOCK DIAGRAM

S G S-THOMSON

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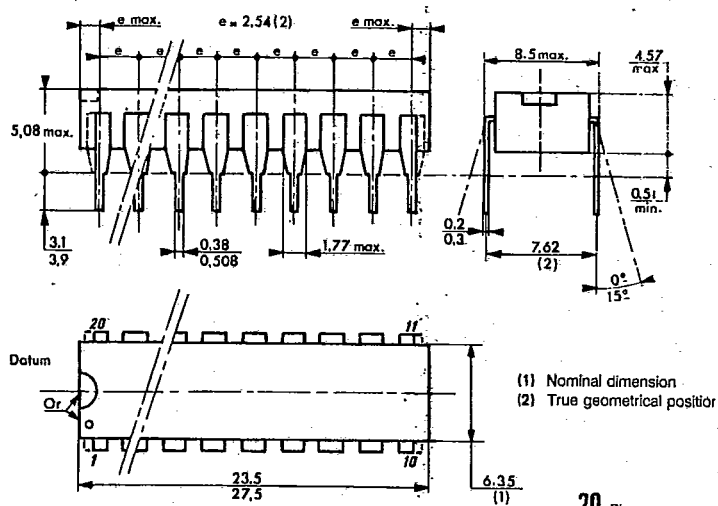


PACKAGE MECHANICAL DATA

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20 PINS - PLASTIC DIP



20 Pins