

MITSUBISHI (DGTL LOGIC)

M54523P**7-UNIT 500mA DARLINGTON TRANSISTOR ARRAY WITH CLAMP DIODE****DESCRIPTION**

The M54523P, 7-channel sink driver, consists of 14 NPN transistors connected to form seven high current gain driver pairs.

FEATURES

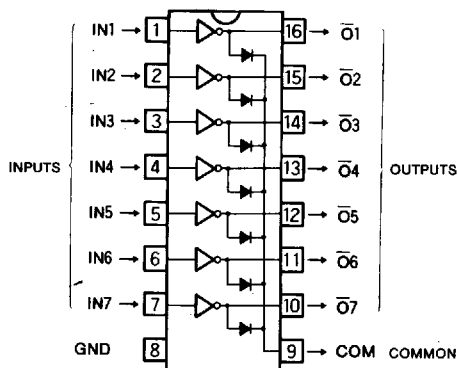
- High output sustaining voltage to 50V
- High output sink current to 500mA
- Integral diodes for transient suppression
- PMOS Compatible input
- Wide operating temperature range ($T_a = -20 \sim +75^\circ\text{C}$)

APPLICATION

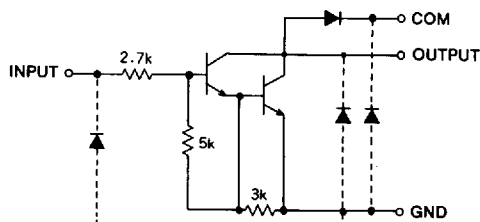
Relay and printer driver, LED or incandescent display digit driver, interfacing for standard MOS/BIPOLAR logics.

FUNCTION

The M54523P is comprised of seven NPN darlington driver pairs with $2.7\text{k}\Omega$ series input resistors. Between pin 9 and each output, there are integral diodes for inductive load transient suppression. All emitters and the substrate are connected together to pin 8. The outputs are capable of sinking 500mA and will withstand 50V in the OFF state.

PIN CONFIGURATION (TOP VIEW)

Outline 16P4

CIRCUIT SCHEMATIC

The diodes shown by broken line are parasite diodes and must not be used

Unit : Ω **ABSOLUTE MAXIMUM RATINGS** ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CEO}	Output sustaining voltage	Transistor OFF	$-0.5 \sim +50$	V
I_C	Collector current per channel	Transistor ON	500	mA
V_I	Input voltage		$-0.5 \sim +30$	V
I_F	Clamp diode forward current		500	mA
V_R	Clamp diode reverse voltage		$-0.5 \sim +50$	V
P_d	Power dissipation	$T_a = 25^\circ\text{C}$	1.47	W
T_{opr}	Operating temperature		$-20 \sim +75$	$^\circ\text{C}$
T_{stg}	Storage temperature		$-55 \sim +125$	$^\circ\text{C}$

7-UNIT 500mA DARLINGTON TRANSISTOR ARRAY WITH CLAMP DIODE

RECOMMENDED OPERATIONAL CONDITIONS ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

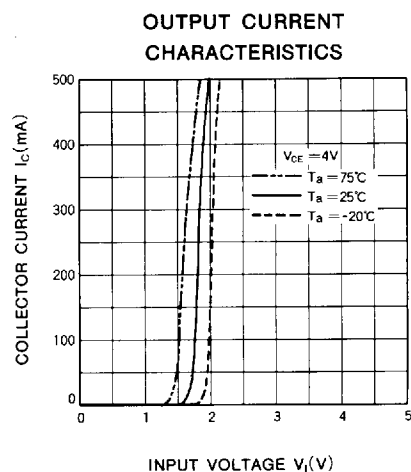
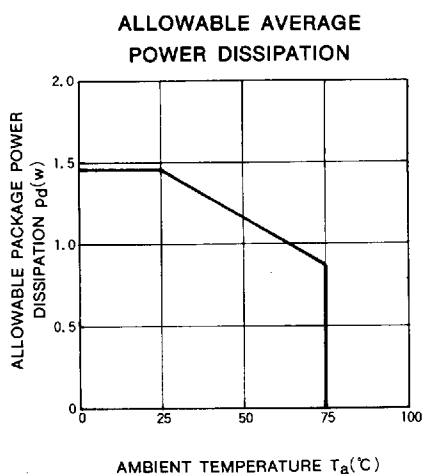
Symbol	Parameter		Limits			Unit
			Min	Typ	Max	
V_O	Output voltage		0		50	V
I_C	Collector current per channel	Percent duty cycle less than 8%	0		400	mA
		Percent duty cycle less than 30%	0		200	
V_{IH}	"H" Input voltage	$I_C = 400\text{mA}$	3.85		25	V
		$I_C = 100\text{mA}$	3.4		25	
V_{IL}	"L" Input voltage		0		0.6	V

ELECTRICAL CHARACTERISTICS ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ*	Max	
$I_{O(\text{leak})}$	Output leakage current	$V_{CE} = 50\text{V}$			100	μA
$V_{CE(\text{sat})}$	Output saturation voltage	$V_I = 3.85\text{V}$, $I_C = 400\text{mA}$		1.3	2.4	V
		$V_I = 3.85\text{V}$, $I_C = 200\text{mA}$		0.95	1.6	
I_I	Input current	$V_I = 3.85\text{V}$		0.95	1.8	mA
		$V_I = 25\text{V}$		9	18	
V_F	Clamp diode forward voltage	$I_F = 400\text{mA}$		1.5	2.4	V
I_R	Clamp diode leakage voltage	$V_R = 50\text{V}$			100	μA
h_{FE}	DC forward current gain	$V_{CE} = 4\text{V}$, $I_C = 350\text{mA}$, $T_a = 25^\circ\text{C}$	1000	2500		—

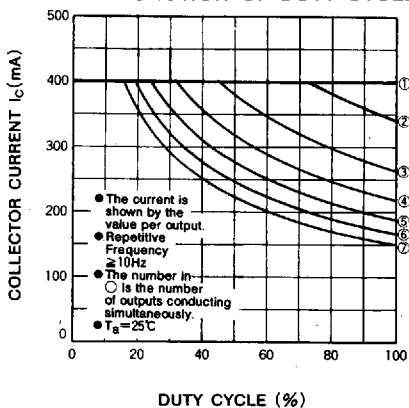
* : Typical values are at $T_a = 25^\circ\text{C}$.

TYPICAL CHARACTERISTICS

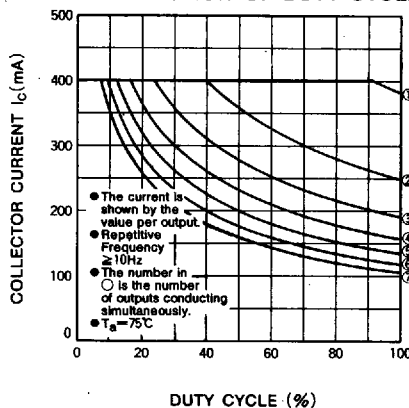


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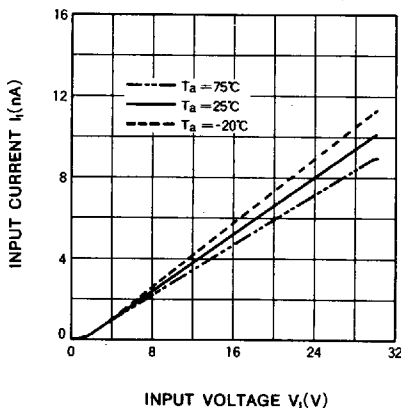
ALLOWABLE COLLECTOR CURRENT AS A FUNCTION OF DUTY CYCLE



ALLOWABLE COLLECTOR CURRENT AS A FUNCTION OF DUTY CYCLE



INPUT CHARACTERISTICS



DC CURRENT GAIN CHARACTERISTICS

