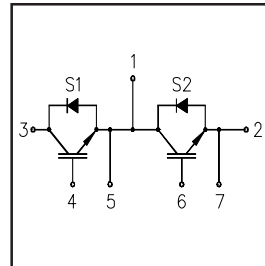


Features

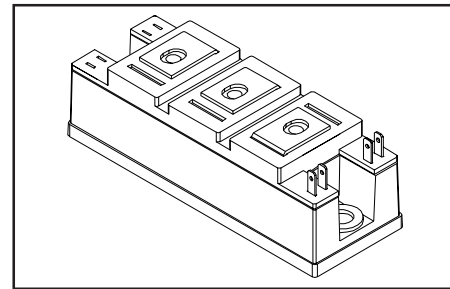
- Generation 4 IGBT technology
- UltraFast: Optimized for high operating frequencies 8-40 kHz in hard switching, >200 kHz in resonant mode
- Very low conduction and switching losses
- HEXFRED™ antiparallel diodes with ultra- soft recovery
- Industry standard package
- UL approved



$V_{CES} = 600V$
$V_{CE(on) typ.} = 1.7V$
@ $V_{GE} = 15V, I_C = 150A$

Benefits

- Increased operating efficiency
- Direct mounting to heatsink
- Performance optimized for power conversion: UPS, SMPS, Welding
- Lower EMI, requires less snubbing



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	600	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	150	A
I_{CM}	Pulsed Collector Current ^①	300	
I_{LM}	Peak Switching Current ^②	300	
I_{FM}	Peak Diode Forward Current	300	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
V_{ISOL}	RMS Isolation Voltage, Any Terminal To Case, $t = 1 \text{ min}$	2500	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	440	W
$P_D @ T_C = 85^\circ C$	Maximum Power Dissipation	230	
T_J	Operating Junction Temperature Range	-40 to +150	$^\circ C$
T_{STG}	Storage Temperature Range	-40 to +125	

Thermal / Mechanical Characteristics

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case - IGBT	—	0.28	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case - Diode	—	0.35	
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink - Module	0.1	—	
	Mounting Torque, Case-to-Heatsink ^③	—	6.0	N·m
	Mounting Torque, Case-to-Terminal 1, 2 & 3 ^④	—	5.0	
	Weight of Module	200	—	g

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)CES}	Collector-to-Emitter Breakdown Voltage	600	—	—	V	V _{GE} = 0V, I _C = 1mA
V _{CE(on)}	Collector-to-Emitter Voltage	—	1.7	2.3		V _{GE} = 15V, I _C = 150A
		—	1.7	—		V _{GE} = 15V, I _C = 150A, T _J = 125°C
V _{GE(th)}	Gate Threshold Voltage	3.0	—	6.0		I _C = 750μA
ΔV _{GE(th)/ΔT_J}	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/°C	V _{CE} = V _{GE} , I _C = 750μA
g _{fe}	Forward Transconductance ④	—	152	—	S	V _{CE} = 25V, I _C = 150A
I _{CES}	Collector-to-Emitter Leaking Current	—	—	1.0	mA	V _{GE} = 0V, V _{CE} = 600V
		—	—	10		V _{GE} = 0V, V _{CE} = 600V, T _J = 125°C
V _{FM}	Diode Forward Voltage - Maximum	—	3.3	—	V	I _F = 150A, V _{GE} = 0V
		—	3.2	—		I _F = 150A, V _{GE} = 0V, T _J = 125°C
I _{GES}	Gate-to-Emitter Leakage Current	—	—	250	nA	V _{GE} = ±20V

Dynamic Characteristics - T_J = 125°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q _g	Total Gate Charge (turn-on)	—	624	937	nC	V _{CC} = 400V
Q _{ge}	Gate - Emitter Charge (turn-on)	—	87	130		I _C = 94A
Q _{gc}	Gate - Collector Charge (turn-on)	—	212	317		T _J = 25°C
t _{d(on)}	Turn-On Delay Time	—	241	—	ns	R _{G1} = 27Ω, R _{G2} = 0Ω,
t _r	Rise Time	—	145	—		I _C = 150A
t _{d(off)}	Turn-Off Delay Time	—	336	—		V _{CC} = 360V
t _f	Fall Time	—	227	—		V _{GE} = ±15V
E _{on}	Turn-On Switching Energy	—	6.0	—	mJ	
E _{off (1)}	Turn-Off Switching Energy	—	12	—		
E _{ts (1)}	Total Switching Energy	—	19	33		
C _{ies}	Input Capacitance	—	14000	—	pF	V _{GE} = 0V
C _{oes}	Output Capacitance	—	860	—		V _{CC} = 30V
C _{res}	Reverse Transfer Capacitance	—	180	—		f = 1 MHz
t _{rr}	Diode Reverse Recovery Time	—	172	—	ns	I _C = 150A
I _{rr}	Diode Peak Reverse Current	—	113	—		A
Q _{rr}	Diode Recovery Charge	—	9696	—	nC	R _{G2} = 0Ω
di _(rec) M _{/dt}	Diode Peak Rate of Fall of Recovery During t _b	—	2000	—	A/μs	V _{CC} = 360V di/dt = 1300A/μs

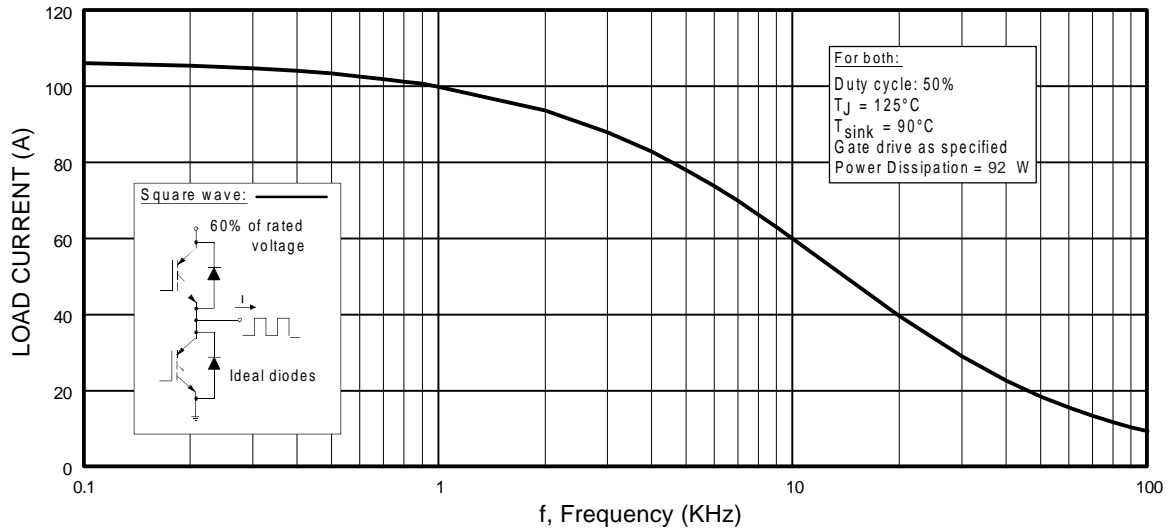


Fig. 1 - Typical Load Current vs. Frequency
 (Load Current = I_{RMS} of fundamental)

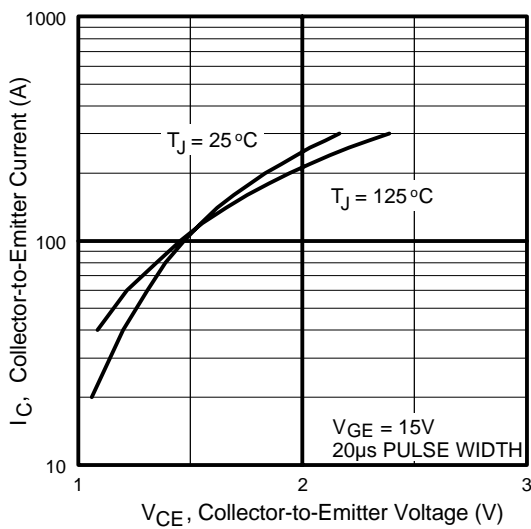


Fig. 2 - Typical Output Characteristics

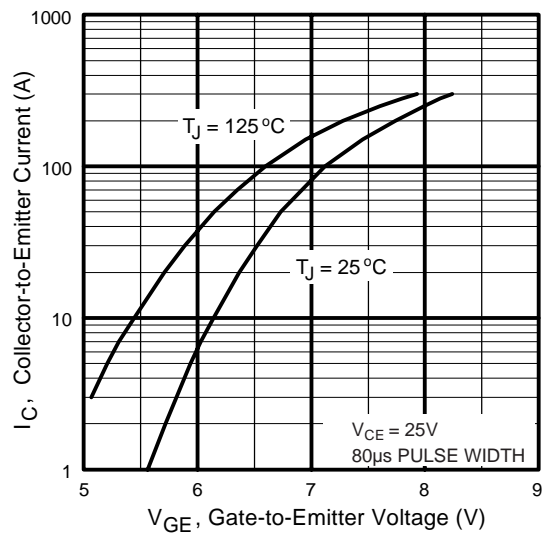


Fig. 3 - Typical Transfer Characteristics

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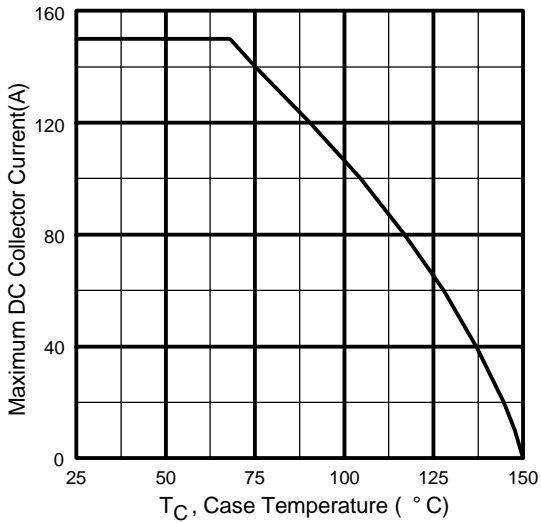


Fig. 4 - Maximum Collector Current vs. Case Temperature

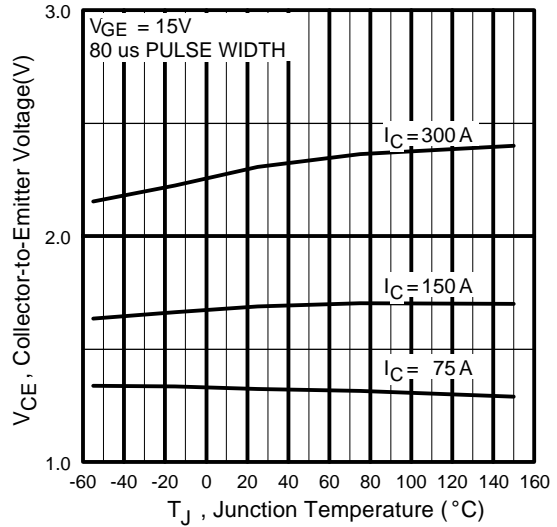


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

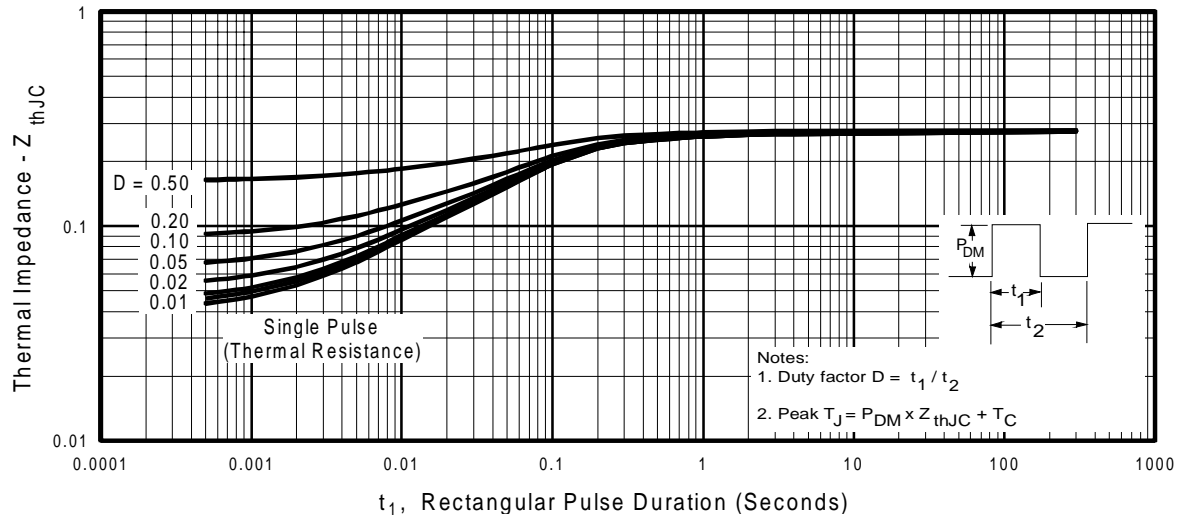


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

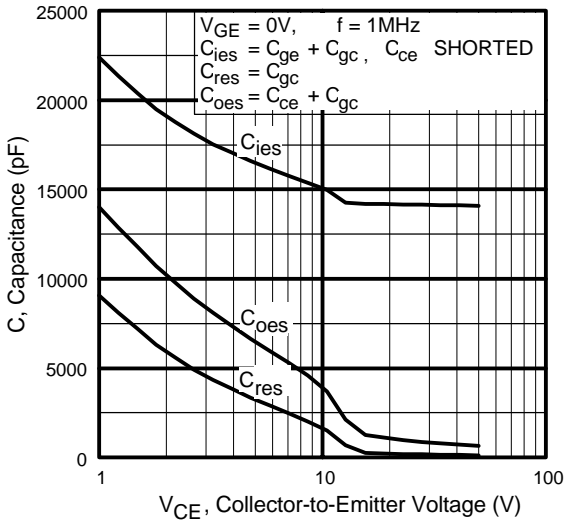


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

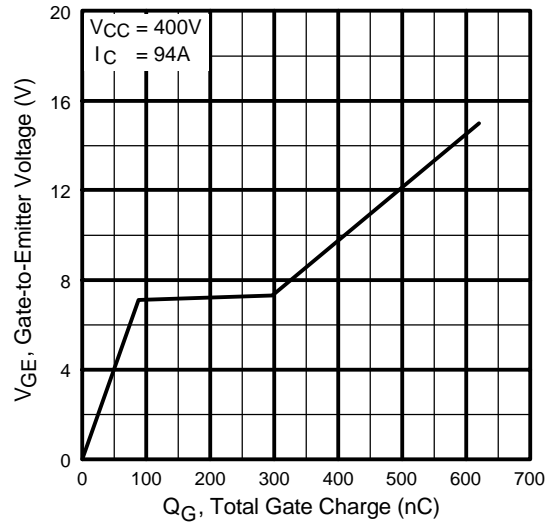


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

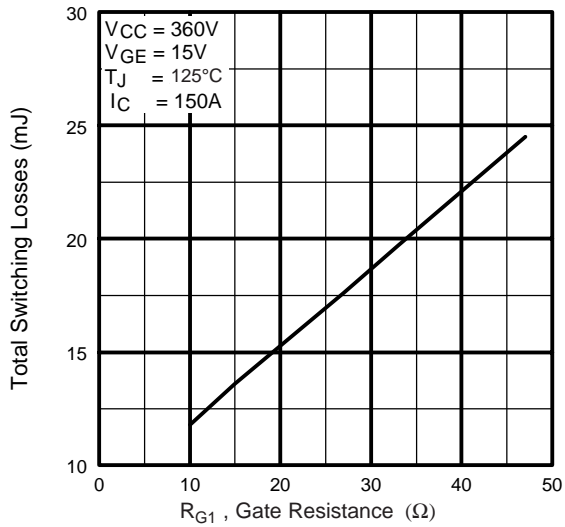


Fig. 9 - Typical Switching Losses vs. Gate Resistance

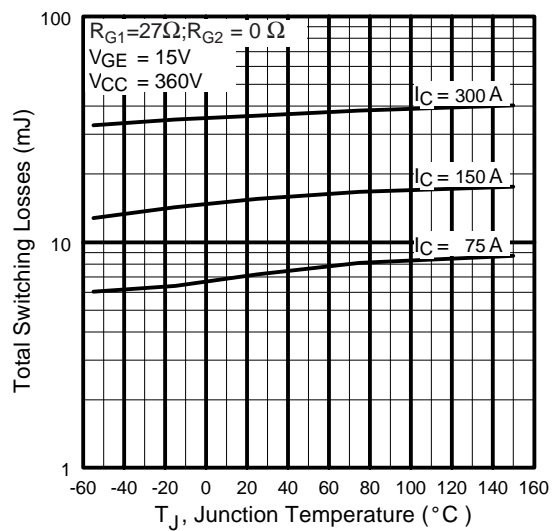


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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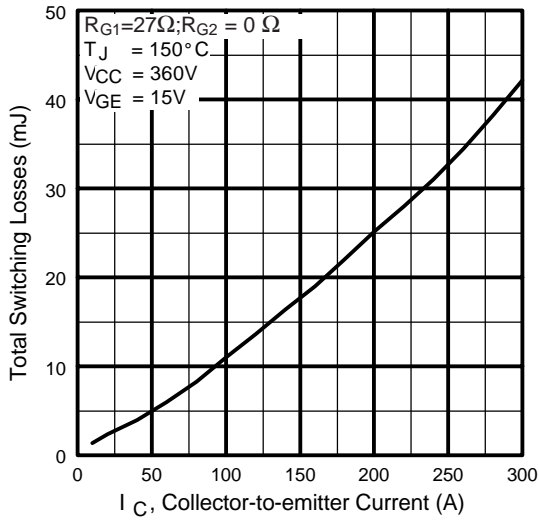


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

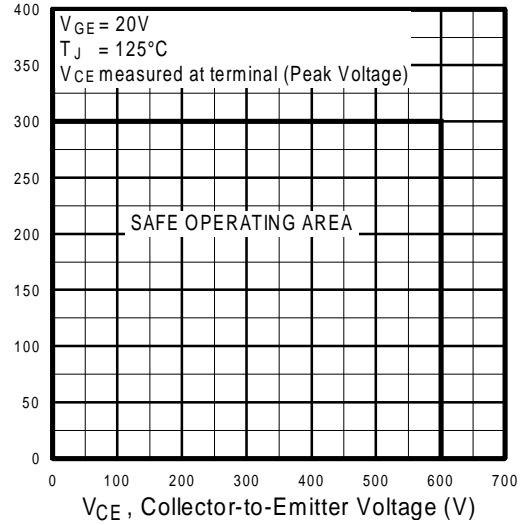


Fig. 12 - Reverse Bias SOA

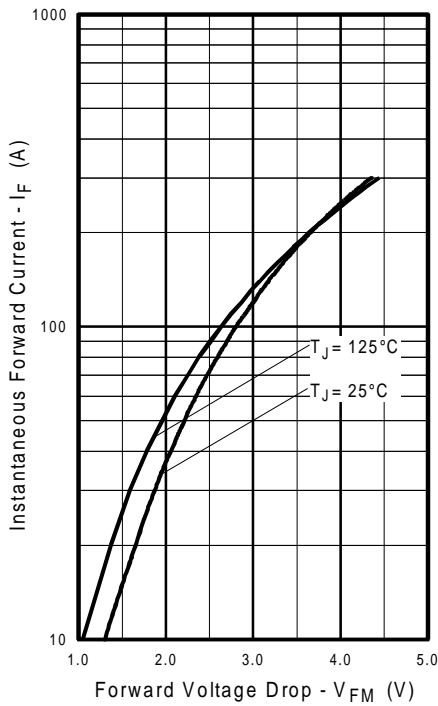


Fig. 13 - Typical Forward Voltage Drop vs. Instantaneous Forward Current

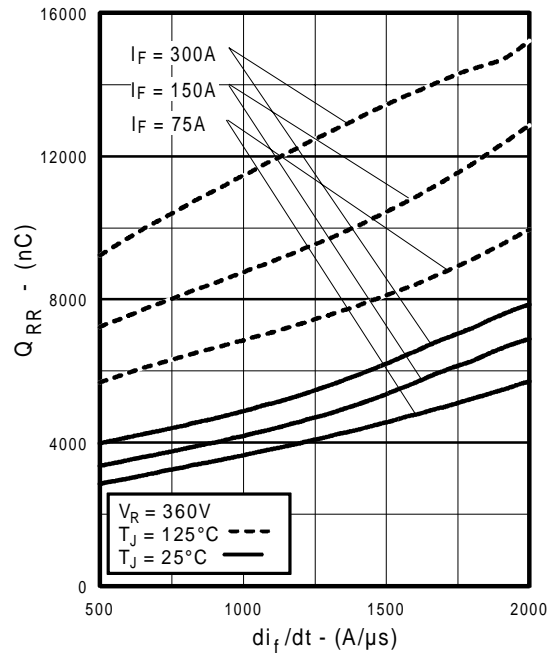


Fig. 14 - Typical Stored Charge vs. di_t/dt

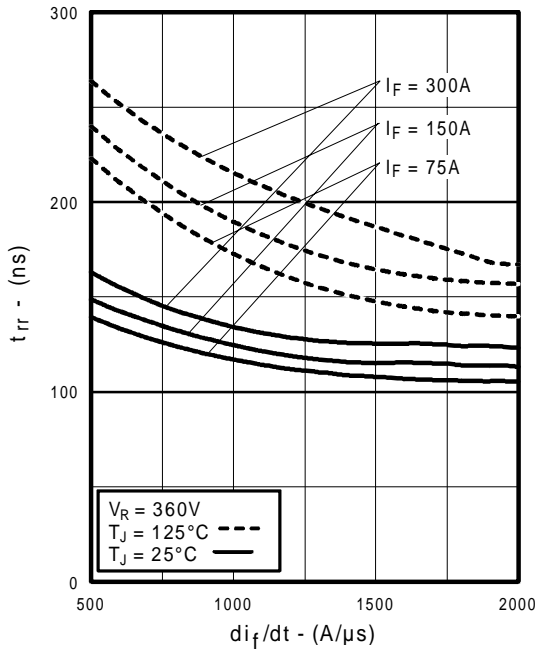


Fig. 15 - Typical Reverse Recovery vs. di_f/dt

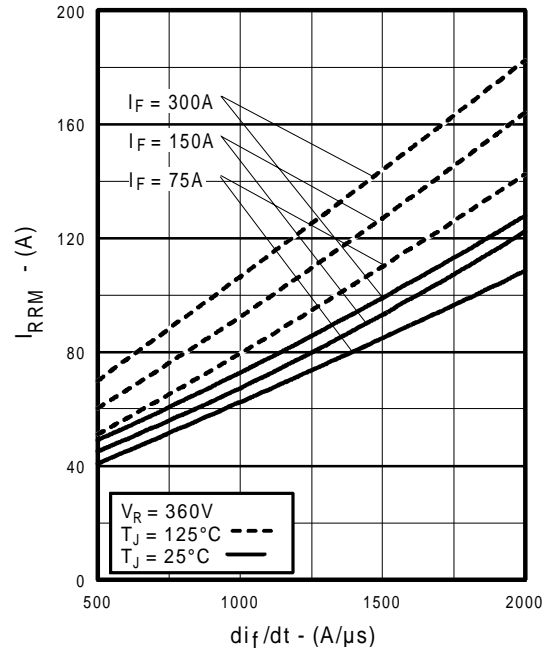


Fig. 16 - Typical Recovery Current vs. di_f/dt

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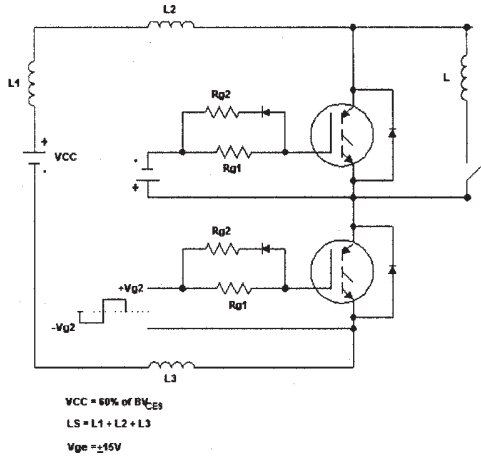


Fig. 17 - Test Circuit for Measurement of I_{LM} , E_{on} , $E_{off}(\text{diode})$, t_{rr} , Q_{rr} , I_{rr} , $t_d(\text{on})$, t_r , $t_d(\text{off})$, t_f

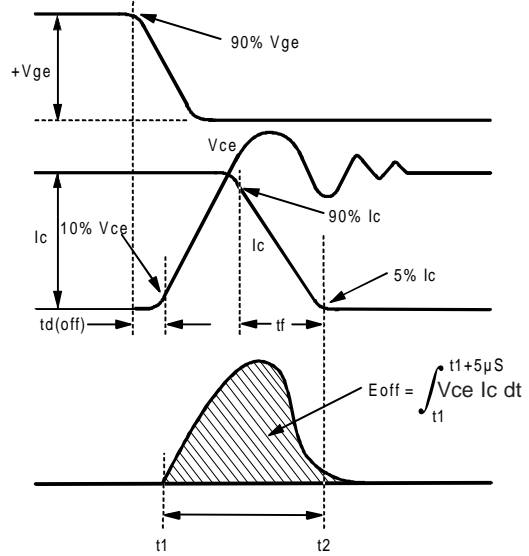


Fig. 18 - Test Waveforms for Circuit of Fig. 17, Defining E_{off} , $t_d(\text{off})$, t_f

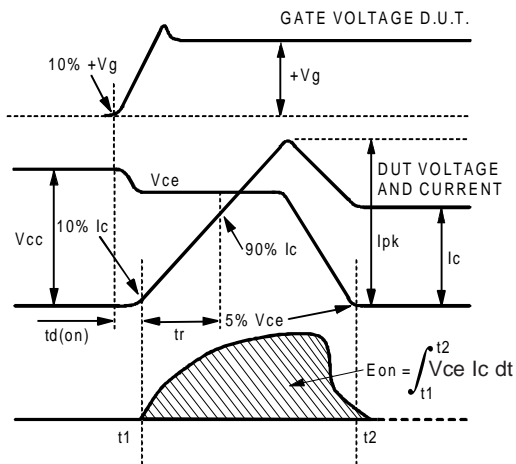


Fig. 19 - Test Waveforms for Circuit of Fig. 17, Defining E_{on} , $t_d(\text{on})$, t_r

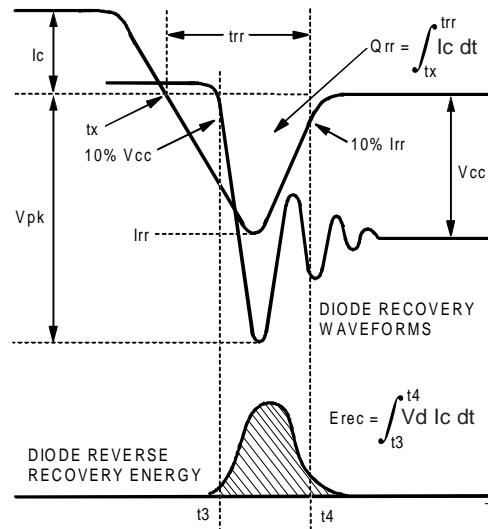


Fig. 20 - Test Waveforms for Circuit of Fig. 17, Defining E_{rec} , t_{rr} , Q_{rr} , I_{rr}

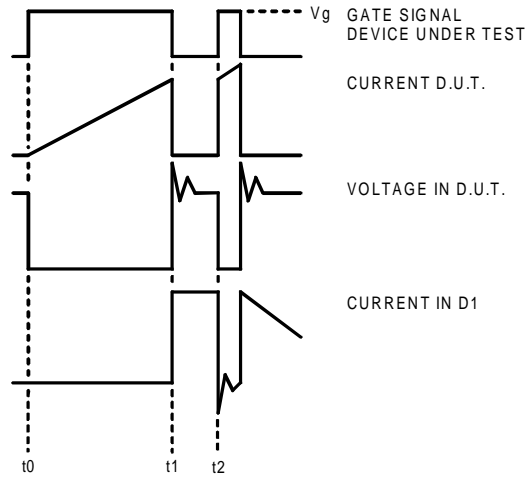


Figure 21. Macro Waveforms for Figure 17's Test Circuit

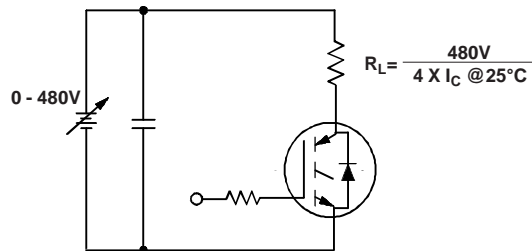


Figure 22. Pulsed Collector Current Test Circuit

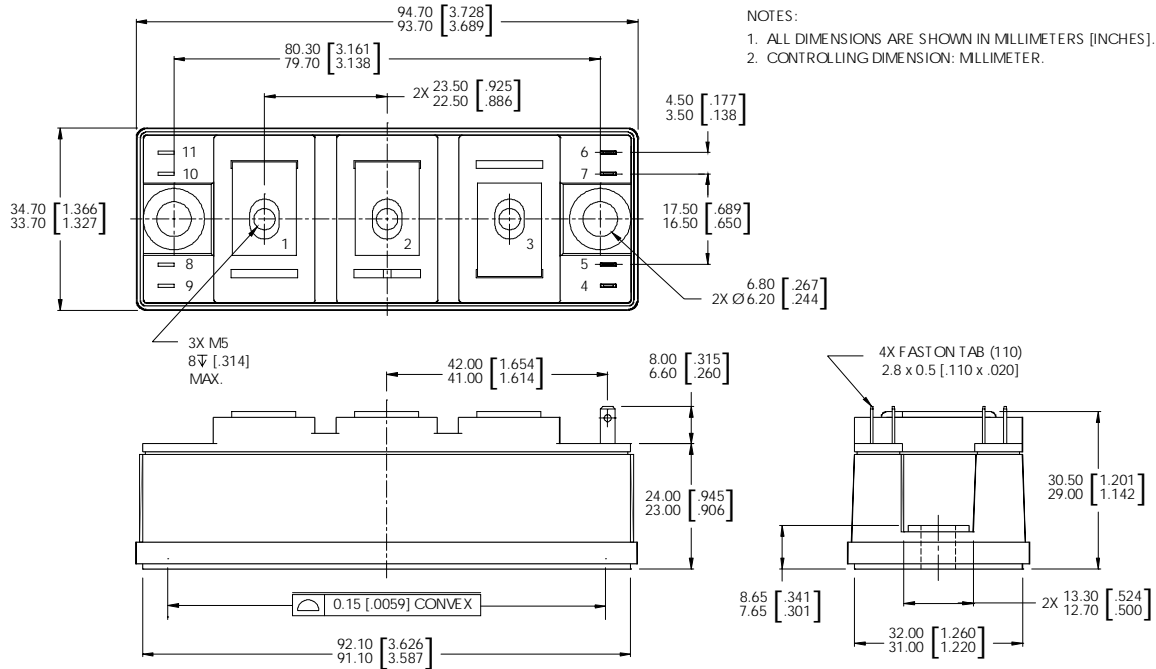
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Notes:

- ① Repetitive rating; $V_{GE} = 20V$, pulse width limited by max. junction temperature.
- ② See fig. 17
- ③ For screws M6.
- ④ For screws M5.
- ⑤ Pulse width 50 μ s; single shot.

Case Outline — INT-A-PAK



Data and specifications subject to change without notice.
This product has been designed and qualified for the Industrial market.
Qualification Standards can be found on IR's Web site.

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