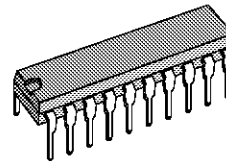


PUSH-PULL FOUR CHANNEL/DUAL H-BRIDGE DRIVER

PRELIMINARY DATA

- 600 mA OUTPUT CURRENT CAPABILITY PER CHANNEL
- 1.2 A PEAK OUTPUT CURRENT (non repetitive) PER CHANNEL
- ENABLE FACILITY
- OVERTEMPERATURE PROTECTION
- LOGICAL "0" INPUT VOLTAGE UP TO 1.5 V (high noise immunity)
- SEPARATE HIGH VOLTAGE POWER SUPPLY (up to 44 V)



POWERDIP (16 + 2 + 2)

ORDERING NUMBER : L293C

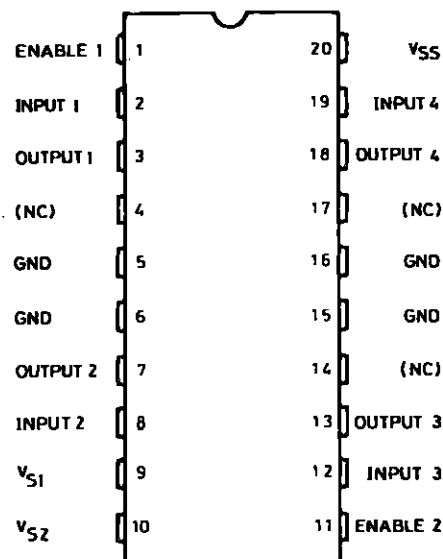
DESCRIPTION

The L293C is a monolithic high voltage, high current integrated circuit four channel driver in a 20 pin DIP. It is designed to accept standard TTL or DTL input logic levels and drive inductive loads (such as relays, solenoids, DC and stepping motors) and switching power transistors.

The device may easily be used as a dual H-bridge driver: separate chip enable and high voltage power supply pins are provided for each H-bridge. In addition, a separate power supply is provided for the logic section of the device.

The L293C is assembled in a 20 lead plastic package which has 4 center pins connected together and used for heatsinking.

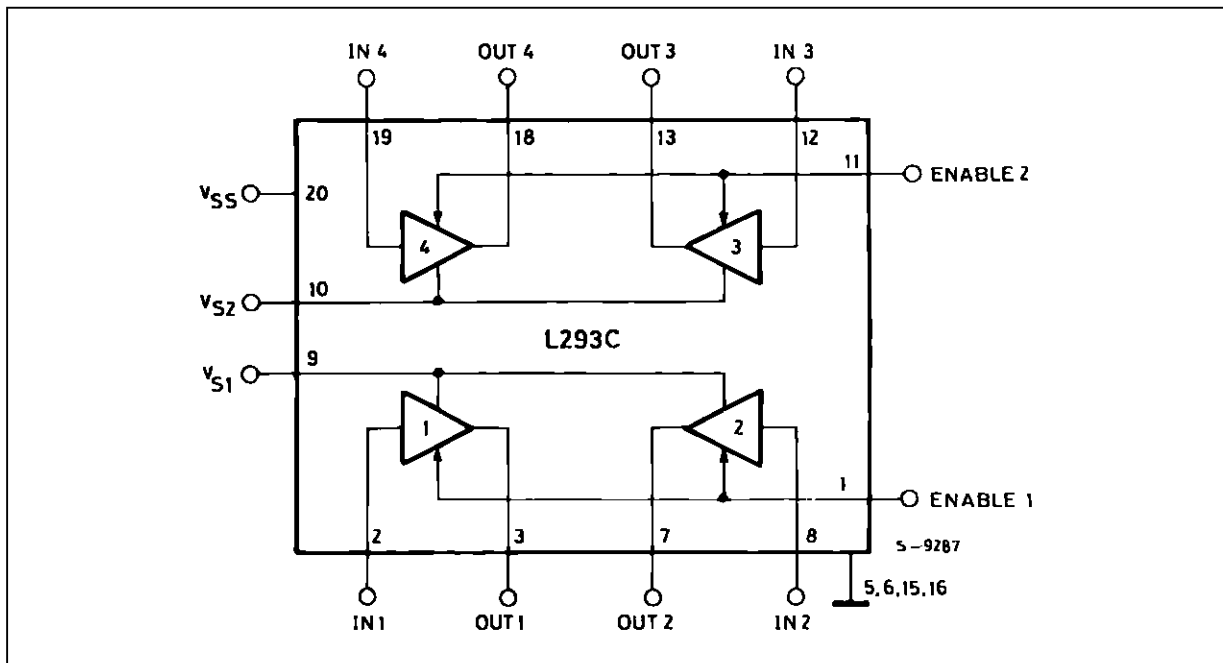
PIN CONNECTION



5-9280

L293C

BLOCK DIAGRAM

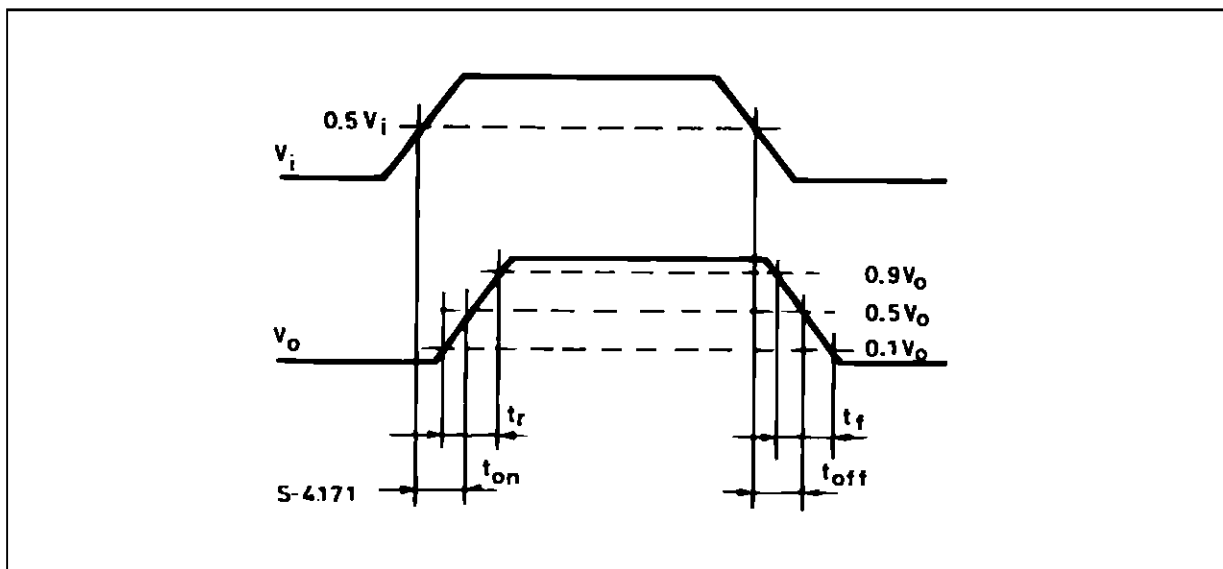


TRUTH TABLE

Input	Enable	Output
H	H	H
L	H	L
X	L	Z

Z = High output impedance

SWITCHING TIMES



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _S	Supply Voltage	50	V
V _{SS}	Logic Supply Voltage	7	V
V _I	Input Voltage	7	V
V _{EN}	Enable Voltage	7	V
I _{out}	Peak Output Current (non-repetitive t = 5 ms)	1.2	A
P _{tot}	Total Power Dissipation at T _{ground-pins} = 80°C	5	W
T _{stg} , T _j	Storage and Junction Temperature	-40 to 150	°C

THERMAL DATA

Symbol	Parameter	Value	Unit
R _{th j-case}	Thermal Resistance Junction-case Max.	14	°C/Ω
R _{th j-amb}	Thermal Resistance Junction-ambient Max.	80	°C/Ω

ELECTRICAL CHARACTERISTICS

(for each channel, V_S = 24 V, V_{SS} = 5 V, T_{amb} = 25°C, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _S	Supply Voltage (pin 9, 10)		V _{SS}		44	V
V _{SS}	Logic Supply Voltage (pin 20)		4.5		7	V
I _S	Total Quiescent Supply Current (pin 9, 10)	V _I = L I _O = 0 V _{EN} = H V _I = H I _O = 0 V _{EN} = H V _{EN} = L		2 16	6 24 4	mA
I _{SS}	Total Quiescent Logic Supply Current (pin 20)	V _I = L I _O = 0 V _{EN} = H V _I = H I _O = 0 V _{EN} = H V _{EN} = L		44 16 16	60 22 24	mA
V _{IL}	Input Low Voltage (pin 2, 8, 12, 19)		-0.3		1.5	V
V _{IH}	Input High Voltage (pin 2, 8, 12, 19)		2.3		V _{SS}	V
I _{IL}	Low Voltage Input Current (pin 2, 8, 12, 19)	V _I = 1.5 V			-10	μA
I _{IH}	High Voltage Input Current (pin 2, 8, 12, 19)	2.3 V ≤ V _I ≤ V _{SS} - 0.6 V		30	100	μA
V _{EN L}	Enable Low Voltage (pin 1, 11)		-0.3		1.5	V
V _{EN H}	Enable High Voltage (pin 1, 11)		2.3		V _{SS}	V
I _{EN L}	Low Voltage Enable Current (pin 1, 11)	V _{EN L} = 1.5 V		-30	-100	μA
I _{EN H}	High Voltage Enable Current (pin 1, 11)	2.3 V ≤ V _{EN H} ≤ V _{SS} - 0.6			±10	μA
V _{CE(sat)H}	Source Output Saturation Voltage (pins 3, 7, 13, 18)	I _O = -0.6 A		1.4	1.8	V
V _{CE(sat)L}	Sink Output Saturation Voltage (pins 3, 7, 13, 18)	I _O = +0.6 A		1.2	1.8	V
t _r	Rise Time (*)	0.1 to 0.9 V _O		250		ns
t _f	Fall Time (*)	0.9 to 0.1 V _O		250		ns
t _{on}	Turn-on Delay (*)	0.5 V _I to 0.5 V _O		750		ns
t _{off}	Turn-off Delay (*)	0.5 V _I to 0.5 V _O		200		ns

(*) See switching times diagram

DIP20PW.TBL

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