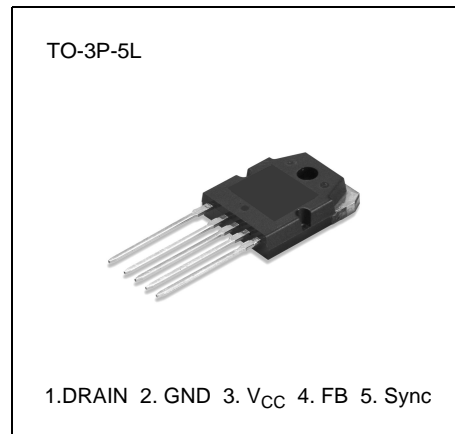


**SPS**

The SPS product family is specially designed for an off line SMPS with minimal external component. The SPS consist of high voltage Power SenseFET and current mode PWM IC. Included control IC features a tr-immed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, and temperature compensated precision current source for loop compensation and fault protection circuitry. Compared to discrete MOSFET and controller or RCC switching converter solution, a SPS can reduce total component count, design size, weight and at the same time increase efficiency, productivity and system reliability. It has a basic platform well suited for cost effective monitor power supply.



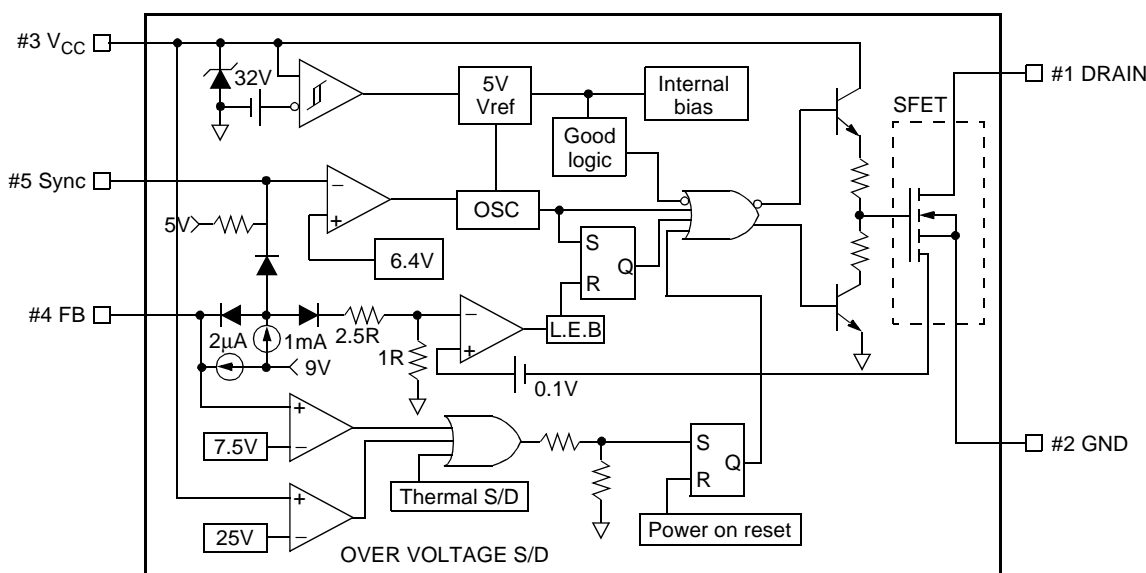
**FEATURES**

- Wide operating frequency range up to 150kHz
- Pulse by pulse over current limiting
- Over load protection
- Over voltage protection (Min. 23V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- External sync terminal
- Latch up Mode

**ORDERING INFORMATION**

Device	Package	Rating	Topr (°C)
KA2S0880	TO-3P-5L	800V, 8A	-25°C to +85°C

**BLOCK DIAGRAM**



## ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Drain-source (GND) voltage <sup>(1)</sup>	$V_{DSS}$	800	V
Drain-Gate voltage ( $R_{GS}=1M\Omega$ )	$V_{DGR}$	800	V
Gate-source (GND) voltage	$V_{GS}$	$\pm 30$	V
Drain current pulsed <sup>(2)</sup>	$I_{DM}$	32.0	$A_{DC}$
Single pulsed avalanche energy <sup>(3)</sup>	$E_{AS}$	810	mJ
Avalanche current <sup>(4)</sup>	$I_{AS}$	25	A
Continuous drain current ( $T_C=25^\circ C$ )	$I_D$	8.0	$A_{DC}$
Continuous drain current ( $T_C=100^\circ C$ )	$I_D$	5.6	$A_{DC}$
Supply voltage	$V_{CC}$	30	V
Analog input voltage range	$V_{FB}$	$-0.3$ to $V_{SD}$	V
Total power dissipation	$P_D$ (watt H/S)	190	W
	Derating	1.54	$W/^\circ C$
Operating temperature	$T_{OPR}$	$-25$ to $+85$	$^\circ C$
Storage temperature	$T_{STG}$	$-55$ to $+150$	$^\circ C$

## NOTES:

- $T_j=25^\circ C$  to  $150^\circ C$
- Repetitive rating: Pulse width limited by maximum junction temperature
- $L=24mH$ ,  $V_{DD}=50V$ ,  $R_G=25\Omega$ , starting  $T_j=25^\circ C$
- $L=13\mu H$ ,  $V_{DD}=330V$ ,  $T_j=25^\circ C$

**ELECTRICAL CHARACTERISTICS (SFET part)**

(Ta=25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=50\mu A$	800	–	–	V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS}=\text{Max.}, \text{Rating}, V_{GS}=0V$	–	–	50	$\mu A$
		$V_{DS}=0.8\text{Max.}, \text{Rating}, V_{GS}=0V, T_C=125^\circ C$	–	–	200	$\mu A$
Static drain-source on resistance <sup>(note)</sup>	$R_{DS(ON)}$	$V_{GS}=10V, I_D=5.0A$	–	1.2	1.5	$\Omega$
Forward transconductance <sup>(note)</sup>	gfs	$V_{DS}=15V, I_D=5.0A$	1.5	2.5	–	mho
Input capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=25V, f=1\text{MHz}$	–	2460	–	$\mu F$
Output capacitance	$C_{oss}$		–	210	–	
Reverse transfer capacitance	$C_{rss}$		–	64	–	
Turn on delay time	td(on)	$V_{DD}=0.5BV_{DSS}, I_D=8.0A$ (MOSFET switching time are essentially independent of operating temperature)	–	–	90	nS
Rise time	tr		–	95	200	
Turn off delay time	td(off)		–	150	450	
Fall time	tf		–	60	150	
Total gate charge (gate-source+gate-drain)	Qg	$V_{GS}=10V, I_D=8.0A, V_{DS}=0.5BV_{DSS}$ (MOSFET switching time are essentially independent of operating temperature)	–	–	150	nC
Gate-source charge	Qgs		–	20	–	
Gate-drain (Miller) charge	Qgd		–	70	–	

**NOTE:** Pulse test: Pulse width  $\leq 300\mu S$ , duty cycle  $\leq 2\%$

## ELECTRICAL CHARACTERISTICS (Control part)

( $T_a=25^\circ\text{C}$  unless otherwise specified)

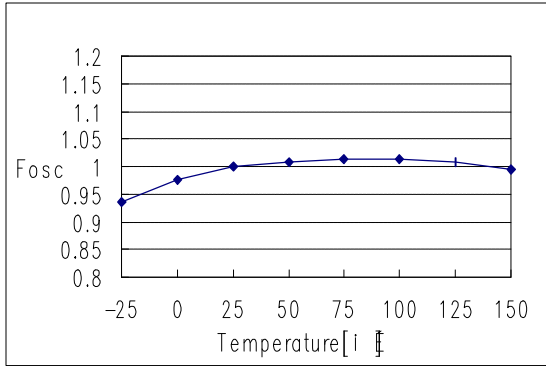
Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
<b>REFERENCE SECTION</b>						
Output voltage <sup>(1)</sup>	$V_{\text{ref}}$	$T_a=25^\circ\text{C}$	4.80	5.00	5.20	V
Temperature Stability <sup>(1)(2)</sup>	$V_{\text{ref}}/\Delta T$	$-25^\circ\text{C}\leq T_a\leq+85^\circ\text{C}$	–	0.3	0.6	mV/ $^\circ\text{C}$
<b>OSCILLATOR SECTION</b>						
Initial accuracy	$F_{\text{OSC}}$	$T_a=25^\circ\text{C}$	18	20	22	kHz
Frequency change with temperature <sup>(2)</sup>	$\Delta F/\Delta T$	$-25^\circ\text{C}\leq T_a\leq+85^\circ\text{C}$	–	$\pm 5$	$\pm 10$	%
Sync threshold voltage <sup>(3)</sup>	$V_{\text{SYTH}}$	$V_{\text{fb}}=5\text{V}$	6.0	6.4	6.8	V
<b>PWM SECTION</b>						
Maximum duty cycle	$D_{\text{max}}$	–	92	95	98	%
<b>FEEDBACK SECTION</b>						
Feedback source current	$I_{\text{FB}}$	$T_a=25^\circ\text{C}$ , $V_{\text{fb}}=\text{GND}$	0.7	0.9	1.1	mA
Shutdown delay current	$I_{\text{delay}}$	$T_a=25^\circ\text{C}$ , $5\text{V}\leq V_{\text{fb}}\leq V_{\text{SD}}$	1.4	1.8	2.2	$\mu\text{A}$
<b>OVER CURRENT PROTECTION SECTION</b>						
Over current protection	$I_{\text{L}}(\text{max})$	Max. inductor current	4.40	5.00	5.60	A
<b>UVLO SECTION</b>						
Start threshold voltage	$V_{\text{th}}(\text{H})$	–	14	15	16	V
Minimum operating voltage	$V_{\text{th}}(\text{L})$	After turn on	9	10	11	V
<b>TOTAL STANDBY CURRENT SECTION</b>						
Start current	$I_{\text{ST}}$	$V_{\text{CC}}=14\text{V}$	0.1	0.3	0.55	mA
Operating supply current (control part only)	$I_{\text{OPR}}$	$T_a=25^\circ\text{C}$	6	12	18	mA
$V_{\text{CC}}$ zener voltage	$V_{\text{Z}}$	$I_{\text{CC}}=20\text{mA}$	30	32.5	35	V
<b>SHUTDOWN SECTION</b>						
Shutdown Feedback voltage	$V_{\text{SD}}$	–	6.9	7.5	8.1	V
Thermal shutdown temperature ( $T_j$ ) <sup>(1)</sup>	$T_{\text{SD}}$	–	140	160	–	$^\circ\text{C}$
Over voltage protection voltage	$V_{\text{OVP}}$	–	23	25	28	V
<b>SOFT START SECTION</b>						
Soft start current	$I_{\text{SS}}$	Sync & S/S=GND	0.8	1.0	1.2	mA
Soft start voltage	$V_{\text{SS}}$	$V_{\text{FB}}=2\text{V}$	4.7	5.0	5.3	V

### NOTES:

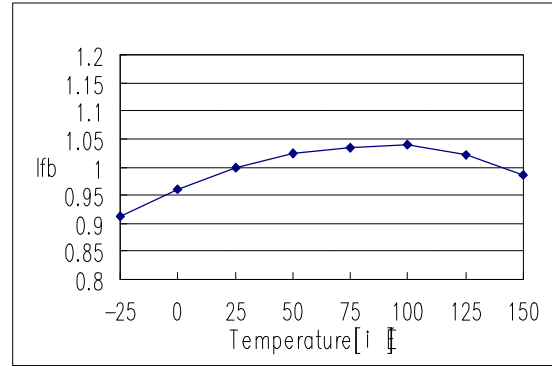
1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS (wafer test) process
3. The amplitude of the sync. pulse is recommended to be between 2V and 3V for stable sync. function.

**TYPICAL PERFORMANCE CHARACTERISTICS**

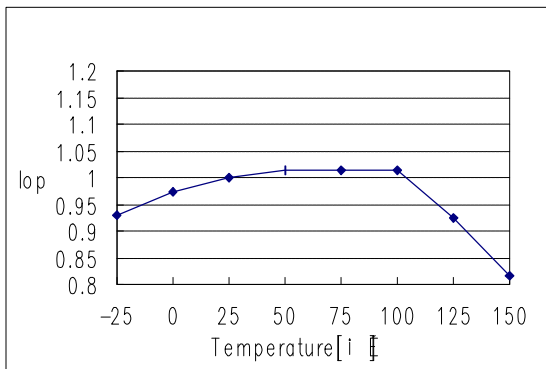
(These characteristic graphs are normalized at Ta=25°C)



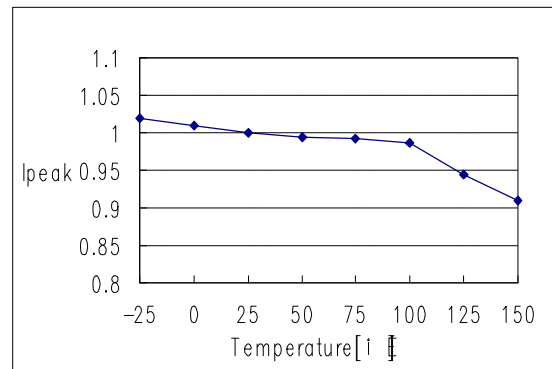
**Figure 1. Operating Frequency**



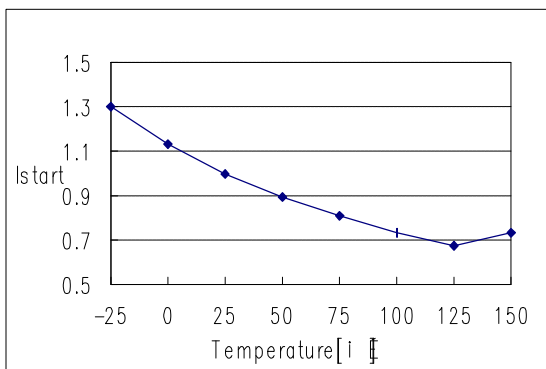
**Figure 2. Feedback Source Current**



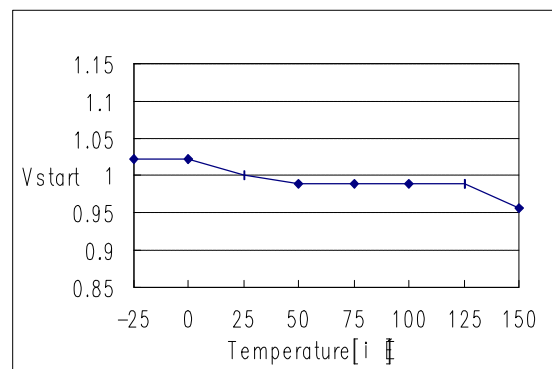
**Figure 3. Operating Current**



**Figure 4. Max. Inductor Current**



**Figure 5. Start up Current**



**Figure 6. Start Threshold Voltage**

### TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(These characteristic graphs are normalized at  $T_a=25^\circ\text{C}$ )

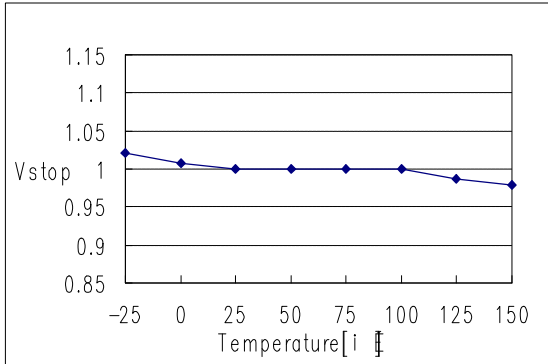


Figure 7. Stop Threshold Voltage

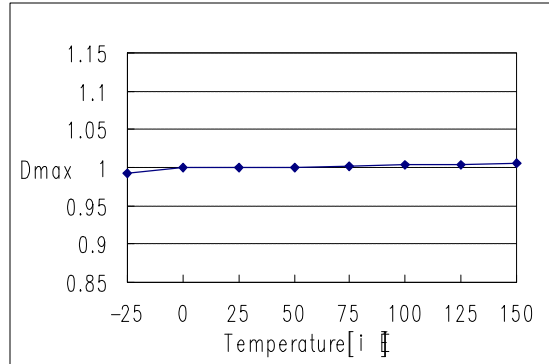


Figure 8. Maximum Duty Cycle

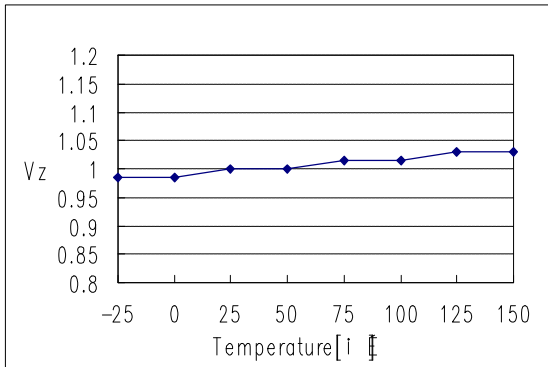


Figure 9. V<sub>CC</sub> Zener Voltage

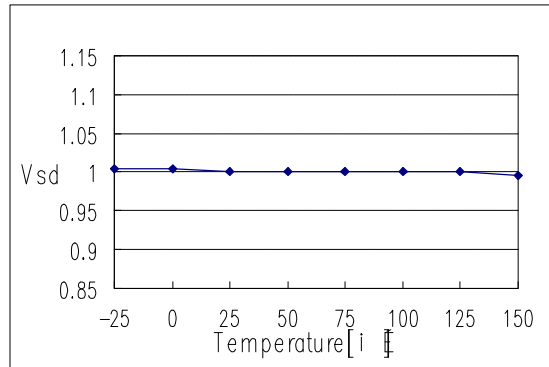


Figure 10. Shutdown Feedback Voltage

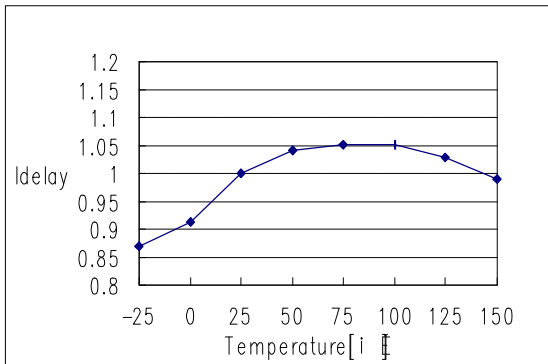


Figure 11. Shutdown Delay Current

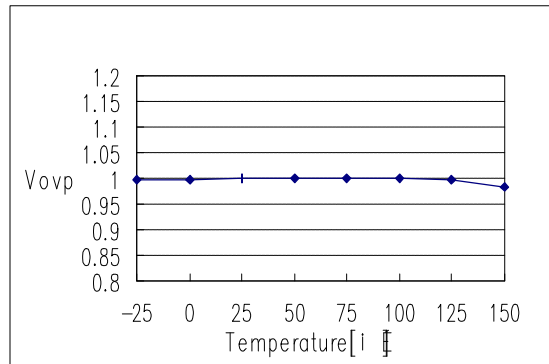
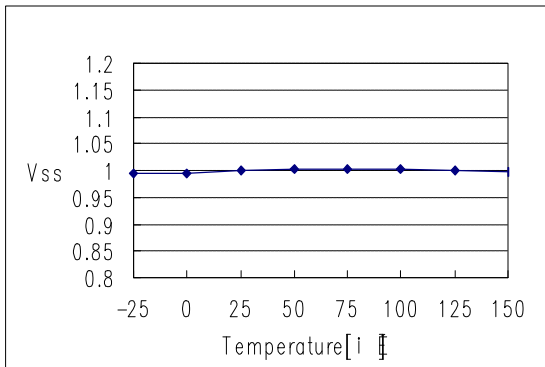


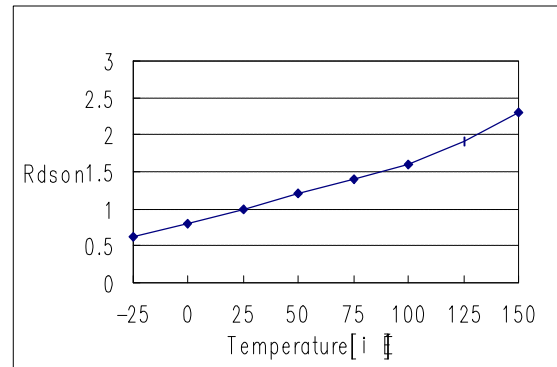
Figure 12. Over Voltage Protection

**TYPICAL PERFORMANCE CHARACTERISTICS (Continued)**

(These characteristic graphs are normalized at  $T_a=25^\circ\text{C}$ )



**Figure 13. Soft Start Voltage**



**Figure 14. Drain Source Turn-on Resistance**

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