DATA SHEET

BFQ255; BFQ255A
PNP video transistors

Product specification
Supersedes data November 1995
File under Discrete Semiconductors, SC05

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PNP video transistors BFQ255; BFQ255A

FEATURES

- High breakdown voltages
- Low output capacitance
- High gain bandwidth
- Good thermal stability
- Gold metallization ensures excellent reliability.

APPLICATIONS

- Buffer/driver in high-resolution colour graphics monitors.

DESCRIPTION

PNP video transistor in a SOT128B (TO-202) plastic package.
NPN complements: BFQ235 and BFQ235A.

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base

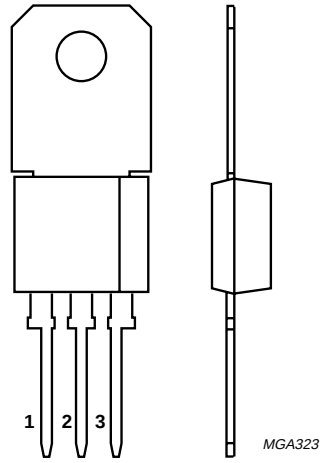


Fig.1 Simplified outline (SOT128B; TO-202).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter				
	BFQ255		–	–	–100	V
	BFQ255A		–	–	–115	V
V_{CER}	collector-emitter voltage	R_{BE} = 100 Ω				
	BFQ255		–	–	–95	V
	BFQ255A		–	–	–110	V
I_C	collector current (DC)		–	–	–300	mA
P_{tot}	total power dissipation	$T_s \leq 100$ °C; note 1	–	–	3	W
h_{FE}	DC current gain	I_C = –50 mA; V_{CE} = –10 V; T_{amb} = 25 °C	20	30	–	
f_T	transition frequency	I_C = –50 mA; V_{CE} = –10 V; f = 100 MHz; T_{amb} = 25 °C				
	BFQ255		1	1.3	–	GHz
	BFQ255A		0.8	1.2	–	GHz

Note

1. T_s is the temperature at the soldering point of the collector pin.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	BFQ255		–	–100	V
	BFQ255A		–	–115	V
V_{CEO}	collector-emitter voltage	open base			
	BFQ255		–	–65	V
	BFQ255A		–	–95	V
V_{CER}	collector-emitter voltage	R_{BE} = 100 Ω			
	BFQ255		–	–95	V
	BFQ255A		–	–110	V
V_{EBO}	emitter-base voltage	open collector	–	–3	V
I_C	collector current (DC)		–	–300	mA
P_{tot}	total power dissipation	$T_s \leq 100$ °C; note 1; see Fig.3	–	3	W
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	175	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}s}$	thermal resistance from junction to soldering point	$T_s \leq 100$ °C; note 1	25	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.

CHARACTERISTICS

T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)CBO}$	collector-base breakdown voltage	I_C = –0.1 mA; I_E = 0				
	BFQ255		–100	–	–	V
	BFQ255A		–115	–	–	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	I_C = –10 mA; I_B = 0				
	BFQ255		–66	–	–	V
	BFQ255A		–95	–	–	V
$V_{(BR)CER}$	collector-emitter breakdown voltage	I_C = –10 mA; R_{BE} = 100 Ω				
	BFQ255		–95	–	–	V
	BFQ255A		–110	–	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I_E = –0.1 mA; I_C = 0	–3	–	–	V
I_{CES}	collector-emitter cut-off current	I_B = 0; V_{CE} = –50 V	–	–	–100	μA
I_{CBO}	collector-base cut-off current	I_E = 0; V_{CB} = –50 V	–	–	–20	μA
h_{FE}	DC current gain	I_C = –50 mA; V_{CE} = –10 V; T_{amb} = 25 °C; see Fig.4	20	30	–	
C_{cb}	collector-base capacitance	I_C = 0; V_{CB} = –10 V; f = 1 MHz; see Fig.5	–	2	–	pF
f_T	transition frequency	I_C = –50 mA; V_{CE} = –10 V; f = 100 MHz; T_{amb} = 25 °C; see Fig.6				
	BFQ255		1	1.3	–	GHz
	BFQ255A		0.8	1.2	–	GHz

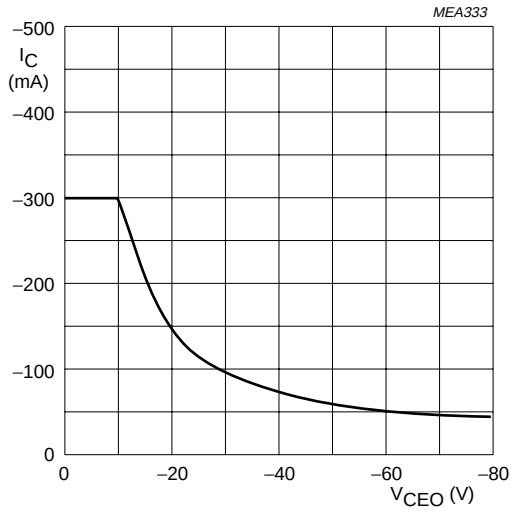


Fig.2 DC SOAR.

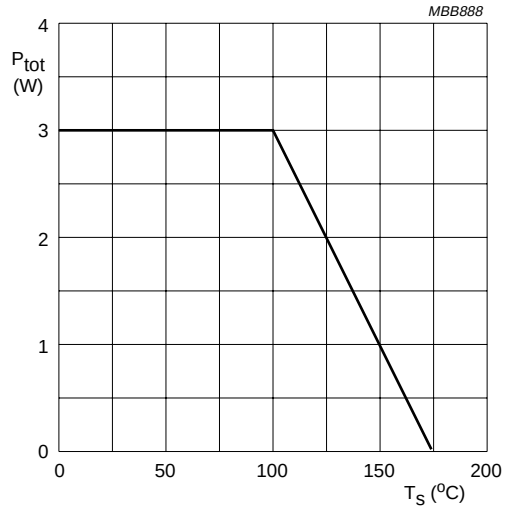
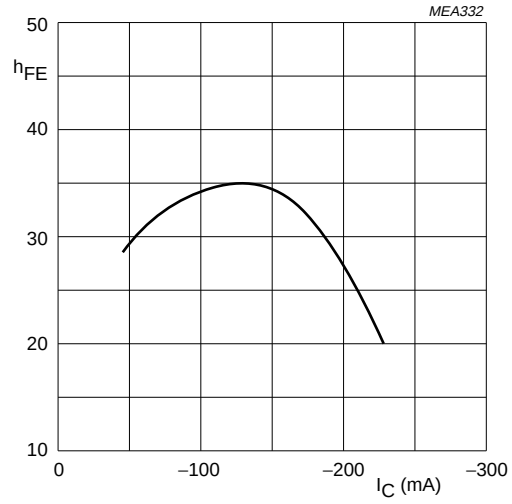
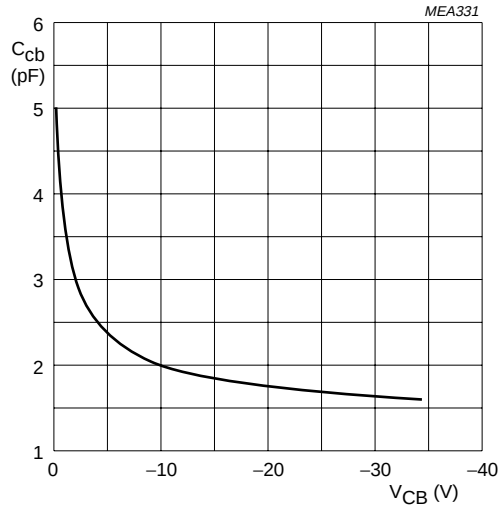


Fig.3 Power derating curve.



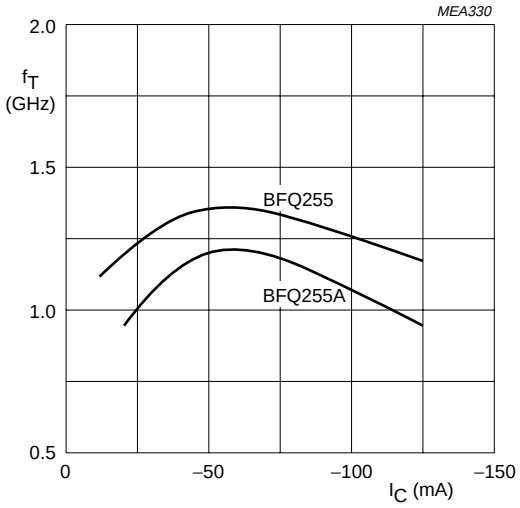
V_{CE} = −10 V; T_{amb} = 25 °C.

Fig.4 DC current gain as a function of collector current; typical values.



f = 1 MHz; T_{amb} = 25 °C.

Fig.5 Collector-base capacitance as a function of collector-base voltage; typical values.

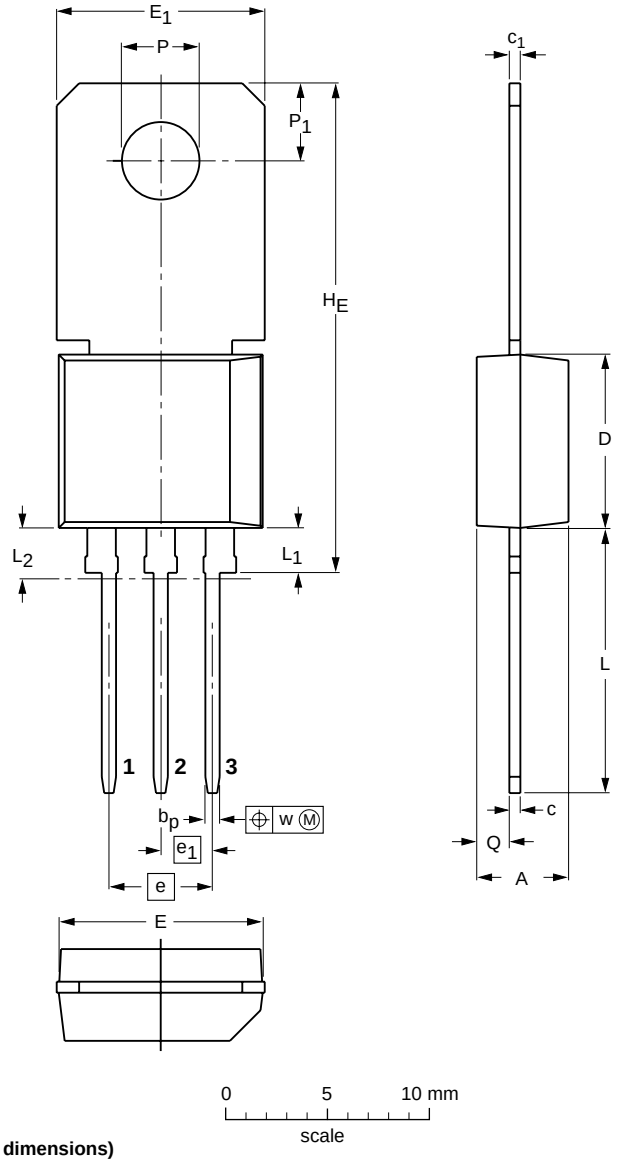


V_{CE} = −10 V; f = 100 MHz; T_{amb} = 25 °C.

Fig.6 Transition frequency as a function of collector current; typical values.

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; with cooling fin, mountable to heatsink, 1 mounting hole; 3 leads (in-line) **SOT128B**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b_p	c	c_1	D	E	E_1	e	e_1	H_E	L	L_1	$L_2^{(1)}$ max	P	P_1	Q	w
mm	4.6 4.4	0.8 0.6	0.65 0.5	0.56 0.46	8.6 8.4	10.1 9.9	10.4 10.0	5.08	2.54	24.2 23.8	13.3 12.2	2.4 2.0	2.5	3.8 3.6	3.9 3.7	1.7 1.5	0.25

Note

1. Plastic flash allowed within this zone

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT128B		TO-202				97-02-28

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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