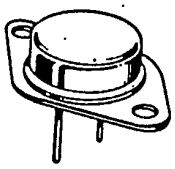
IRF320/321/322/323

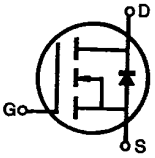
N-CHANNEL POWER MOSFETS

FEATURES

- Low $R_{DS(on)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Low input capacitance
- Extended safe operating area
- Improved high temperature reliability
- TO-3 package (Standard)

TO-3





PRODUCT SUMMARY

Part Number	V_{DS}	$R_{DS(on)}$	I_D
IRF320	400V	1.8Ω	3.0A
IRF321	350V	1.8Ω	3.0A
IRF322	400V	2.5Ω	2.5A
IRF323	350V	2.5Ω	2.5A

MAXIMUM RATINGS

Characteristic	Symbol	IRF320	IRF321	IRF322	IRF323	Unit
Drain-Source Voltage (1)	V_{DSS}	400	350	400	350	Vdc
Drain-Gate Voltage (R_{GS}=1.0MΩ) (1)	V_{DGR}	400	350	400	350	Vdc
Gate-Source Voltage	V_{GS}	±20				Vdc
Continuous Drain Current T_C=25°C	I_D	3.0	3.0	2.5	2.5	Adc
Continuous Drain Current T_C=100°C	I_D	2.0	2.0	1.5	1.5	Adc
Drain Current—Pulsed (3)	I_{DM}	12	12	10	10	Adc
Gate Current—Pulsed	I_{GM}	±1.5				Adc
Total Power Dissipation @ T_C=25°C Derate above 25°C	P_D	40 0.32				Watts W/°C
Operating and Storage Junction Temperature Range	T_J, Tstg	−55 to 150				°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	300				°C

Notes: (1) T_J=25°C to 150°C
(2) Pulse test: Pulse width≤300μs, Duty Cycle≤2%
(3) Repetitive rating: Pulse width limited by max. junction temperature

ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise specified)

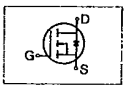
Characteristic	Symbol	Type	Min	Typ	Max	Units	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	IRF320 IRF322	400	—	—	V	V_{GS}=0V
		IRF321 IRF323	350	—	—	V	I_D=250μA
Gate Threshold Voltage	$V_{GS(th)}$	ALL	2.0	—	4.0	V	V_{DS}=V_{GS}, I_D=250μA
Gate-Source Leakage Forward	I_{GSS}	ALL	—	—	100	nA	V_{GS}=20V
Gate-Source Leakage Reverse	I_{GSS}	ALL	—	—	-100	nA	V_{GS}=−20V
Zero Gate Voltage Drain Current	I_{DSS}	ALL	—	—	250	μA	V_{DS}=Max. Rating, V_{GS}=0V
			—	—	1000	μA	V_{DS}=Max. Rating×0.8, V_{GS}=0V, T_C=125°C
On-State Drain-Source Current (2)	$I_{D(on)}$	IRF320 IRF321	3.0	—	—	A	V_{DS}>$I_{D(on)}$×$R_{DS(on)\ max.}$, V_{GS}=10V
		IRF322 IRF323	2.5	—	—	A	
Static Drain-Source On-State Resistance (2)	$R_{DS(on)}$	IRF320 IRF321	—	1.4	1.8	Ω	V_{GS}=10V, I_D=1.5A
		IRF322 IRF323	—	1.7	2.5	Ω	
Forward Transconductance (2)	g_{fs}	ALL	1.0	2.2	—	℧	V_{DS}>$I_{D(on)}$×$R_{DS(on)\ max.}$, I_D=1.5A
Input Capacitance	C_{iss}	ALL	—	460	600	pF	V_{GS}=0V, V_{DS}=25V, f=1.0MHz
Output Capacitance	C_{oss}	ALL	—	90	200	pF	
Reverse Transfer Capacitance	C_{rss}	ALL	—	30	40	pF	
Turn-On Delay Time	$t_{d(on)}$	ALL	—	—	40	ns	V_{DD}=0.5BV_{DSS}, I_D=1.5A, Z_O=50Ω (MOSFET switching times are essentially independent of operating temperature.)
Rise Time	t_r	ALL	—	—	50	ns	
Turn-Off Delay Time	$t_{d(off)}$	ALL	—	—	100	ns	
Fall Time	t_f	ALL	—	—	50	ns	
Total Gate Charge (Gate-Source Plus Gate-Drain)	Q_g	ALL	—	12.5	15	nC	V_{GS}=10V, I_D=4.0A, V_{DS}=0.8 Max. Rating (Gate charge is essentially independent of operating temperature.)
Gate-Source Charge	Q_{gs}	ALL	—	2.8	—	nC	
Gate-Drain ("Miller") Charge	Q_{gd}	ALL	—	9.7	—	nC	

THERMAL RESISTANCE

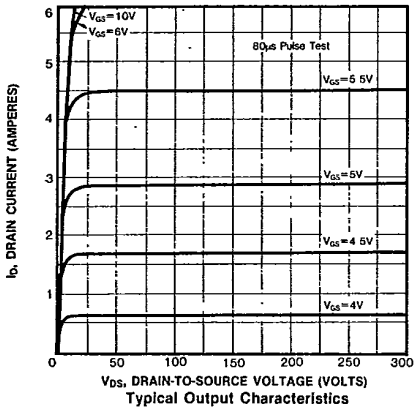
Junction-to-Case	R_{thJC}	ALL	—	—	3.12	K/W	
Case-to-Sink	R_{thCS}	ALL	—	0.1	—	K/W	Mounting surface flat, smooth, and greased
Junction-to-Ambient	R_{thJA}	ALL	—	—	30	K/W	Free Air Operation

Notes: (1) T_J=25°C to 150°C
(2) Pulse test: Pulse width≤300μs, Duty Cycle≤2%
(3) Repetitive rating: Pulse width limited by max. junction temperature

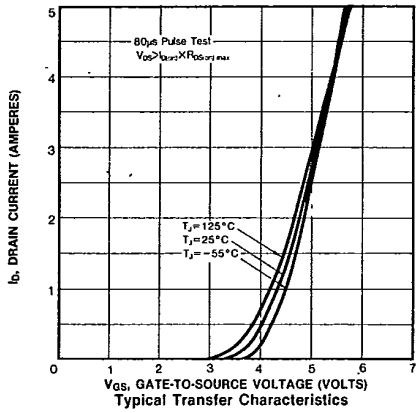
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristic	Symbol	Type	Min	Typ	Max	Units	Test Conditions
Continuous Source Current (Body Diode)	I_S	IRF320 IRF321	—	—	3.0	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
		IRF322 IRF323	—	—	2.5	A	
Pulse Source Current (Body Diode) (3)	I_{SM}	IRF320 IRF321	—	—	12	A	
		IRF322 IRF323	—	—	10	A	
Diode Forward Voltage (2)	V_{SD}	IRF320 IRF321	—	—	1.6	V	T_C=25°C, I_S=3.0A, V_{GS}=0V
		IRF322 IRF323	—	—	1.5	V	T_C=25°C, I_S=2.5A, V_{GS}=0V
Reverse Recovery Time	t_{rr}	ALL	—	450	—	ns	T_J=150°C, I_F=3.0A, dI_F/dt=100A/μs

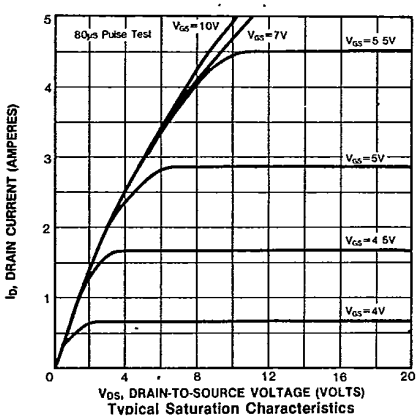
Notes: (1) T_J=25°C to 150°C (2) Pulse test: Pulse width≤300μs, Duty Cycle≤2%
(3) Repetitive rating: Pulse width limited by max. junction temperature



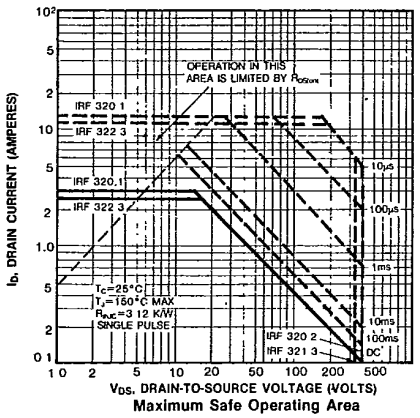
Typical Output Characteristics



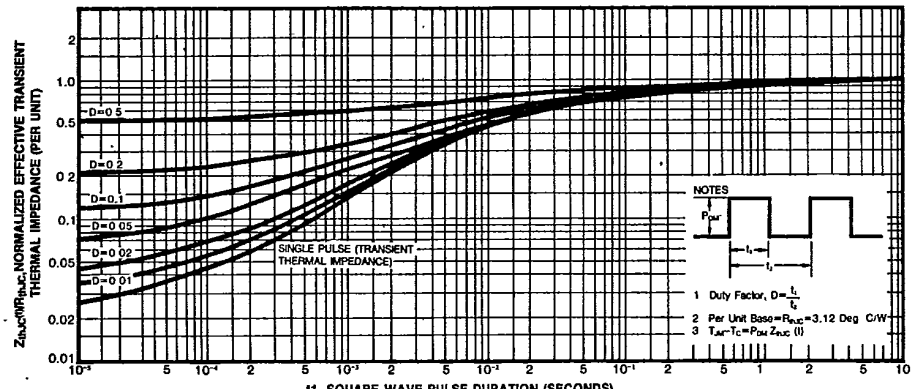
Typical Transfer Characteristics



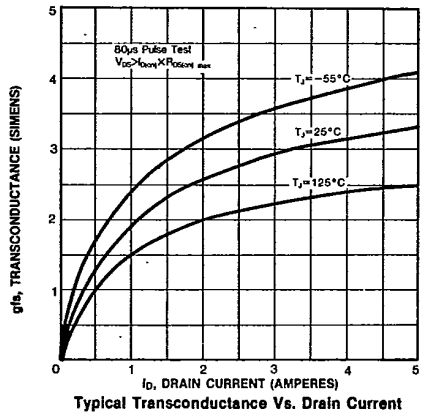
Typical Saturation Characteristics



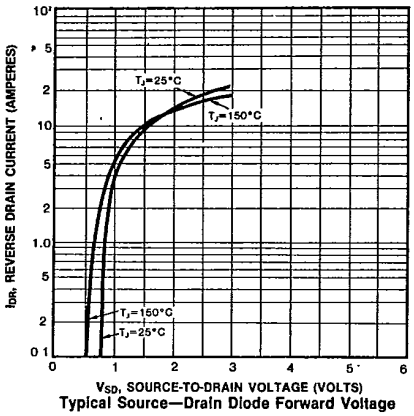
Maximum Safe Operating Area



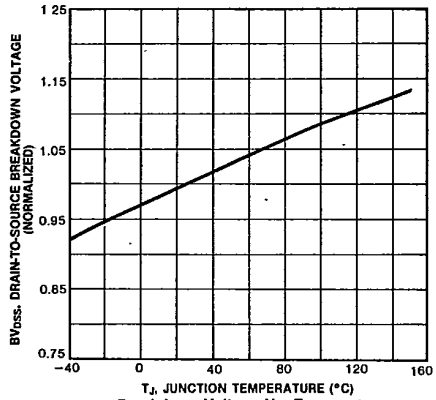
Maximum Effective Transient Thermal Impedance Junction-to-Case Vs. Pulse Duration



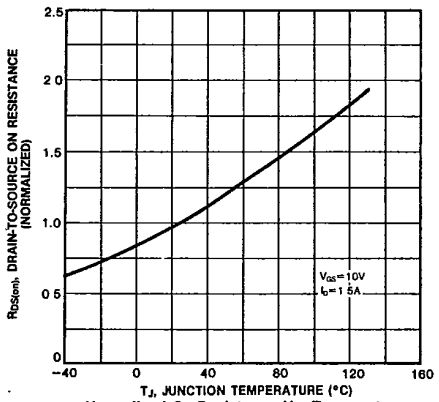
Typical Transconductance Vs. Drain Current



Typical Source—Drain Diode Forward Voltage



Breakdown Voltage Vs. Temperature



Normalized On-Resistance Vs. Temperature

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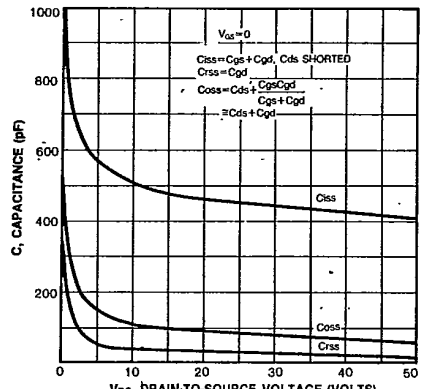
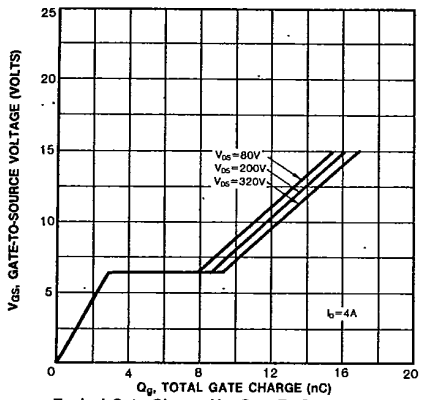
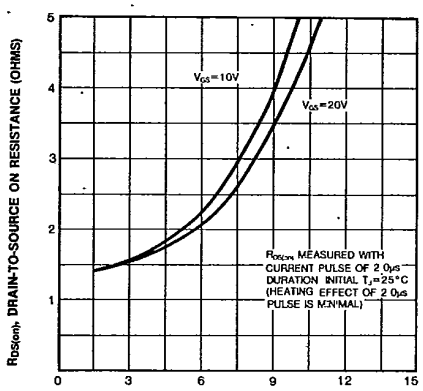


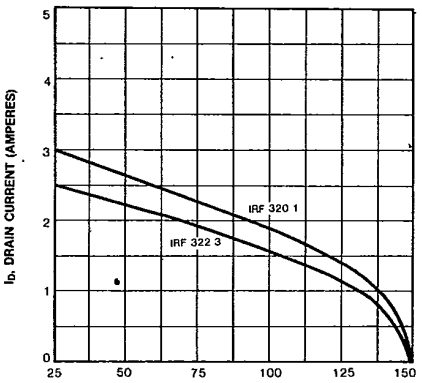
Typical Capacitance Vs. Drain to Source Voltage



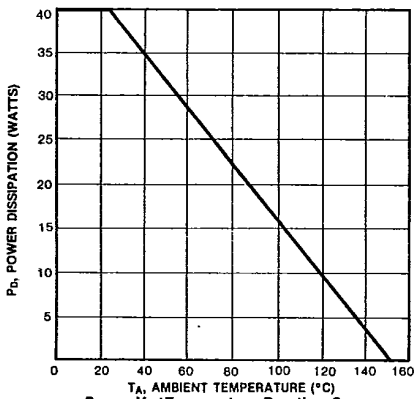
Typical Gate Charge Vs. Gate-To-Source Voltage



Typical On-Resistance Vs. Drain Current



Maximum Drain Current Vs. Case Temperature



Power Vs. Temperature Derating Curve

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