Ordering number: EN2256C

SANYO

No.2256C

LA4495, 4496

Monolithic Linear IC

Car Stereo-Use BTL-OCL 20W AF Power Amp

Features

- High output
- Low distortion
- Excellent ripple rejection
- Low residual noise
- Low pop noise at the time of power ON/OFF
- Small-sized package SEP-14HS (Zigzag pins)
- Pin compatible with LA4475, 4476

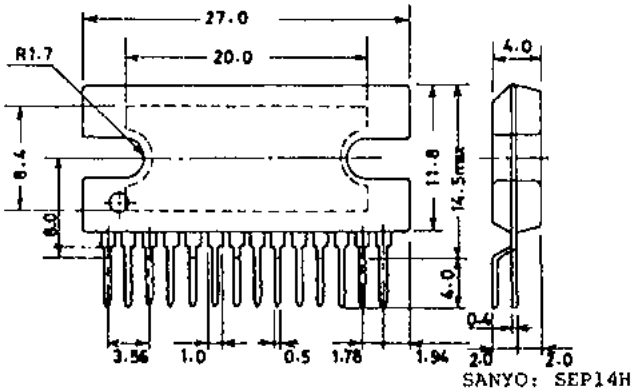
Functions

- Standby circuit (Standby current : 1μA typ.)
- Pop noise preventer (Starting time : 0.6 to 0.8sec.)
- Thermal shutdown protector
- Overvoltage/surge protector
- Output pin-to-GND short protector (with speaker protection)
- Output pin-to-V_{CC} short protector (with speaker protection)
- Load short protector

Maximum Ratings at Ta = 25°C

				unit
Maximum Supply Voltage	V_{CC} max	Quiescent, t = 30sec	26	V
	V_{CC} max	Quiescent	18	V
	V_{CC} max	Operating	16	V
Surge Supply Voltage	V_{CC} surge	t ≦ 0.2sec, single giant pulse rise time 1msec	50	V
Output Current	Io		4	A
Thermal Resistance	θj-c		3	°C/W
Junction Temperature	Tj		150	°C
Allowable Power Dissipation	Pd max		15	W
Operating Temperature	Topg		−20 to +75	°C
Storage Temperature	Tstg		−40 to +150	°C

Case Outline 3082-S14HIC
(unit : mm)



SANYO: SEP14H

Specifications and information herein are subject to change without notice.

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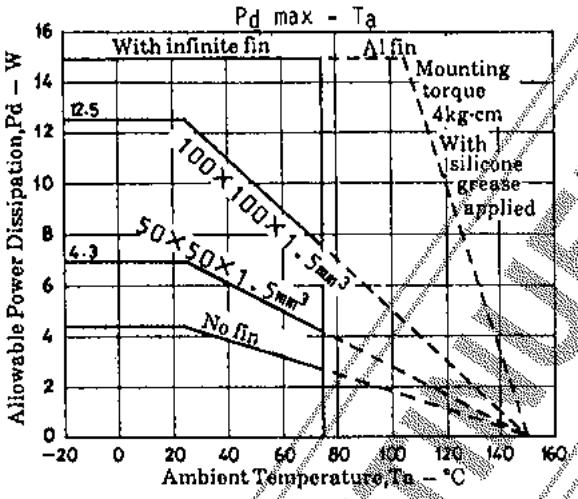
TOKYO OFFICE Tokyo Bldg., 1-10,1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

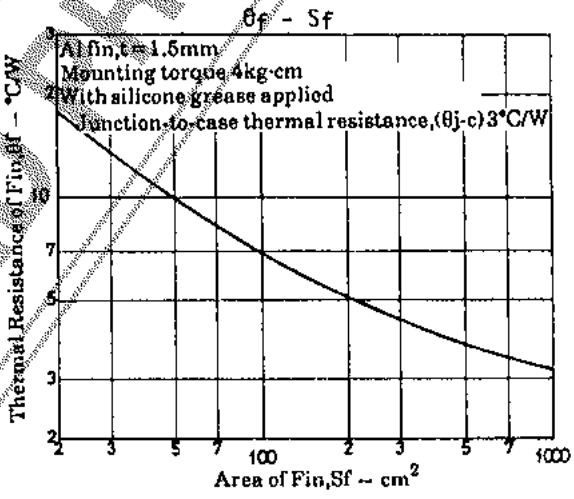
Operating Conditions at Ta = 25°C

			unit
Recommended Supply Voltage	V_{CC}	13.2	V
Recommended Load Resistance	R_L	4	Ω
Operating Voltage Range	V_{CC} op	9 to 16	V

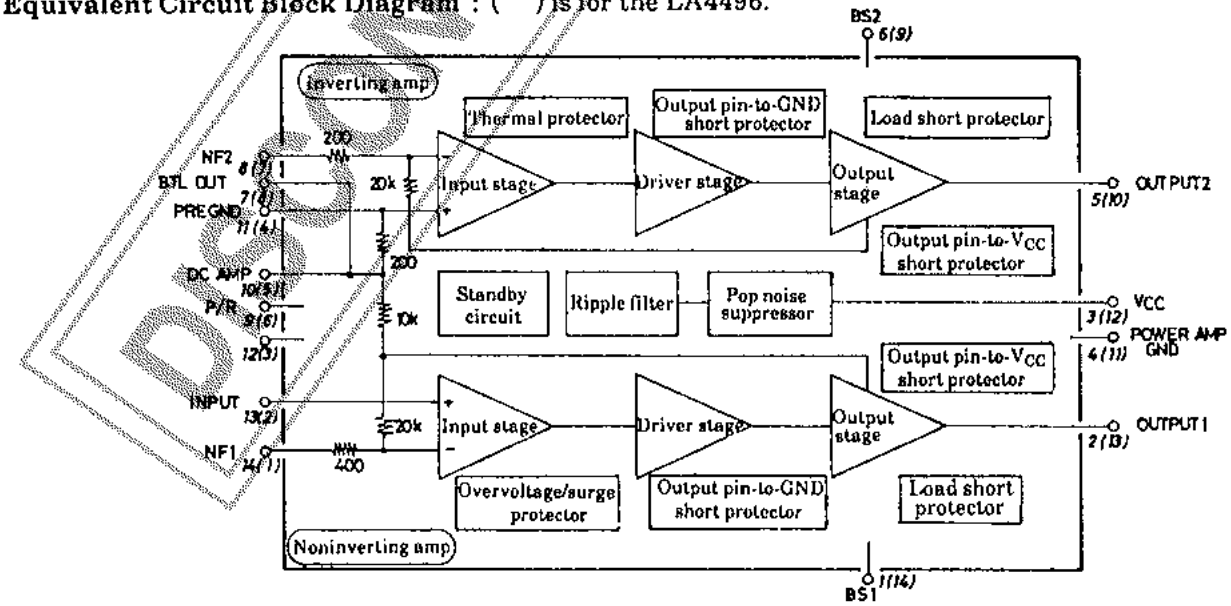
Operating Characteristics at Ta = 25°C, V_{CC} = 13.2V, R_L = 4Ω, f = 1kHz, Rg = 600Ω, with 100×100×1.5mm³ Al heat sink, See specified Test Circuit (standby switch ON).

			min	typ	max	unit
Quiescent Current	Icco		40	80	160	mA
Voltage Gain	VG		38	40	42	dB
Output Power	Po1	THD = 10%	16	20		W
	Po2	THD = 1%		15		W
Total Harmonic Distortion	THD	Po = 1W		0.06	0.3	%
Input Resistance	ri		20	30	40	kΩ
Output Noise Voltage	V_{NO1}	Rg = 0, B.P.F. = 20Hz to 20kHz		90	180	μV
	V_{NO2}	Rg = 10kΩ, B.P.F. = 20Hz to 20kHz		160	320	μV
Output Offset Voltage	Voff		−300		300	mV
Ripple Rejection	Rr	Rg = 0, V_R = 0dBm, f_R = 100Hz	45	55		dB
Standby Current	Ist			1.0	100	μA





Equivalent Circuit Block Diagram : ( ) is for the LA4496.



LA4495 No.	①	②	③	④	⑤	⑥	⑦	⑧	⑨	⑩	⑪	⑫	⑬	⑭
LA4496 No.	(14)	(13)	(12)	(11)	(10)	(9)	(8)	(7)	(6)	(5)	(4)	(3)	(2)	(1)
Pin Name	BS 1	OUT 1	V_{CC}	PWR G	OUT 2	BS 2	BTL OUT	NF 2	P/R	DC A	PRE G	DC B	IN	NF 1
Pin Voltage (V)	10.6	6.9	13.2	0	6.9	10.6	0.14	1.25	5.0	12.3	0	1.00	0.614	1.25