HITACHI

PM5075J

Silicon N-Channel Power MOS FET Module for High-Speed Power Switching

Features

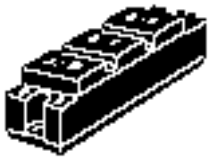
- Equipped with Power MOS FET
- Low on-resistance
- High speed switching
- Low drive current
- Wide area of safe operation
- Inherent parallel diode between source and drain
- Isolated base from Terminal
- Suitable for motor driver, switching regulator and etc.

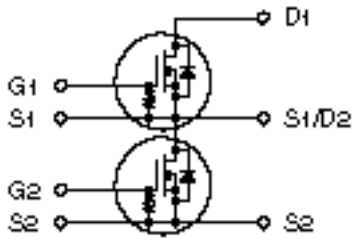
Absolute Maximum Ratings (Ta = 25°C) (Per FET chip)

Item	Symbol	Rating	Unit
Drain source voltage	$V_{(BR)DSS}$	500	V
Gate source voltage	$V_{(BR)GSS}$	±30	V
Drain current	I_D	75	A
Drain peak current	$I_{D(peak)}$	180	A
Body-drain diode reverse drain current	I_{DR}	75	A
Body-drain diode reverse peak current	$I_{DR(peak)}$	180	A
Channel dissipation	Pch*1	300	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–45 to +125	°C
Insulation dielectric	Viso*2	2000	Vrms

Notes: 1. Value at Ta = 25 °C
2. Base to terminals AC minute

Pin Arrangement





Symbol	Electrode	Terminals	Remarks
S1	Source 1	M5 screw	Power terminal
D1	Drain 1	M5 screw	Power terminal
S2	Source 2	M5 screw	Power terminal
D2	Drain 2	M5 screw	Power terminal
G1	Gate 1	# 110	Signal terminal
S1	Source 1	# 110	Signal terminal
G2	Gate 2	# 110	Signal terminal
S2	Source 2	# 110	Signal terminal

Electrical Characteristics (Ta = 25°C) (Per FET chip)

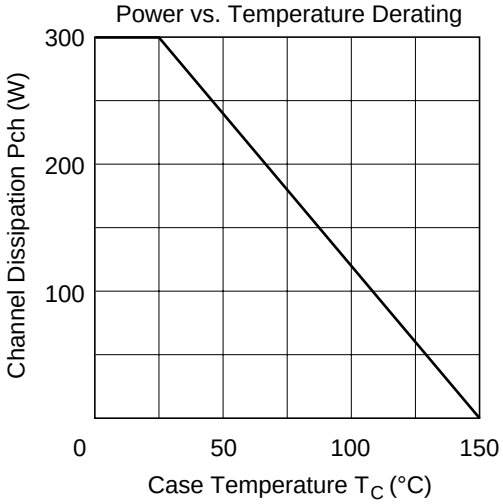
Item	Symbol	Min	Typ	Max	Unit	Test Condition
Drain-source breakdown voltage	$V_{(BR)DSS}$	500	—	—	V	I_D = 10 mA, V_{GS}= 0 V
Gate-source leak current	I_{GSS}	—	—	±10	µA	V_{GS} = ± 25 V, V_{DS} = 0 V
Gate-source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	I_G = ±100 µA, V_{DS} = 0 V
Drain leak current	I_{DSS}	—	—	500	µA	V_{DS} = 400 V, V_{GS} = 0 V
Gate-source threshold voltage	$V_{GS(th)}$	2.0	—	3.0	V	I_D = 1 mA, V_{DS} = 10 V
Drain-source saturation voltage	$V_{DS(on)}$	—	3.7	4.44	V	I_D = 37 A, V_{GS} = 10 V[*1]
Static Drain-source on state resistance	$R_{DS(on)}$	—	0.10	0.12	Ω	I_D = 37 A, V_{GS} = 10 V[*1]
Forward transfer admittance	$\|y_{fs}\|$	—	45	—	S	I_D = 37 A, V_{DS} = 10 V[*1]
Input capacitance	Ciss	—	9600	—	pF	V_{DS} = 10 V, V_{GS} = 0 V f = 1 MHz
Output capacitance	Coss	—	2300	—		
Reverse transfer capacitance	Crss	—	330	—		
Turn-on delay time	$t_{d(on)}$	—	100	—	ns	I_D = 37 A, V_{GS} = 10 V Rg = 50 Ω R_L = 1 Ω
Rise time	t_r	—	310	—		
Turn-off delay time	$t_{d(off)}$	—	550	—		
Fall time	t_f	—	135	—		
Body-drain diode forward voltage	V_{DF}	—	1.8	—	V	I_F = 75 A, V_{GS} = 0 V
Body-drain diode reverse recovery time	t_{rr}	—	130	—	ns	I_F = 75 A, V_{GS} = 0 V di/dt = 100 A/µs

Note: 1. Pulse Test

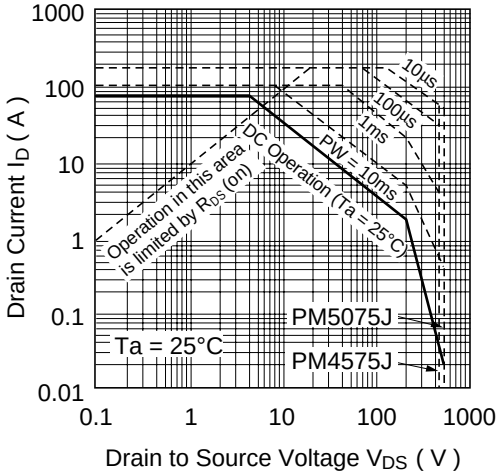
Mechanical characteristics

Item	Symbol	Condition	Rating	Unit
Fixing strength	—	Mounting into main-terminal with M4 screw	1.45 to 1.95	N-m
	—	Mounting into heat sink with M5 screw	1.95 to 2.9	N-m
Weight	—	Typical value	200	g

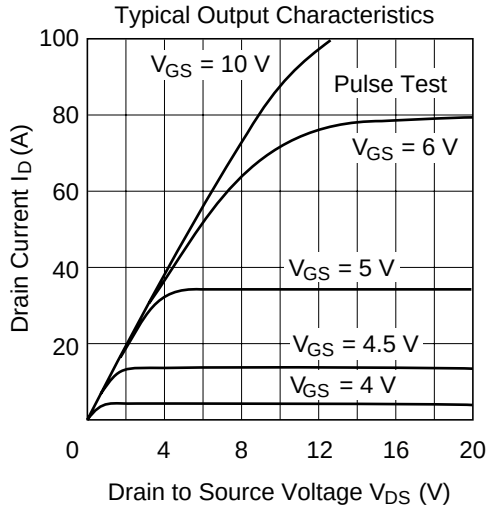
Power vs. Temperature Derating



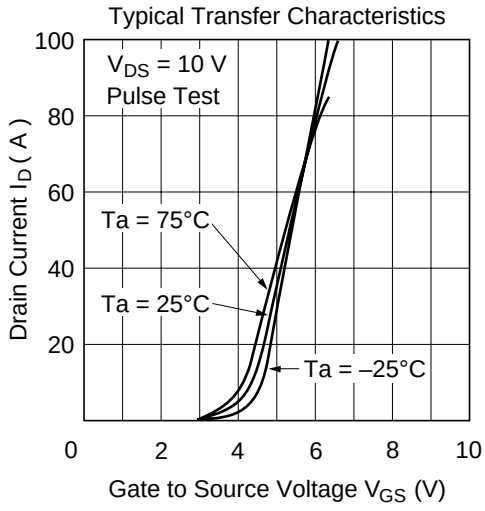
Maximum Safe Operation Area

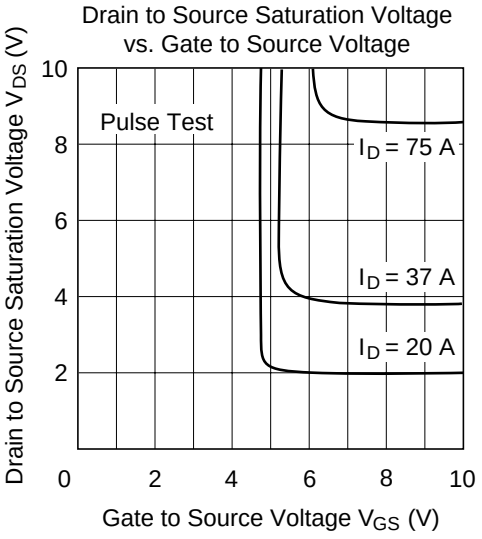


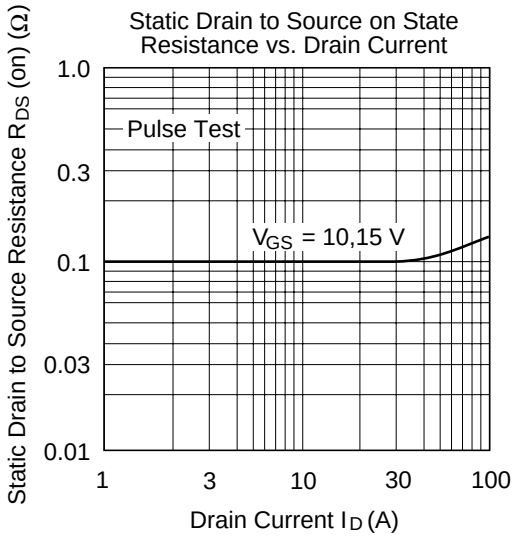
Typical Output Characteristics

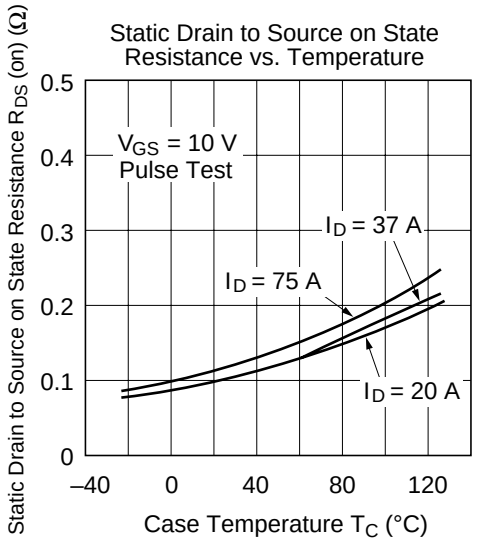


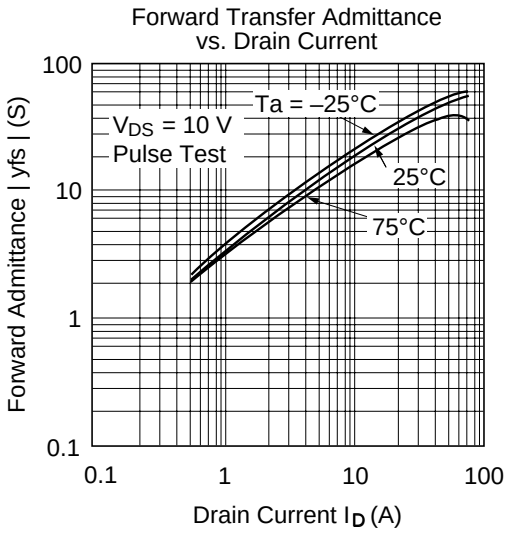
Typical Transfer Characteristics



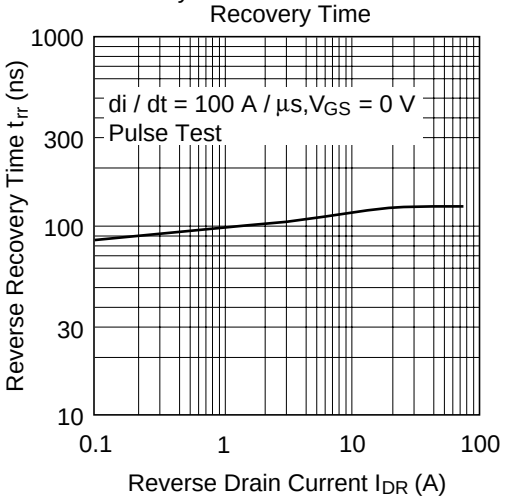

Drain to Source Saturation Voltage
vs. Gate to Source Voltage
Drain to Source Saturation Voltage V_{DS} (V)
Pulse Test
I_D = 75 A
I_D = 37 A
I_D = 20 A
10
8
6
4
2
0
2
4
6
8
10
Gate to Source Voltage V_{GS} (V)


Static Drain to Source on State
Resistance vs. Drain Current
Static Drain to Source Resistance R_{DS} (on) (Ω)
Pulse Test
V_{GS} = 10,15 V
1.0
0.3
0.1
0.03
0.01
1
3
10
30
100
Drain Current I_D (A)

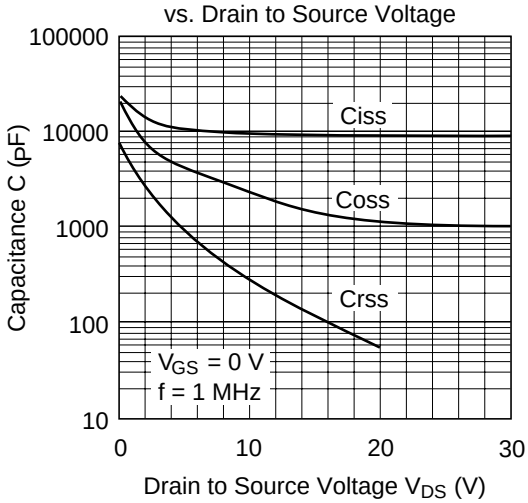

Static Drain to Source on State
Resistance vs. Temperature
Static Drain to Source on State Resistance R_{DS} (on) (Ω)
V_{GS} = 10 V
Pulse Test
I_D = 37 A
I_D = 75 A
I_D = 20 A
0.5
0.4
0.3
0.2
0.1
0
–40
0
40
80
120
Case Temperature T_C (°C)


Forward Transfer Admittance
vs. Drain Current
Forward Admittance | yfs | (S)
Ta = –25°C
V_{DS} = 10 V
Pulse Test
25°C
75°C
100
10
1
0.1
0.1
1
10
100
Drain Current I_D (A)

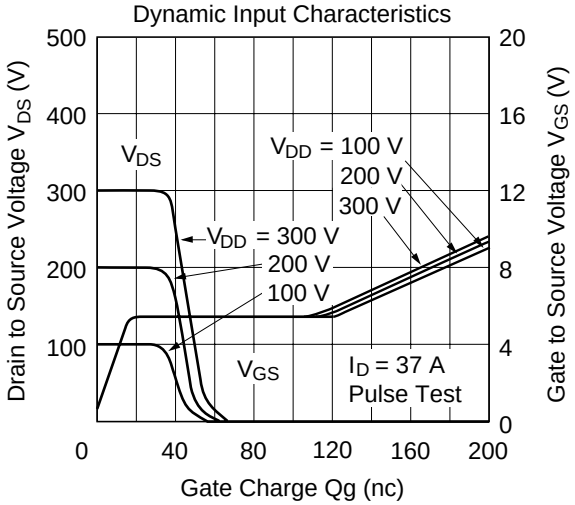
Body to Drain Diode Reverse Recovery Time



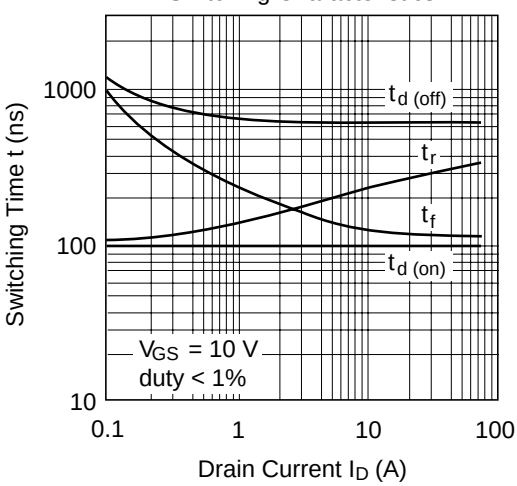
Typical Capacitance vs. Drain to Source Voltage

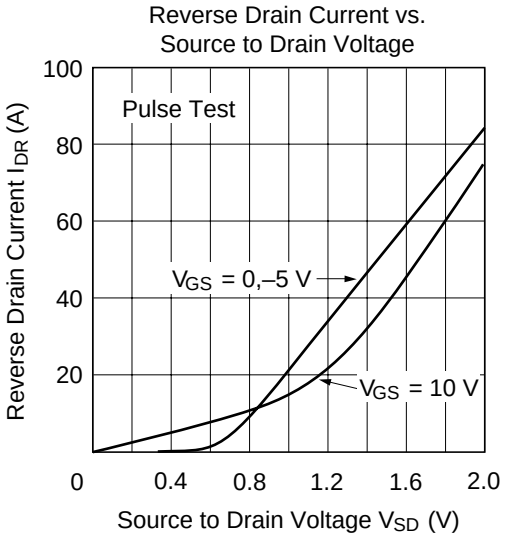


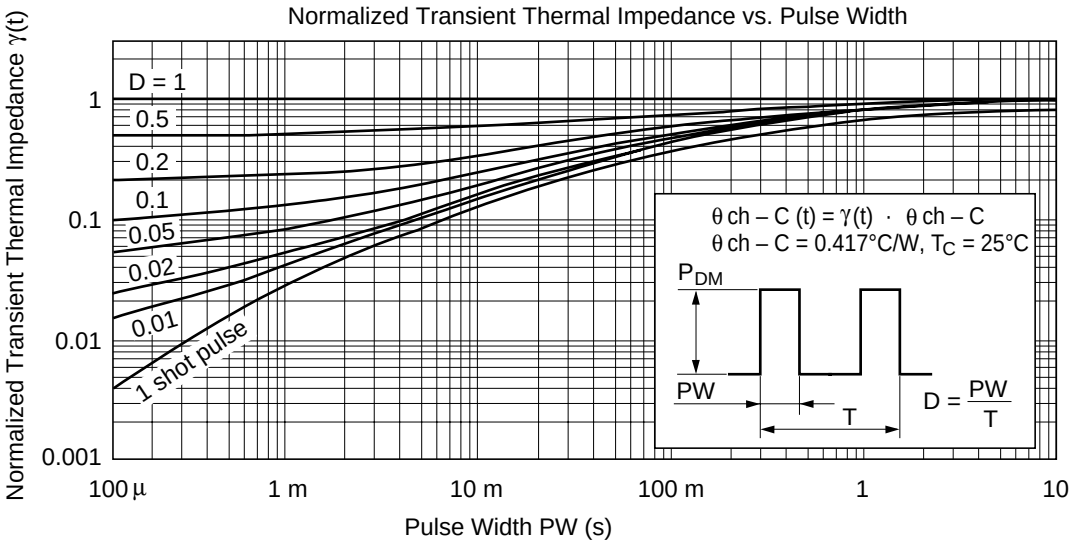
Dynamic Input Characteristics



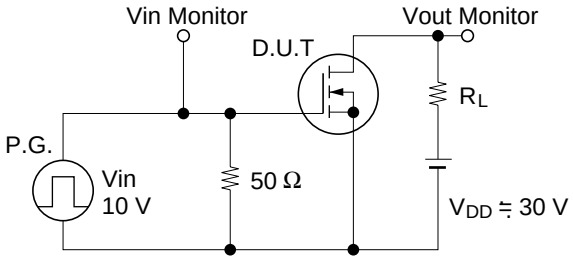
Switching Characteristics



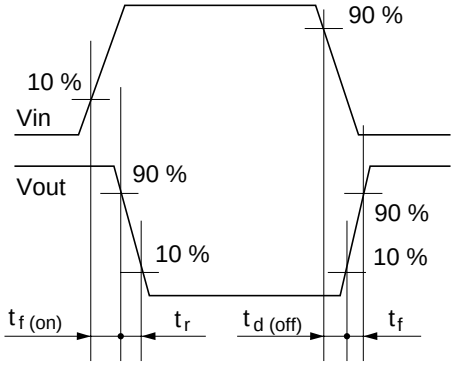

Reverse Drain Current vs.
Source to Drain Voltage
Pulse Test
V_{GS} = 0,–5 V
V_{GS} = 10 V
Reverse Drain Current I_{DR} (A)
Source to Drain Voltage V_{SD} (V)
100
80
60
40
20
0
0.4
0.8
1.2
1.6
2.0


Normalized Transient Thermal Impedance vs. Pulse Width
Normalized Transient Thermal Impedance γ(t)
D = 1
0.5
0.2
0.1
0.05
0.02
0.01
1 shot pulse
θch – C (t) = γ(t) · θch – C
θch – C = 0.417°C/W, T_C = 25°C
P_{DM}
PW
T
D = PW/T
1
0.1
0.01
0.001
100 µ
1 m
10 m
100 m
1
10
Pulse Width PW (s)

Switching Time Test Circuit



Wave Forms



Package Dimensions

Unit: mm