

AN6397, AN6397S

VTR SECAM Color Signal Processing Circuits

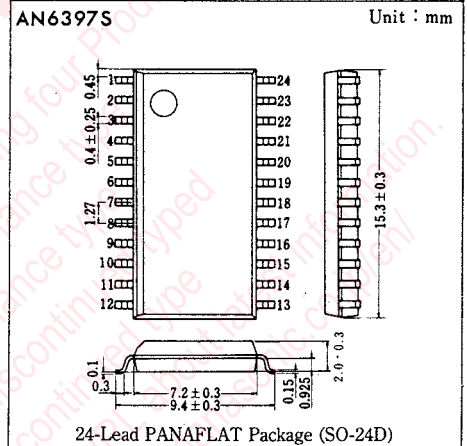
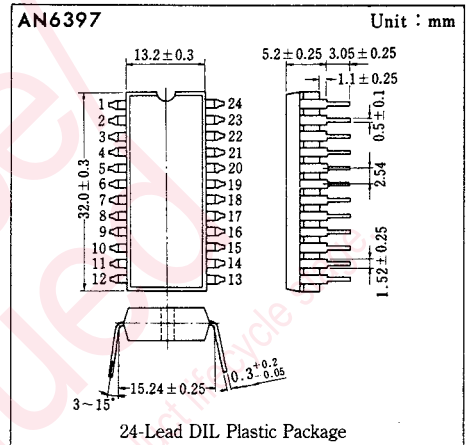
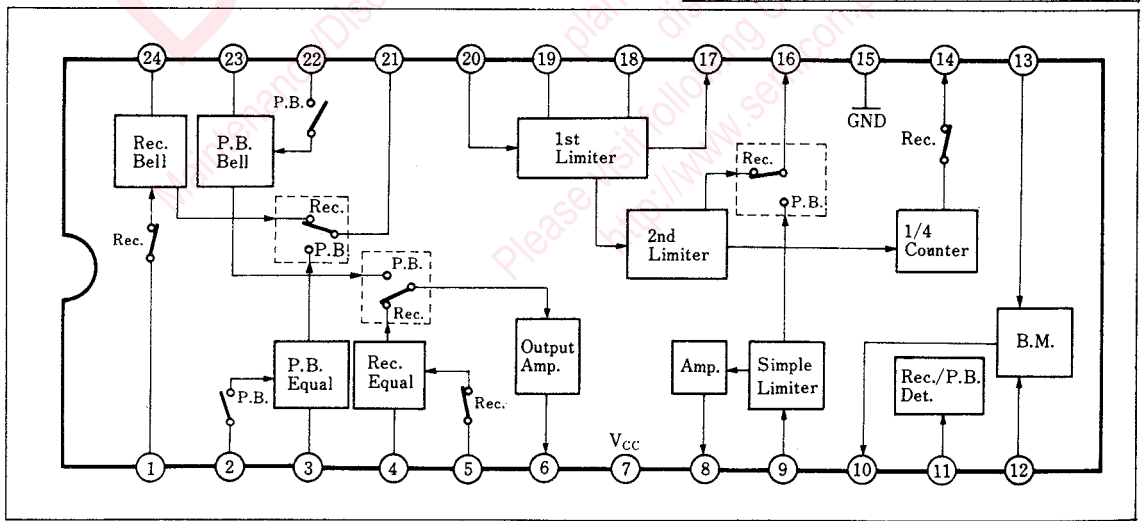
■ Outline

The AN6397 and the AN6397S are integrated circuits designed for VTR SECAM color signal processing and constitute a VTR SECAM-system color signal processing circuit with the AN6398 or the AN6398S.

■ Features

- The functions consist of :
 - Bell filter circuit
 - Equalizer circuit
 - Limiter circuit
 - 1/4 Frequency divider
 - Balanced modulator
- Supply voltage : 5V

■ Block Diagram



■ Pin

Pin No.	Pin Name	Pin No.	Pin Name
1	Rec. Chroma Input	13	Carrier Input
2	PB Chroma Input	14	1/4 Divider Output
3	Equal Filter	15	GND
4	Equal Filter	16	Chroma Output(2)
5	Rec. Eq. Input	17	1st Limiter Output
6	Chroma Output(1)	18	Limiter
7	V _{cc}	19	Limiter
8	Amp. Output	20	1st Limiter Input
9	Simple Limiter Input	21	Rec. Bell P.B. Equal Output
10	B.M. Output	22	P.B. Bell Input
11	Rec. V _{cc} Input	23	Bell Filter
12	Signal Input	24	Bell Filter

■ Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Rating	Unit
Supply voltage	V _{CC}	6	V
Power dissipation (Ta=70°C)	AN6397	350	mW
	AN6397S	270 *	
Operating ambient temperature	T _{opr}	-20 ~ +70	°C
Storage temperature	T _{stg}	-40 ~ +125	°C

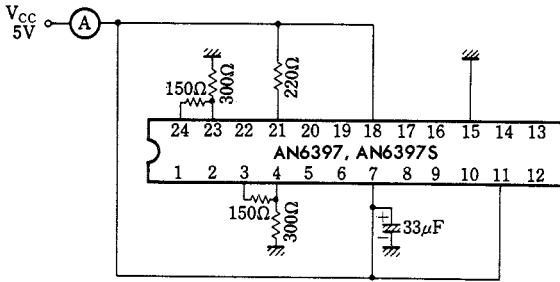
*Indicates a package capability.

■ Electrical Characteristics (V_{CC}=5V, Ta=25°C±2°C)

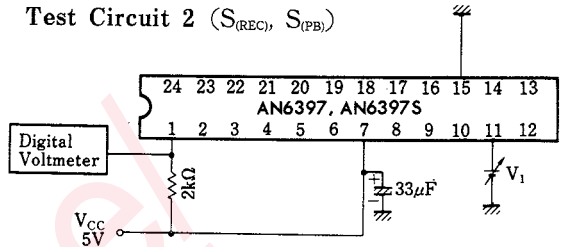
Item	Symbol	Test Circuit	Condition	min.	typ.	max.	Unit
Total circuit current	I _{tot}	1	In recording	30		62	mA
Rec. mode select level	S _(REC)	2		4		5	V
PB mode select level	S _(PB)	2		0		0.5	V
Rec. Bell output amplitude	v _{O-21-R}	3	Pin ① input 150mV _{p-p}	600		920	mV _{P-P}
Rec. Equal output amplitude	v _{O-6-R}	4	Pin ⑤ input 200mV _{p-p}	350		680	mV _{P-P}
PB Equal output amplitude	v _{O-21-P}	5	Pin ② input 150mV _{p-p}	420		700	mV _{P-P}
PB Bell output amplitude	v _{O-6-P}	6	Pin ② input 200mV _{p-p}	280		600	mV _{P-P}
1st limiter output amplitude	v _{O-17}	7	Pin ⑩ input 200mV _{p-p}	500		1000	mV _{P-P}
2nd limiter output amplitude	v _{O-16-R}	7	Pin ⑩ input 200mV _{p-p}	450		900	mV _{P-P}
1/4 counter output amplitude	v _{O-14}	7	Pin ⑩ input 200mV _{p-p}	900		1500	mV _{P-P}
Simple limiter output amplitude	v _{O-16-P}	8	Pin ⑨ input 400mV _{p-p}	450		900	mV _{P-P}
BM output amplitude	v _{O-10}	9	Pin ⑩ input 100mV _{p-p}	580		1050	mV _{P-P}
BM carrier leak	CL ₁₀	9	Pin ⑩ input 100mV _{p-p}			-22	dB

Note) Operating supply voltage range V_{cc(oper)}=4.5~5.5V

Test Circuit 1 (I_{tot})

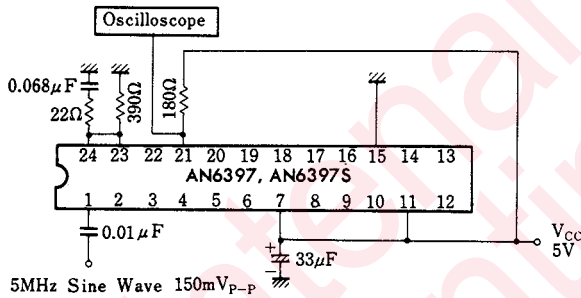


Test Circuit 2 ($S_{(REC)}$, $S_{(PB)}$)

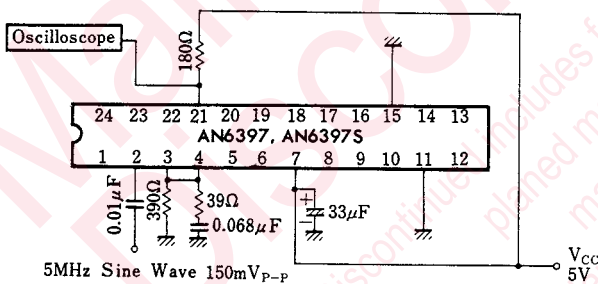


Notes) V (REC): When a Pin ① voltage is changed. Pin ① voltage when a Pin ① voltage is lowered to about 4.3V.
 V (PB): When the Pin ① voltage is changed. Pin ① voltage when the Pin ① voltage becomes equal to Vcc (5V)

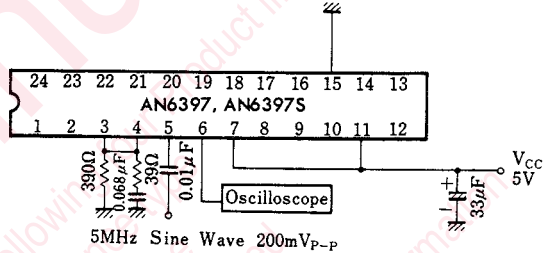
Test Circuit 3 (v_{0-21-R})



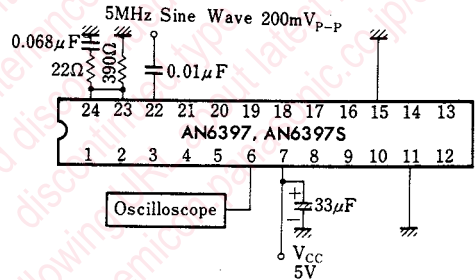
Test Circuit 5 (v_{0-21-P})



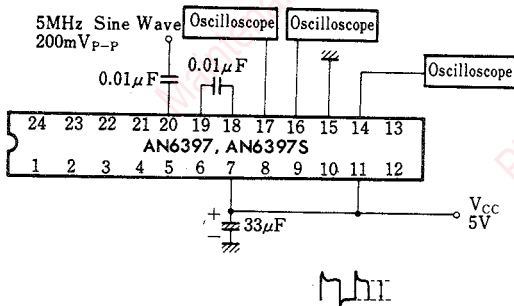
Test Circuit 4 (v_{0-6-R})



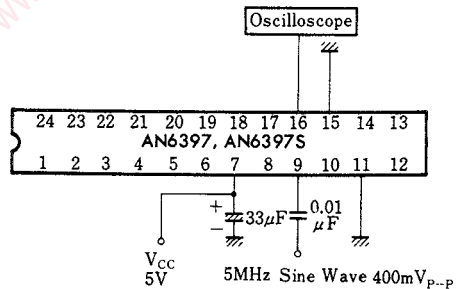
Test Circuit 6 (v_{0-6-P})



Test Circuit 7 (v_{0-17} , v_{0-16-R} , v_{0-14})



Test Circuit 8 (v_{0-16-P})



Note) As shown in the figure, an amplitude value shall not include overshoot.

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