

TOSHIBA PHOTOCOUPLER GaAlAs IRED & PHOTO-IC

TLP114A

DIGITAL LOGIC ISOLATION.

LINE RECEIVER.

POWER SUPPLY CONTROL FEEDBACK CONTROL.

SWITCHING POWER SUPPLY.

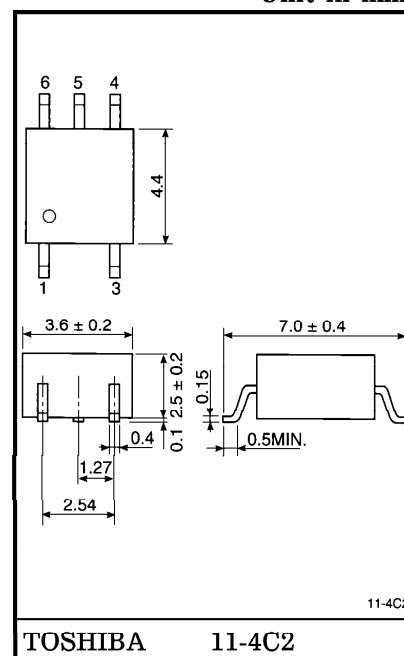
TRANSISTOR INVERTOR.

The TOSHIBA MINI FLAT COUPLER TLP114A is a small outline coupler, suitable for surface mount assembly.

TLP114A consists of a high output power GaAlAs light emitting diode, optically coupled to a high speed detector of one chip photodiode-transistor.

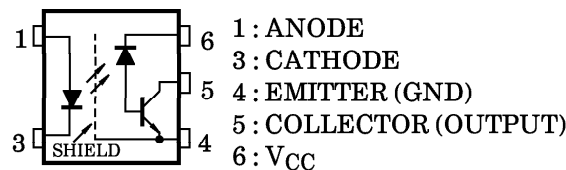
- Isolation Voltage : 3750 Vrms (Min.)
- Switching Speed : $t_{pHL}=0.8\mu s$, $t_{pLH}=0.8\mu s$, (Max.)
($R_L=1.9k\Omega$)
- TTL Compatible
- UL Recognized : UL1577, File No. E67349

Unit in mm

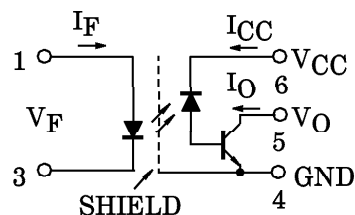


Weight : 0.09g

PIN CONFIGURATION (TOP VIEW)



SCHEMATIC



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MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
L D E	Forward Current (Note 1)	I _F	20	mA
	Pulse Forward Current (Note 2)	I _{FP}	40	mA
	Peak Transient Forward Current (Note 3)	I _{FPT}	1	A
	Reverse Voltage	V _R	5	V
D E T E C T O R	Output Current	I _O	8	mA
	Peak Output Current	I _{OP}	16	mA
	Supply Voltage	V _{CC}	−0.5~30	V
	Output Voltage	V _O	−0.5~20	V
	Output Power Dissipation (Note 4)	P _O	100	mW
Operating Temperature Range		T _{opr}	−55~100	°C
Storage Temperature Range		T _{stg}	−55~125	°C
Lead Solder Temperature (10 sec.)		T _{sol}	260	°C
Isolation Voltage (AC, 1 min., R.H. ≤ 60%) (Note 5)		BV _S	3750	V _{rms}

(Note 1) Derate 0.36mA/°C above 70°C.

(Note 2) 50% duty cycle, lms pulse width.

Derate 0.72mA/°C above 70°C.

(Note 3) Pulse width ≤ 1μs, 300pps.

(Note 4) Derate 1.8mW/°C above 70°C.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
LDE	Forward Voltage	V _F	I _F = 16mA	1.22	1.42	1.72	V
	Forward Voltage Temperature Coefficient	$\Delta V_F / \Delta T_a$	I _F = 16mA	—	—2	—	mV / °C
	Reverse Current	I _R	V _R = 3V	—	—	10	μA
	Capacitance Between Terminals	C _T	V _F = 0, f = 1MHz	—	30	—	pF
DETECTOR	High Level Output Current	I _{OH} (1)	I _F = 0mA, V _{CC} = V _O = 5.5V	—	3	500	nA
		I _{OH} (2)	I _F = 0mA, V _{CC} = 30V V _O = 20V	—	—	5	μA
		I _{OH}	I _F = 0mA, V _{CC} = 30V V _O = 20V, Ta = 70°C	—	—	50	
	High Level Supply Current	I _{CCH}	I _F = 0mA, V _{CC} = 30V	—	0.01	1	μA
COUPLED	Current Transfer Ratio	I _O / I _F	I _F = 16mA, V _{CC} = 4.5V V _O = 0.4V	20	—	—	%
	Low Level Output Voltage	V _{OL}	I _F = 16mA, V _{CC} = 4.5V I _O = 2.4mA	—	—	0.4	V
	Isolation Resistance	R _S	R.H. ≤ 60%, V _S = 500V (Note 5)	5 × 10 ¹⁰	10 ¹⁴	—	Ω
	Stray Capacitance Between Input to Output	C _S	V _S = 0, f = 1MHz (Note 5)	—	0.8	—	pF

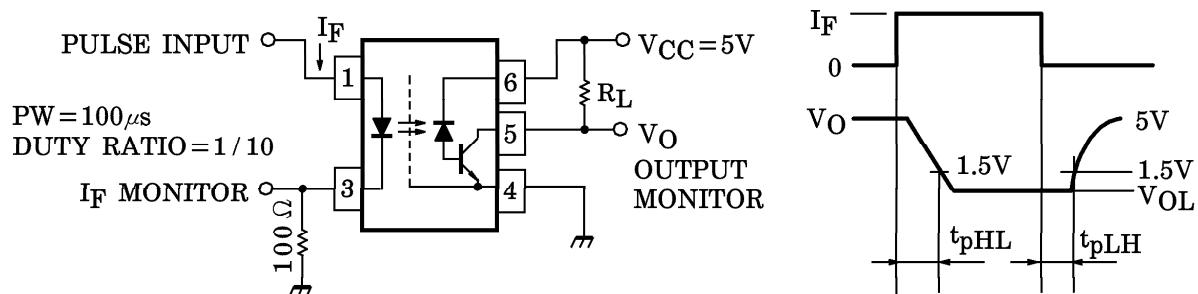
SWITCHING CHARACTERISTICS (Ta = 25°C, V_{CC} = 5V)

CHARACTERISTIC	SYMBOL	TEST CIR - CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Propagation Delay Time (H→L)	t _{pHL}	1	I _F = 0→16mA V _{CC} = 5V, R _L = 1.9kΩ	—	—	0.8	μs
Propagation Delay Time (L→H)	t _{pLH}	1	I _F = 16→0mA V _{CC} = 5V, R _L = 1.9kΩ	—	—	0.8	μs
Common Mode Transient Immunity at High Output Level	C _{MH}	2	I _F = 0mA V _{CM} = 400Vp-p R _L = 4.1kΩ	5000	10000	—	V / μs
Common Mode Transient Immunity at Low Output Level	C _{ML}	2	I _F = 16mA V _{CM} = 400Vp-p R _L = 4.1kΩ	—5000	—10000	—	V / μs

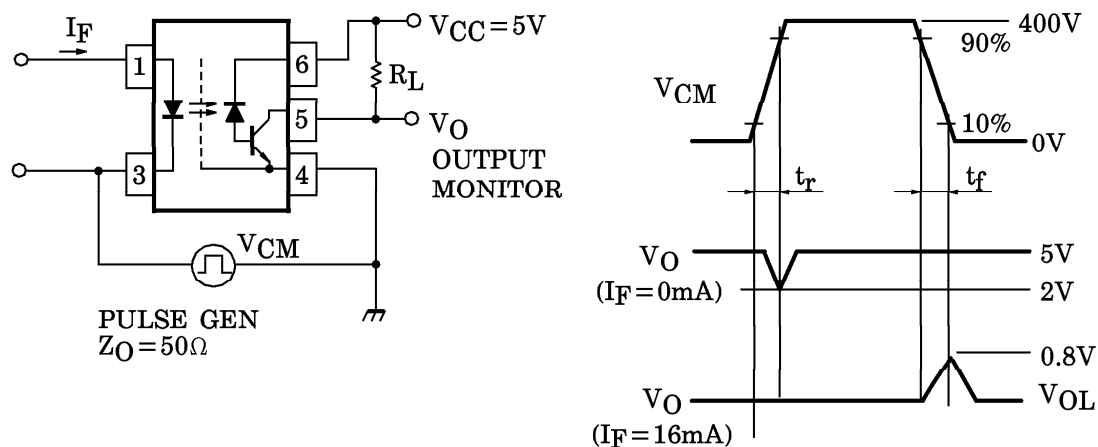
(Note 5) Device considered a two-terminal device: Pins 1 and 3 shorted together, and pins 4, 5 and 6 shorted together.

(Note 6) Maximum electrostatic discharge voltage for any pins: 100V (C=200pF, R=0)

TEST CIRCUIT 1 : Switching Time Test Circuit



TEST CIRCUIT 2 : Common Mode Transient Immunity Test Circuit



$$CM_H = \frac{320 (V)}{t_r (\mu s)} , \quad CM_L = \frac{320 (V)}{t_r (\mu s)}$$

