

2SJ388 L, 2SJ388 S

Silicon P Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 2.5 V Gate drive device can be driven from 3 V Source
- Suitable for Switching regulator, DC – DC converter

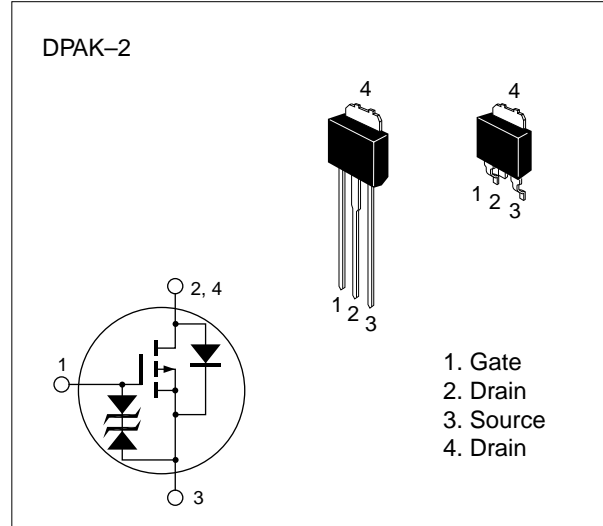


Table 1 Absolute Maximum Ratings (Ta = 25°C)

| Item | Symbol | Ratings | Unit |
|--|-------------------------|-------------|------|
| Drain to source voltage | V _{DSS} | -30 | V |
| Gate to source voltage | V _{GSS} | ±20 | V |
| Drain current | I _D | -10 | A |
| Drain peak current | I _{D(pulse)} * | -40 | A |
| Body-drain diode reverse drain current | I _{DR} | -10 | A |
| Channel dissipation | P _{ch} ** | 20 | W |
| Channel temperature | T _{ch} | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

* PW ≤ 10 μs, duty cycle ≤ 1 %

** Value at T_c = 25°C

Table 2 Electrical Characteristics (Ta = 25°C)

| Item | Symbol | Min | Typ | Max | Unit | Test conditions |
|--|---------------|----------|------|----------|---------------|--|
| Drain to source breakdown voltage | $V_{(BR)DSS}$ | -30 | — | — | V | $I_D = -10 \text{ mA}$, $V_{GS} = 0$ |
| Gate to source breakdown voltage | $V_{(BR)GSS}$ | ± 20 | — | — | V | $I_G = \pm 100 \text{ }\mu\text{A}$, $V_{DS} = 0$ |
| Gate to source leak current | I_{GSS} | — | — | ± 10 | μA | $V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$ |
| Zero gate voltage drain current | I_{DSS} | — | — | -100 | μA | $V_{DS} = -25 \text{ V}$, $V_{GS} = 0$ |
| Gate to source cutoff voltage | $V_{GS(off)}$ | -0.5 | — | -1.5 | V | $I_D = -1 \text{ mA}$, $V_{DS} = -10 \text{ V}$ |
| Static drain to source on state resistance | $R_{DS(on)}$ | — | 0.06 | 0.08 | Ω | $I_D = -5 \text{ A}$ $V_{GS} = -10 \text{ V}^*$ |
| | | — | 0.12 | 0.2 | Ω | $I_D = -5 \text{ A}$ $V_{GS} = -2.5 \text{ V}^*$ |
| Forward transfer admittance | $ y_{fs} $ | 4.5 | 8 | — | S | $I_D = -5 \text{ A}$ $V_{DS} = -10 \text{ V}^*$ |
| Input capacitance | C_{iss} | — | 970 | — | pF | $V_{DS} = -10 \text{ V}$ |
| Output capacitance | C_{oss} | — | 620 | — | pF | $V_{GS} = 0$ |
| Reverse transfer capacitance | C_{rss} | — | 250 | — | pF | $f = 1 \text{ MHz}$ |
| Turn-on delay time | $t_{d(on)}$ | — | 10 | — | ns | $I_D = -5 \text{ A}$ |
| Rise time | t_r | — | 65 | — | ns | $V_{GS} = -10 \text{ V}$ |
| Turn-off delay time | $t_{d(off)}$ | — | 250 | — | ns | $R_L = 6 \text{ }\Omega$ |
| Fall time | t_f | — | 240 | — | ns | |
| Body-drain diode forward voltage | V_{DF} | — | -1.0 | — | V | $I_F = -10 \text{ A}$, $V_{GS} = 0$ |
| Body-drain diode reverse recovery time | t_{rr} | — | 85 | — | μs | $I_F = -10 \text{ A}$, $V_{GS} = 0$, $di_F / dt = 20 \text{ A} / \mu\text{s}$ |

* Pulse Test

