

2SJ410

Preliminary

Silicon P Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter and motor driver

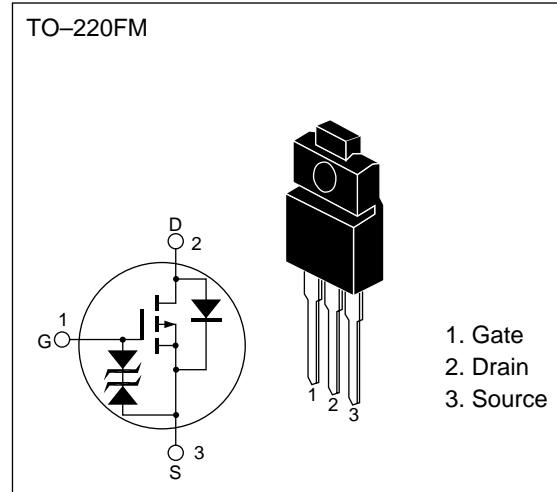


Table 1 Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-200	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	-6	A
Drain peak current	$I_{D(pulse)^*}$	-24	A
Body-drain diode reverse drain current	I_{DR}	-6	A
Channel dissipation	P_{ch}^{**}	-30	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

* $PW \leq 10 \mu s$, duty cycle $\leq 1 \%$

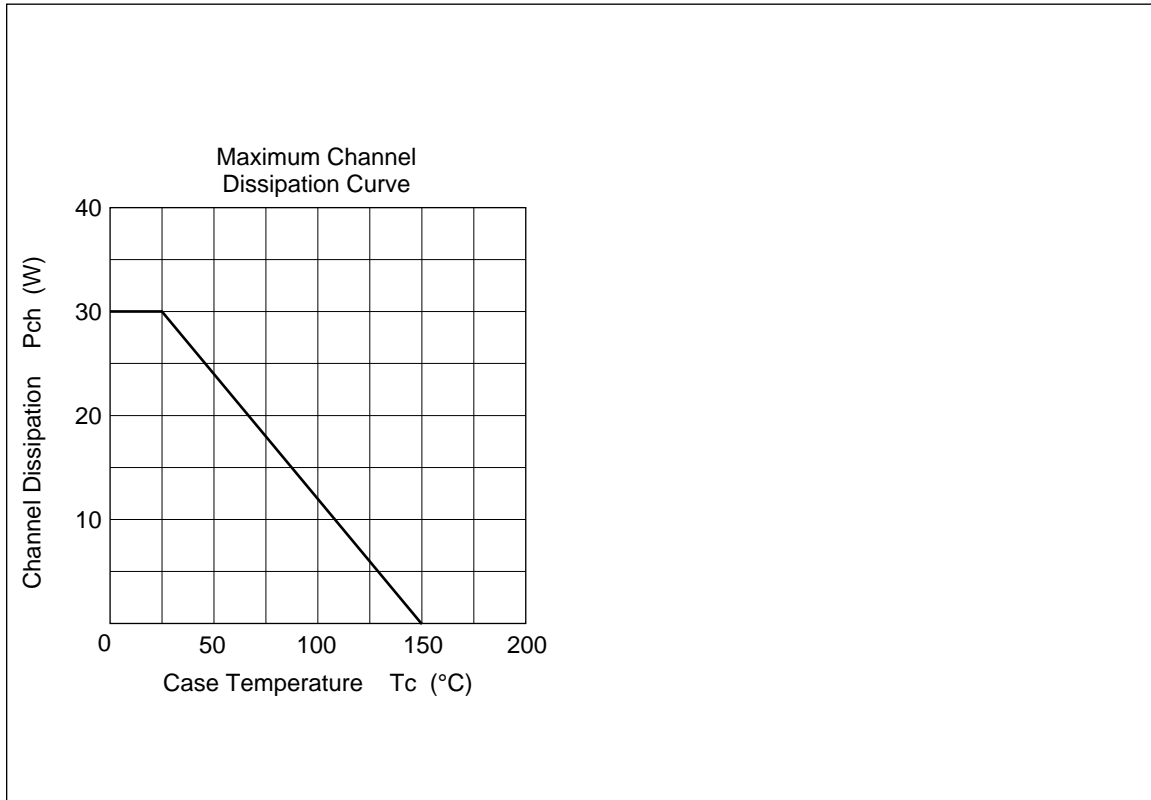
** Value at $T_c = 25 \text{ }^\circ\text{C}$

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Table 2 Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-200	—	—	V	$I_D = -10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	-250	μA	$V_{DS} = -160 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-2.0	—	-4.0	V	$I_D = -1 \text{ mA}$, $V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.7	0.9	Ω	$I_D = -3 \text{ A}$ $V_{GS} = -10 \text{ V}^*$
Forward transfer admittance	$ y_{fs} $	2.0	3.5	—	S	$I_D = -3 \text{ A}$ $V_{DS} = -10 \text{ V}^*$
Input capacitance	C_{iss}	—	(920)	—	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	C_{oss}	—	(190)	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	(70)	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	(17)	—	ns	$I_D = -3 \text{ A}$
Rise time	t_r	—	(40)	—	ns	$V_{GS} = -10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	(85)	—	ns	$R_L = 6\Omega$
Fall time	t_f	—	(45)	—	ns	
Body-drain diode forward voltage	V_{DF}	—	-1.0	—	V	$I_F = -6 \text{ A}$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	(170)	—	ns	$I_F = -6 \text{ A}$, $V_{GS} = 0$, $diF / dt = 50 \text{ A} / \mu\text{s}$

* Pulse Test



Package Dimensions

Unit : mm

