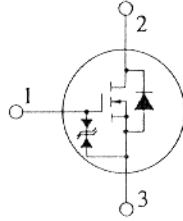


## 2SK1402, 2SK1402A

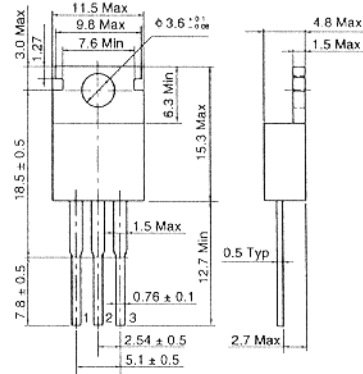
### SILICON N-CHANNEL MOS FET HIGH SPEED POWER SWITCHING

#### ■ FEATURES

- Low On-Resistance
- High Speed Switching
- Low Drive Current
- No Secondary Breakdown
- Suitable for Switching Regulator and DC-DC Converter



1. Gate  
2. Drain (Flange)  
3. Source  
(Dimensions in mm)



(JEDEC TO-220AB)

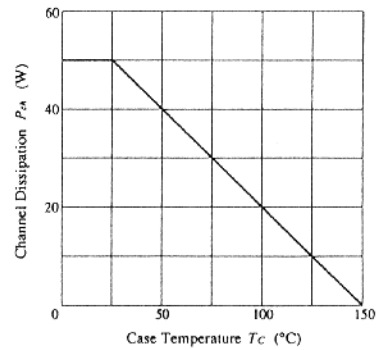
#### ■ ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Item	Symbol	K1402	K1402A	Unit
Drain-Source Voltage	$V_{DSS}$	600	650	V
Gate-Source Voltage	$V_{GSS}$	± 30		V
Drain Current	$I_D$	4		A
Drain Peak Current	$I_{D(pulse)}$ *	16		A
Body-Drain Diode Reverse Drain Current	$I_{DR}$	4		A
Channel Dissipation	$P_{ch}$ **	50		W
Channel Temperature	$T_{ch}$	150		°C
Storage Temperature	$T_{stg}$	-55 ~ +150		°C

\* PW ≤ 10 μs, duty cycle ≤ 1%

\*\* Value at Tc = 25°C

#### POWER VS. TEMPERATURE DERATING

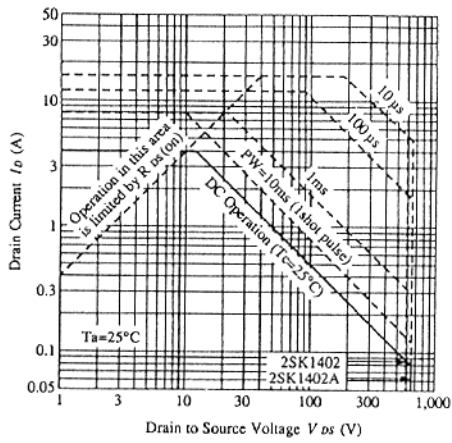


#### ■ ELECTRICAL CHARACTERISTICS (Ta = 25°C)

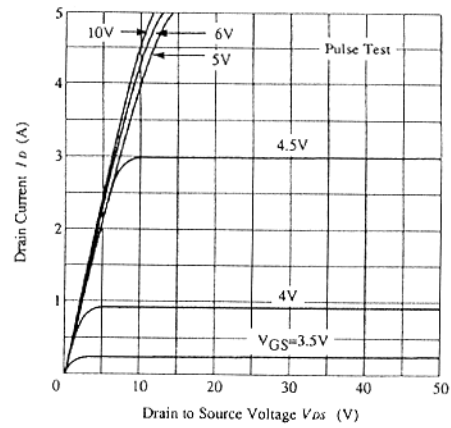
Item	Symbol	Test Condition	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	K1402	$I_D = 10\text{mA}, V_{GS} = 0$	600	—	—	V
	K1402A		650	—	—	V
Gate-Source Breakdown Voltage	$V_{(RR)GSS}$	$I_G = \pm 100\ \mu\text{A}, V_{DS} = 0$	± 30	—	—	V
Gate-Source Leak Current	$I_{GSS}$	$V_{GS} = \pm 25\text{V}, V_{DS} = 0$	—	—	± 10	μA
Zero Gate Voltage Drain Current	K1402	$V_{DS} = 500\text{V}, V_{GS} = 0$ $V_{DS} = 550\text{V}, V_{GS} = 0$	—	—	250	μA
	K1402A		—	—	—	μA
Gate-Source Cutoff Voltage	$V_{GS(off)}$	$I_D = 1\text{mA}, V_{DS} = 10\text{V}$	2.0	—	3.0	V
Static Drain-Source on State Resistance	K1402	$I_D = 2\text{A}, V_{GS} = 10\text{V}^*$	—	1.8	2.4	Ω
	K1402A		—	2.0	2.6	Ω
Forward Transfer Admittance	$ y_{fs} $	$I_D = 2\text{A}, V_{DS} = 10\text{V}^*$	2.2	3.5	—	S
Input Capacitance	$C_{iss}$	$V_{DS} = 10\text{V}, V_{GS} = 0, f = 1\text{MHz}$	—	600	—	pF
Output Capacitance	$C_{oss}$		—	140	—	pF
Reverse Transfer Capacitance	$C_{rss}$		—	25	—	pF
Turn-On Delay Time	$t_{d(on)}$	$I_D = 2\text{A}, V_{GS} = 10\text{V}, R_L = 15\ \Omega$	—	8	—	ns
Rise Time	$t_r$		—	30	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	60	—	ns
Fall Time	$t_f$		—	35	—	ns
Body-Drain Diode Forward Voltage	$V_{DF}$	$I_F = 4\text{A}, V_{GS} = 0$	—	0.9	—	V
Body-Drain Diode Reverse Recovery Time	$t_{rr}$	$I_F = 4\text{A}, V_{GS} = 0, di_F/dt = 100\text{A}/\mu\text{s}$	—	300	—	ns

\* Pulse Test

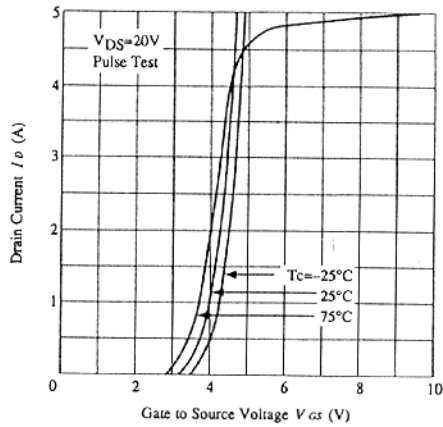
### MAXIMUM SAFE OPERATION AREA



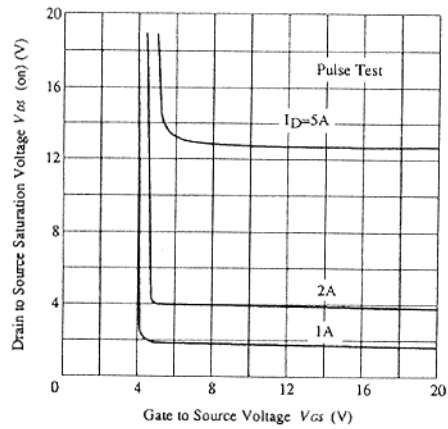
### TYPICAL OUTPUT CHARACTERISTICS



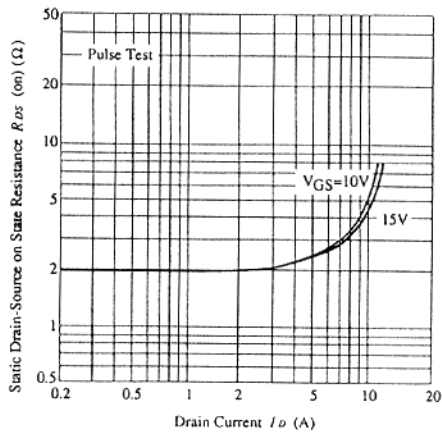
### TYPICAL TRANSFER CHARACTERISTICS



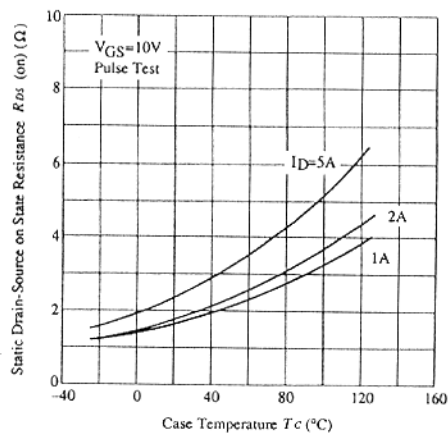
### DRAIN-SOURCE SATURATION VOLTAGE VS. GATE-SOURCE VOLTAGE



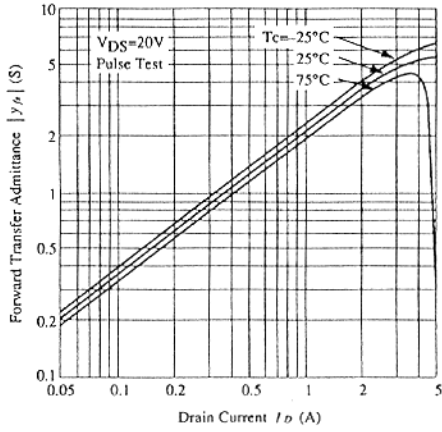
### STATIC DRAIN-SOURCE ON STATE RESISTANCE VS. DRAIN CURRENT



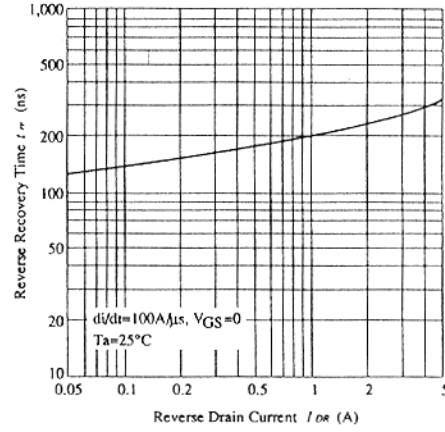
### STATIC DRAIN-SOURCE ON STATE RESISTANCE VS. TEMPERATURE



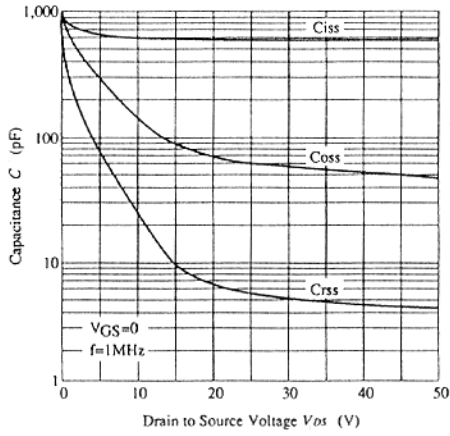
**FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT**



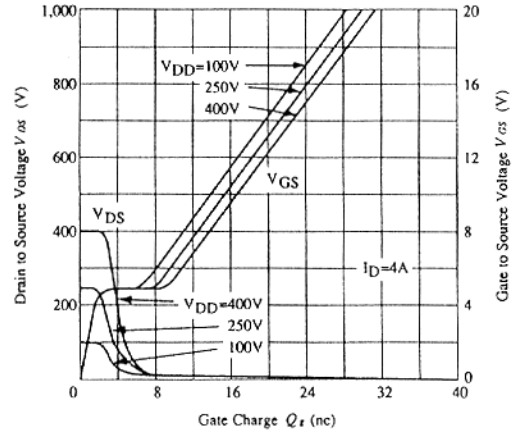
**BODY-DRAIN DIODE REVERSE RECOVERY TIME**



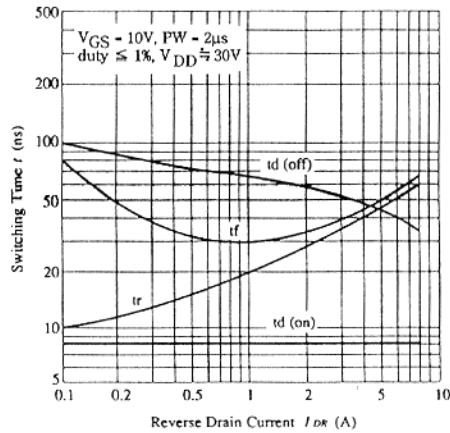
**TYPICAL CAPACITANCE VS. DRAIN-SOURCE VOLTAGE**



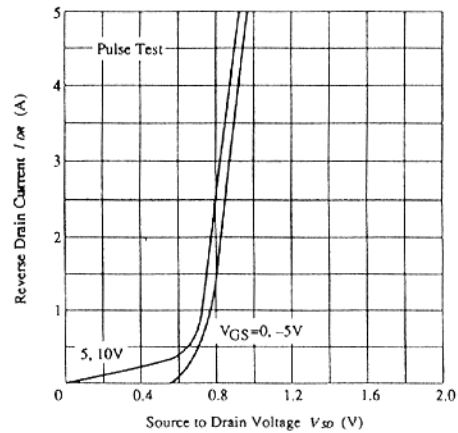
**DYNAMIC INPUT CHARACTERISTICS**



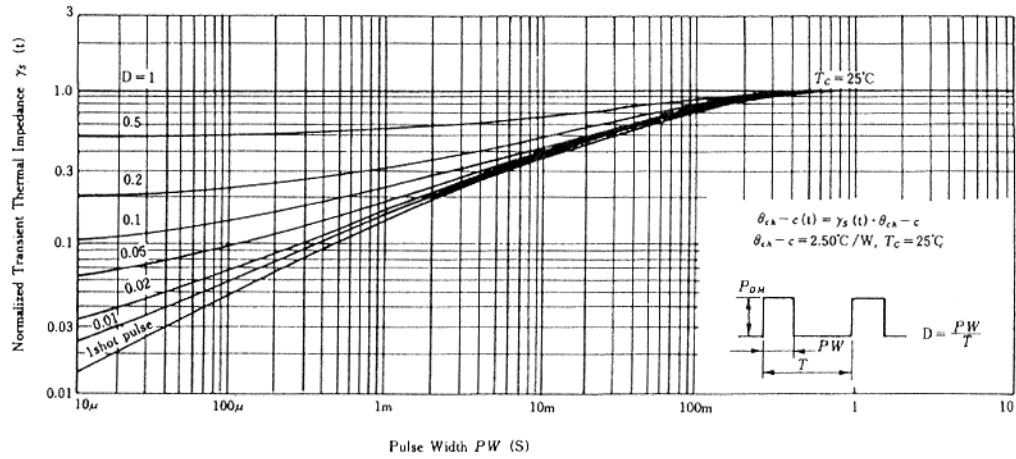
**SWITCHING CHARACTERISTICS**



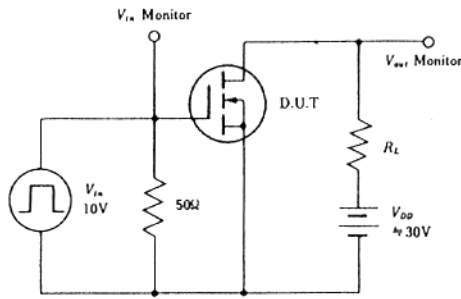
**REVERSE DRAIN CURRENT VS. SOURCE TO DRAIN VOLTAGE**



## NORMALIZED TRANSIENT THERMAL IMPEDANCE VS. PULSE WIDTH



## SWITCHING TIME TEST CIRCUIT



## WAVEFORMS

