

## 2SK1968

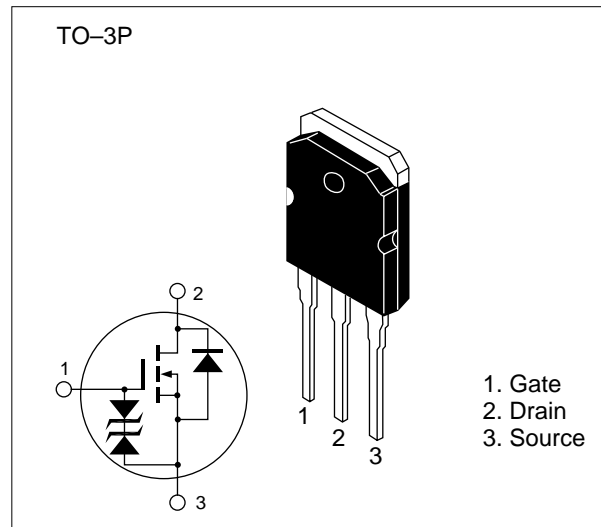
### Silicon N Channel MOS FET

#### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- No secondary breakdown
- Suitable for Switching regulator
- Low drive current



**Table 1 Absolute Maximum Ratings** ( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	600	V
Gate to source voltage	$V_{GSS}$	$\pm 30$	V
Drain current	$I_D$	12	A
Drain peak current	$I_{D(\text{pulse})}^*$	48	A
Body-drain diode reverse drain current	$I_{DR}$	12	A
Channel dissipation	$P_{ch}^{**}$	100	W
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

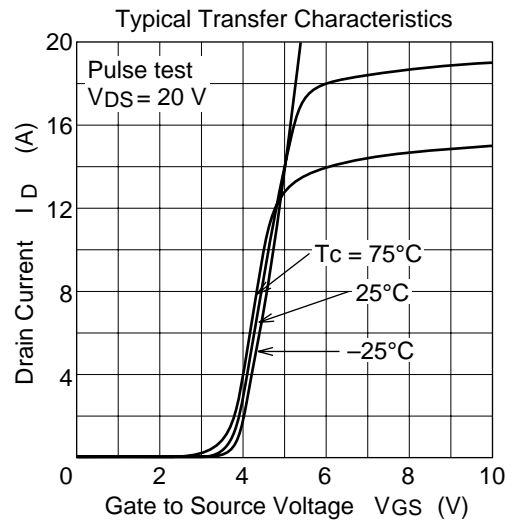
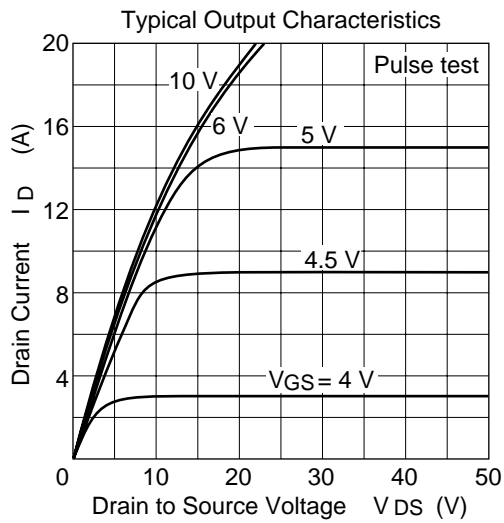
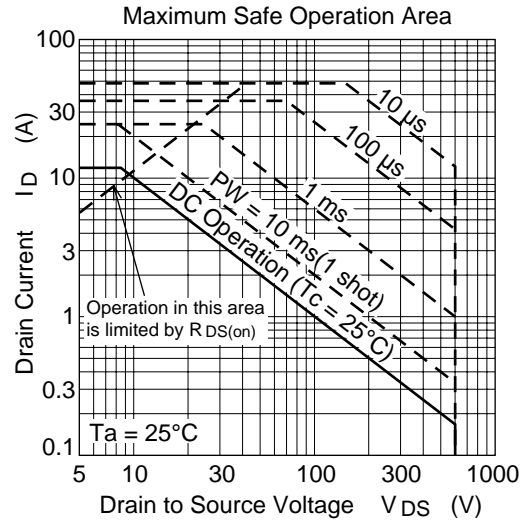
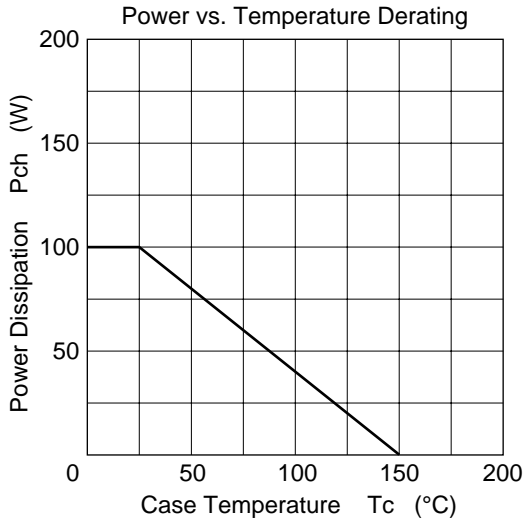
\*  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$

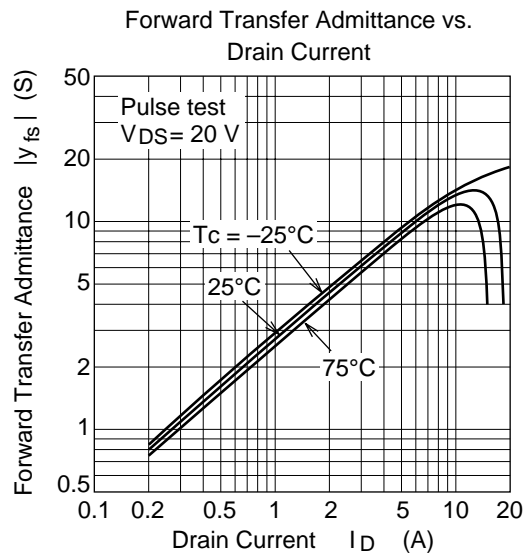
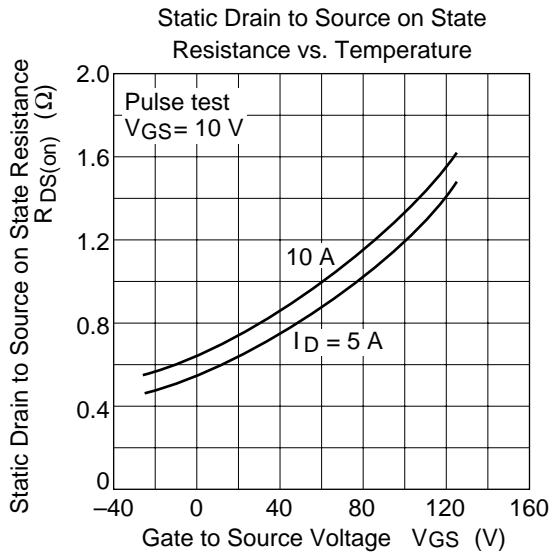
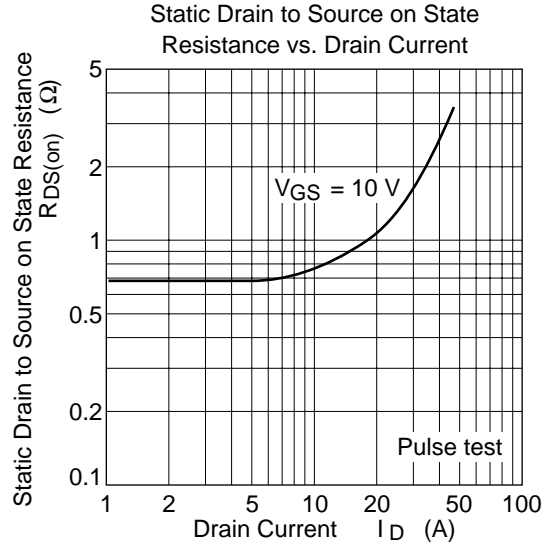
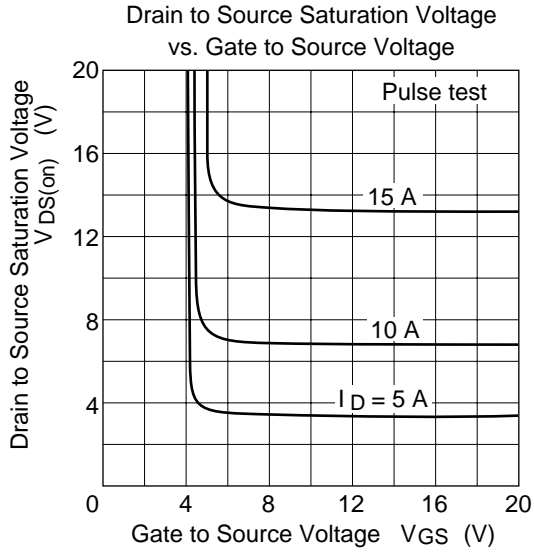
\*\* Value at  $T_c = 25^\circ\text{C}$

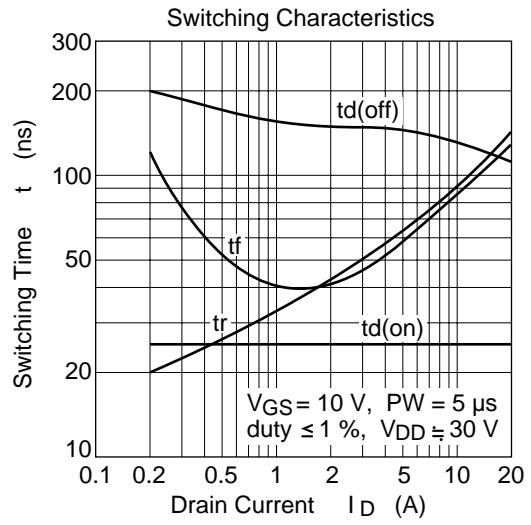
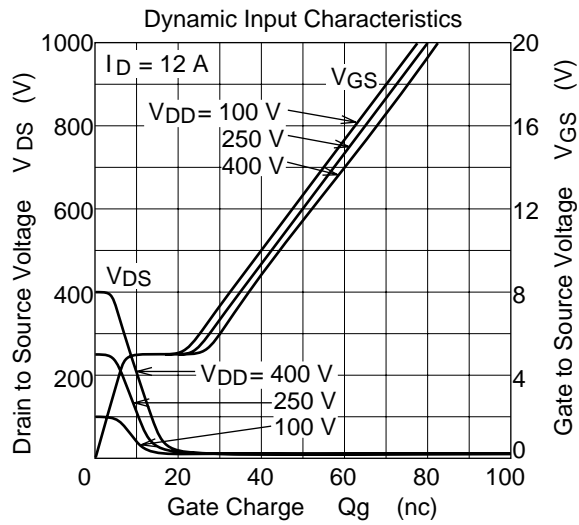
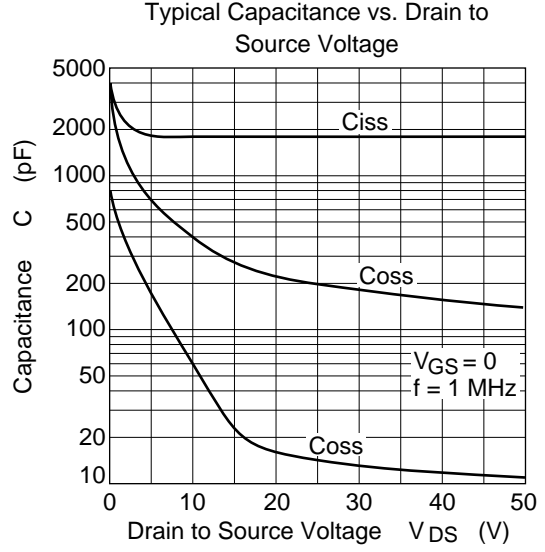
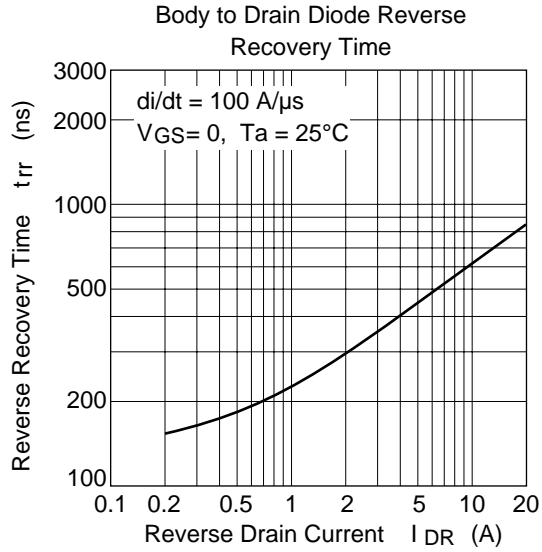
**Table 2 Electrical Characteristics** (Ta = 25°C)

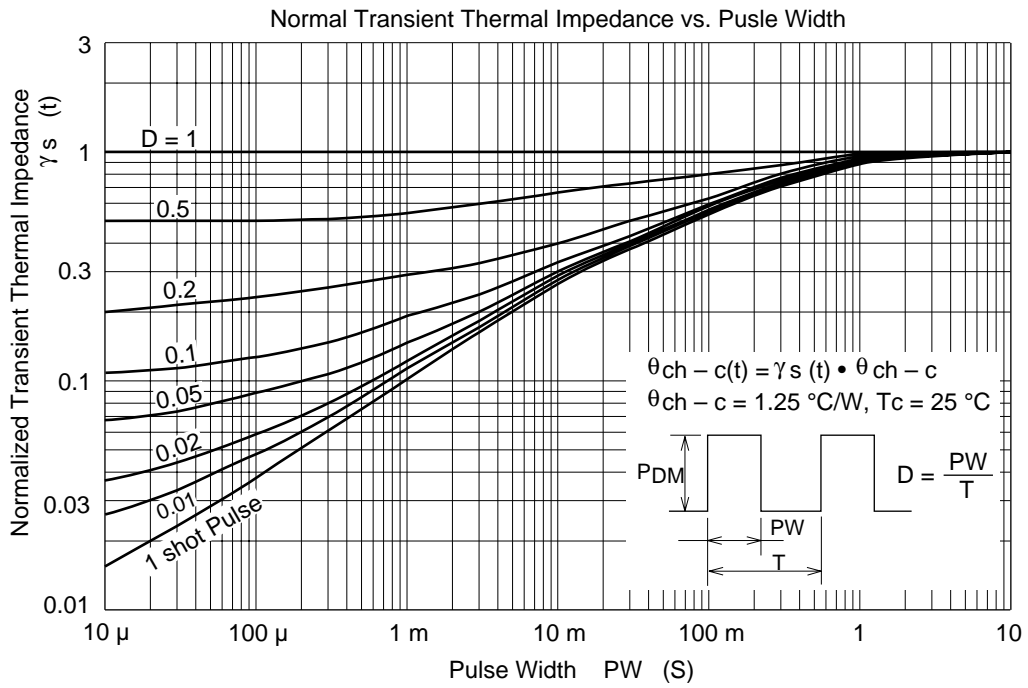
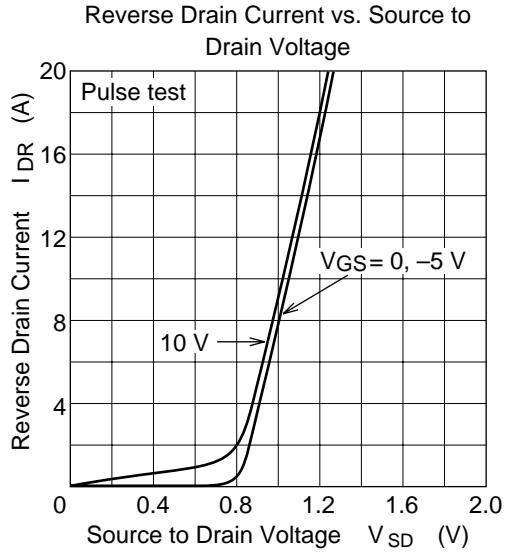
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	$I_G = \pm 100 \text{ } \mu\text{A}$ , $V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	±10	μA	$V_{GS} = \pm 25 \text{ V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	250	μA	$V_{DS} = 500 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	3.0	V	$I_D = 1 \text{ mA}$ , $V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.68	0.88	Ω	$I_D = 6 \text{ A}$ $V_{GS} = 10 \text{ V}^*$
Forward transfer admittance	$ y_{fs} $	5	10	—	S	$I_D = 6 \text{ A}$ $V_{DS} = 10 \text{ V}^*$
Input capacitance	$C_{iss}$	—	1800	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	$C_{oss}$	—	400	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	60	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	25	—	ns	$I_D = 6 \text{ A}$
Rise time	$t_r$	—	70	—	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	145	—	ns	$R_L = 5 \text{ } \Omega$
Fall time	$t_f$	—	65	—	ns	
Body-drain diode forward voltage	$V_{DF}$	—	1.1	—	V	$I_F = 12 \text{ A}$ , $V_{GS} = 0$
Body-drain diode reverse recovery time	$t_{rr}$	—	670	—	ns	$I_F = 12 \text{ A}$ , $V_{GS} = 0$ , $diF / dt = 100 \text{ A} / \mu\text{s}$

\* Pulse Test









Switching Time Test Circuit and Waveform

