

SPECIFICATION

DEVICE NAME : Power MOSFET

TYPE NAME : 2SK2695-01

SPEC. No. :

Fuji Electric Co.,Ltd.

This Specification is subject to change without notice.

	DATE	NAME	APPROVED	Fuji Electric Co.,Ltd	
DRAWN				DWG.NO.	1/2
CHECKED					

1. Scope
This specifies Fuji power MOSFET 2SK2695-01
2. Construction N-channel enhancement mode power MOSFET
3. Application for switching
4. Outview TO-220 Outview See to 5/12 page
5. Absolute maximum ratings at $T_c=25^\circ\text{C}$ (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-source voltage	V_{DS}	700	V	
Continuous Drain current	I_D	± 5	A	
Pulsed drain current	$I_{Dpulsec}$	± 20	A	
Gate-source voltage	V_{GS}	± 30	V	
Avalanche energy	E_{AS}	222.8	mJ	$L=16.3\text{mH}$, $V_{CC}=70\text{V}$
Maximum power dissipation	P_D	60	W	
Operating and storage temperature range	T_{ch}	150	$^\circ\text{C}$	
	T_{stg}	$-55 \sim +150$	$^\circ\text{C}$	

6. Electrical characteristics at $T_c=25^\circ\text{C}$ (unless otherwise specified)
Static ratings

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Drain-source breakdown voltage	BV_{DSS}	$I_D = 1\text{mA}$ $V_{GS} = 0\text{V}$	700			V
Gate threshold voltage	$V_{GS(th)}$	$I_D = 1\text{mA}$ $V_{DS} = V_{GS}$	2.5	3.0	3.5	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 700\text{V}$ $V_{GS} = 0\text{V}$	$T_{ch} = 25^\circ\text{C}$			μA
	I_{DSS}		$T_{ch} = 125^\circ\text{C}$			mA
Gate-source leakage current	I_{GSS}	$V_{GS} = \pm 30\text{V}$ $V_{DS} = 0\text{V}$		10	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D = 2.5\text{A}$ $V_{GS} = 10\text{V}$		1.40	1.85	Ω

Fuji Electric Co., Ltd.

DWG. NO.

2/12

Dynamic ratings

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Forward transconductance	g_{fs}	$I_D = 2.5A$ $V_{DS} = 25V$	2.0	4.0		S
Input capacitance	C_{iss}	$V_{DS} = 25V$ $V_{GS} = 0V$ $f = 1MHz$		820	1230	pF
Output capacitance	C_{oss}			120	180	pF
Reverse transfer capacitance	C_{rss}			65	100	pF
Turn-on time	$t_{d(on)}$	$V_{CC} = 500V$ $V_{GS} = 10V$ $I_D = 5A$ $R_{GS} = 10\Omega$		20	30	ns
	t_r			45	70	ns
Turn-off time	$t_{d(off)}$			80	120	ns
	t_f			45	70	ns

Reverse diode

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Avalanche capability	I_{AV}	$L = 100\mu H$, $T_{ch} = 25^\circ C$ * See Fig1 and 2	5			A
Diode forward on-voltage	V_{SD}	$I_F = 2 \times I_{DR}$ $V_{GS} = 0V$, $T_{ch} = 25^\circ C$		0.93	1.40	V
Reverse recovery time	t_{rr}	$I_F = I_{DR}$ $V_{GS} = 0V$ $-di_F/dt = 100A/\mu s$ $T_{ch} = 25^\circ C$		610		ns
Reverse recovery charge	Q_{rr}				5.5	

7. Thermal resistance

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Thermal resistance	$R_{th_{ch-c}}$				2.08	$^\circ C/W$
	$R_{th_{ch-a}}$				75.0	$^\circ C/W$

Fig.1 Test circuit

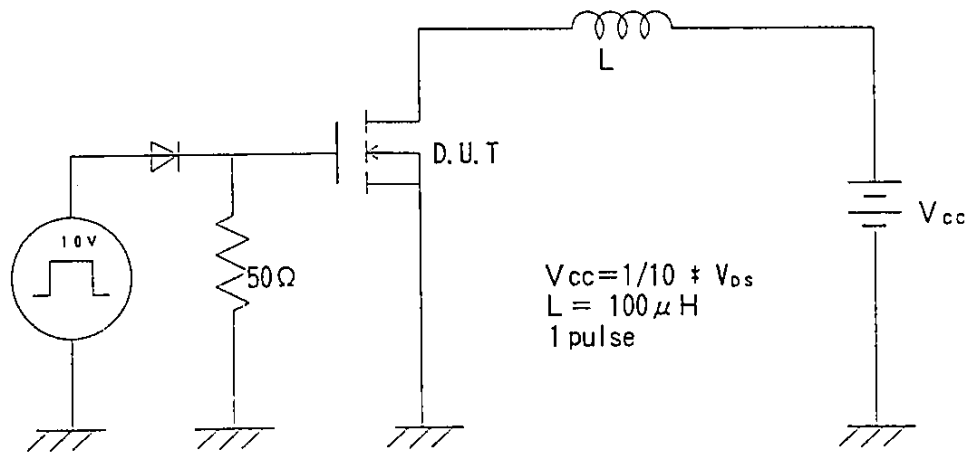
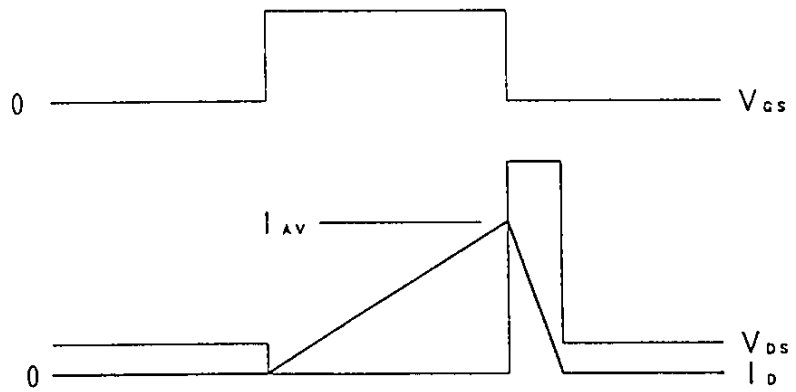
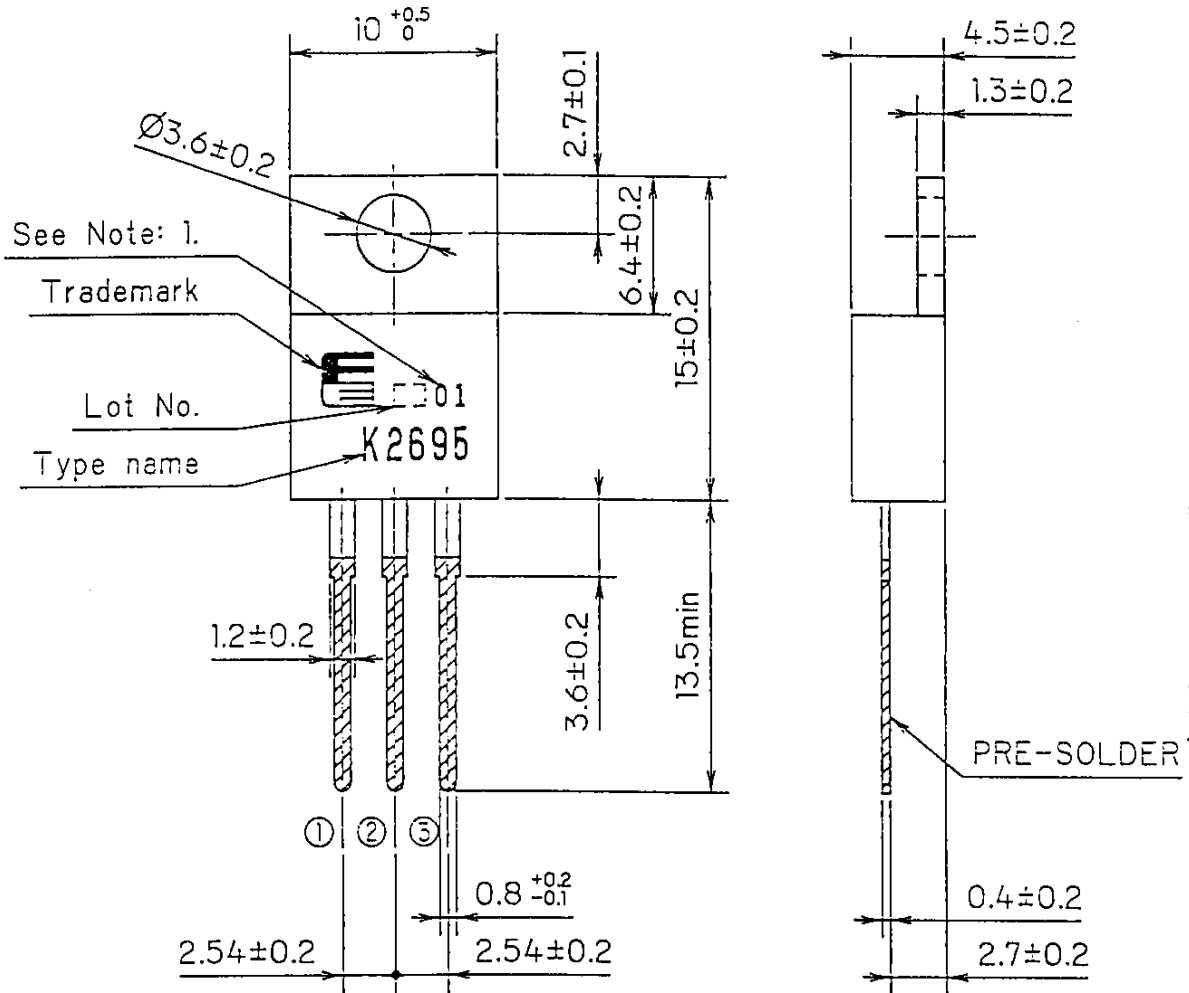


Fig.2 Operating waveforms

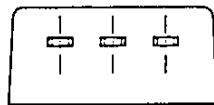


FUJI POWER MOS FET

TYPE : 2SK2695-01



CONNECTION



① ② ③

- ① GATE
- ② DRAIN
- ③ SOURCE

JEDEC : TO-220AB

Note: 1. Guaranteed mark of avalanche ruggedness.

DIMENSIONS ARE IN MILLIMETERS.

Fuji Electric Co.,Ltd

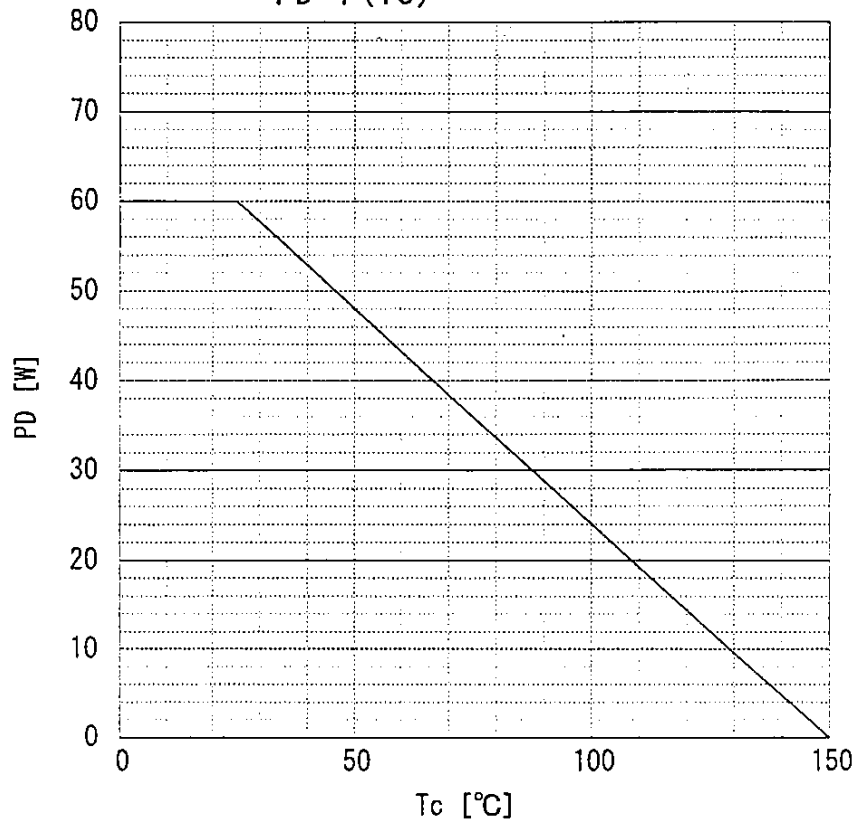
DWG.NO.

5/12

Y 0257-R-003a

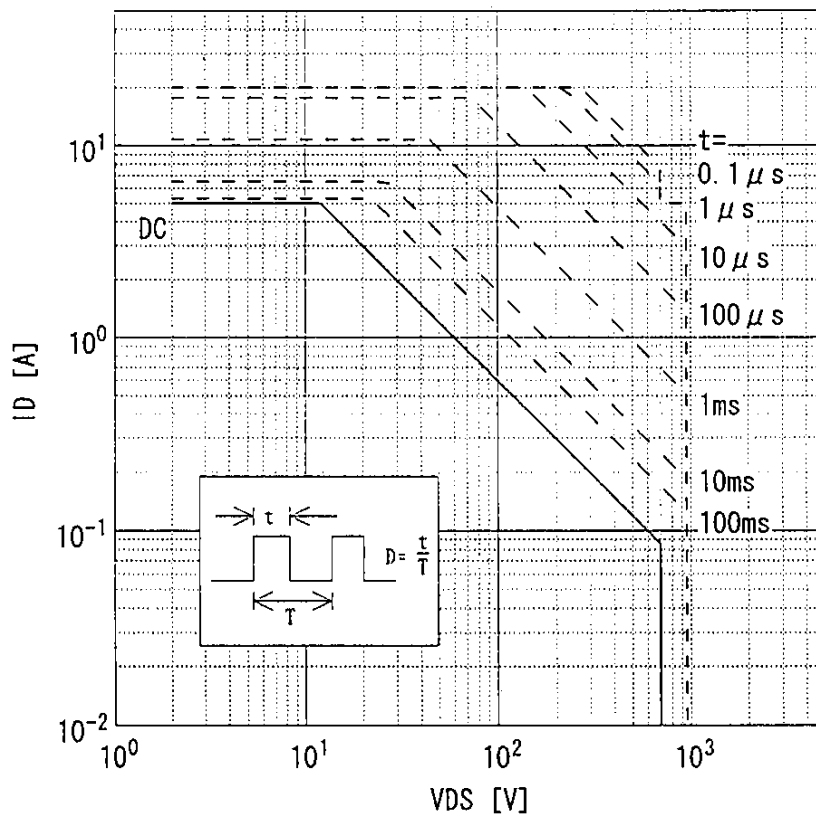
Power Dissipation

$$PD = f(T_c)$$

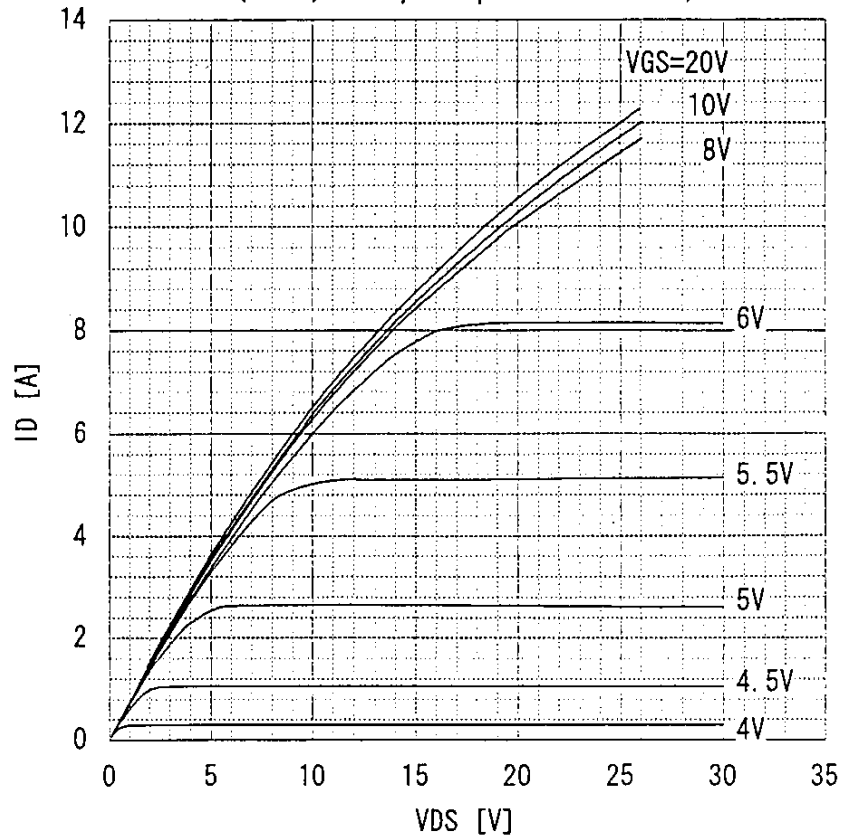


Safe operating area

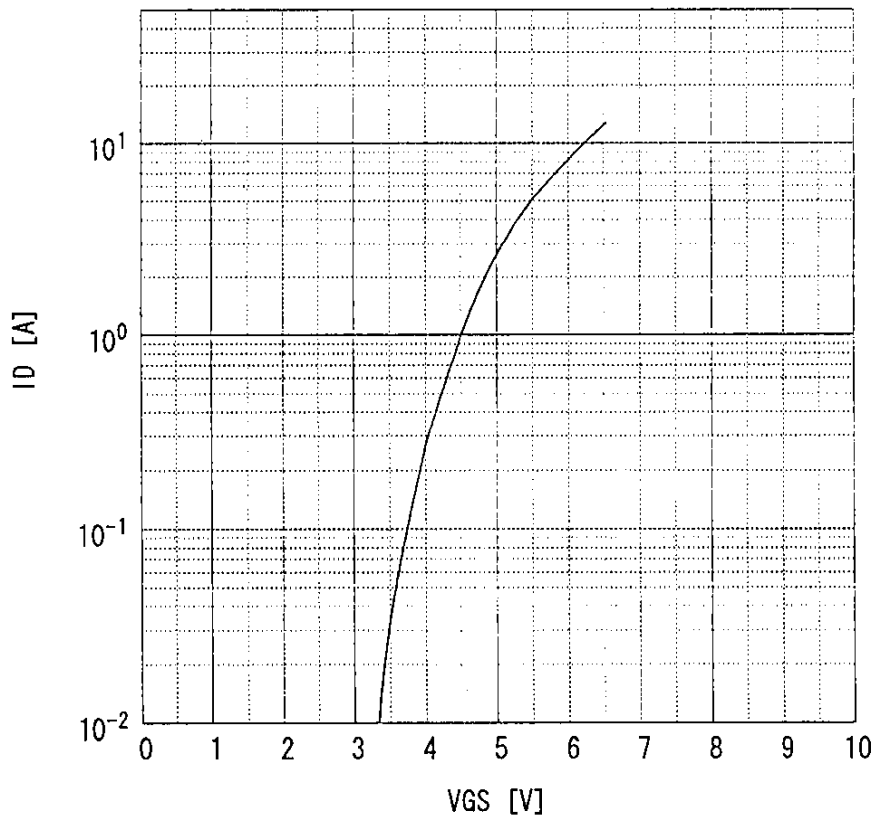
$$I_D = f(V_{DS}) : D = 0.01, T_c = 25^\circ\text{C}$$



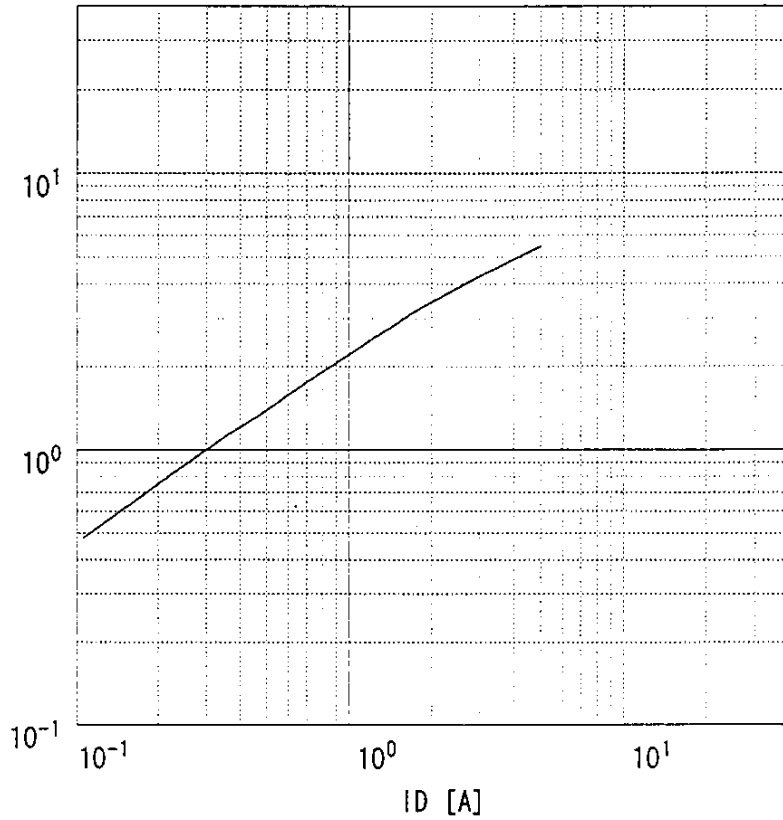
Typical output characteristics
 $I_D = f(V_{DS}) : 80 \mu s$ pulse test, $T_c = 25^\circ C$



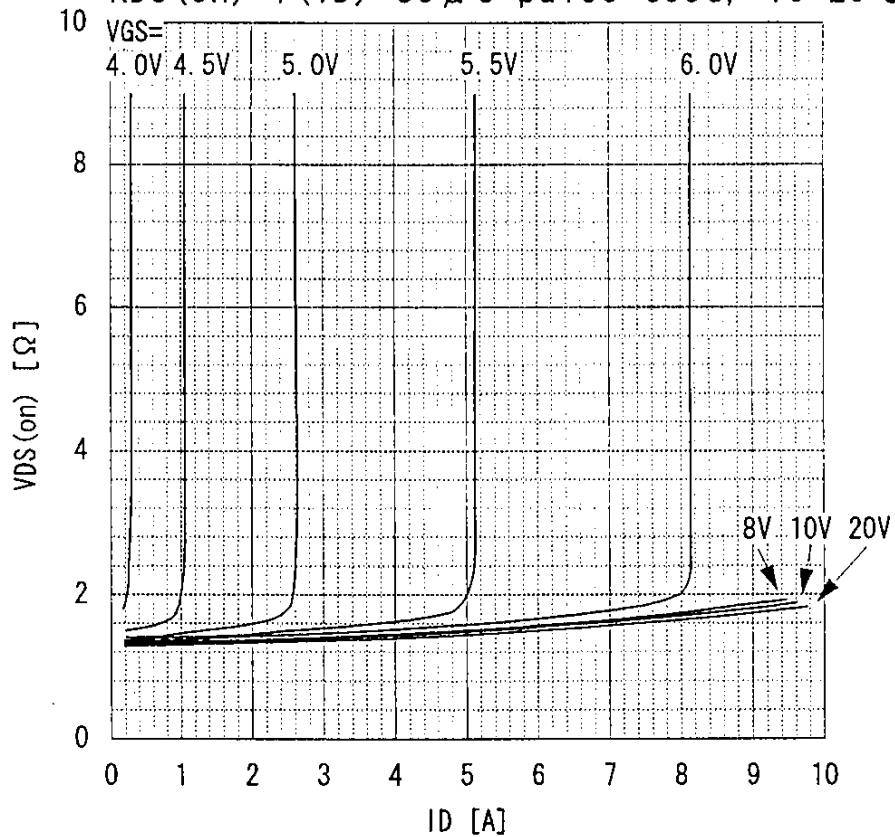
Typical transfer characteristic
 $I_D = f(V_{GS}) : 80 \mu s$ pulse test, $V_{DS} = 25V$, $T_{ch} = 25^\circ C$



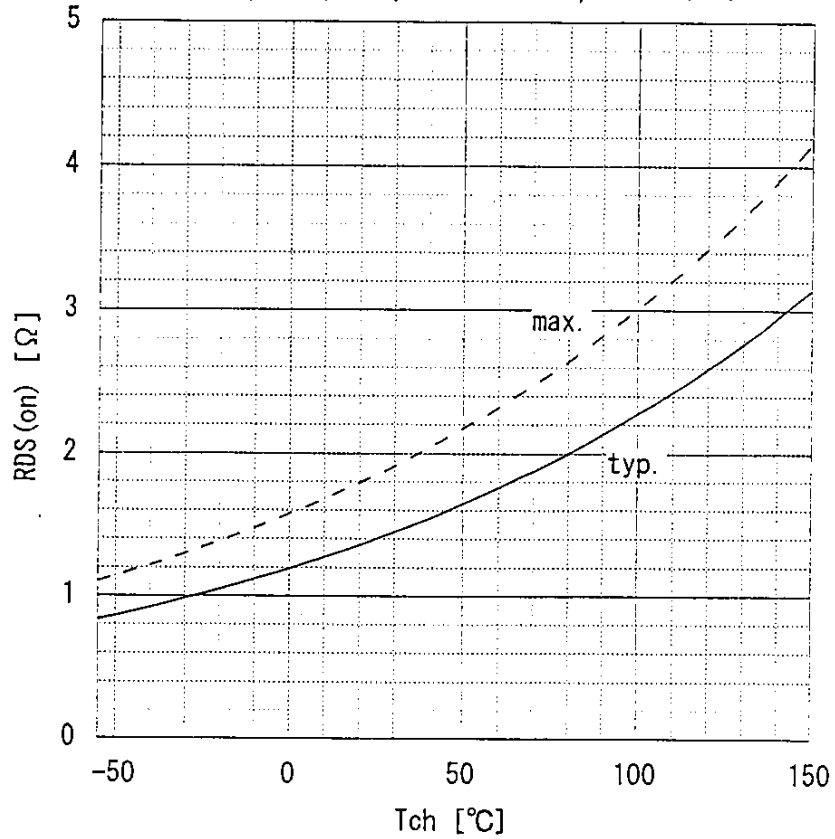
Typical forward transconductance
 $g_{fs}=f(I_D)$: 80 μ s pulse test, $V_{DS}=25V$, $T_{ch}=25^\circ C$



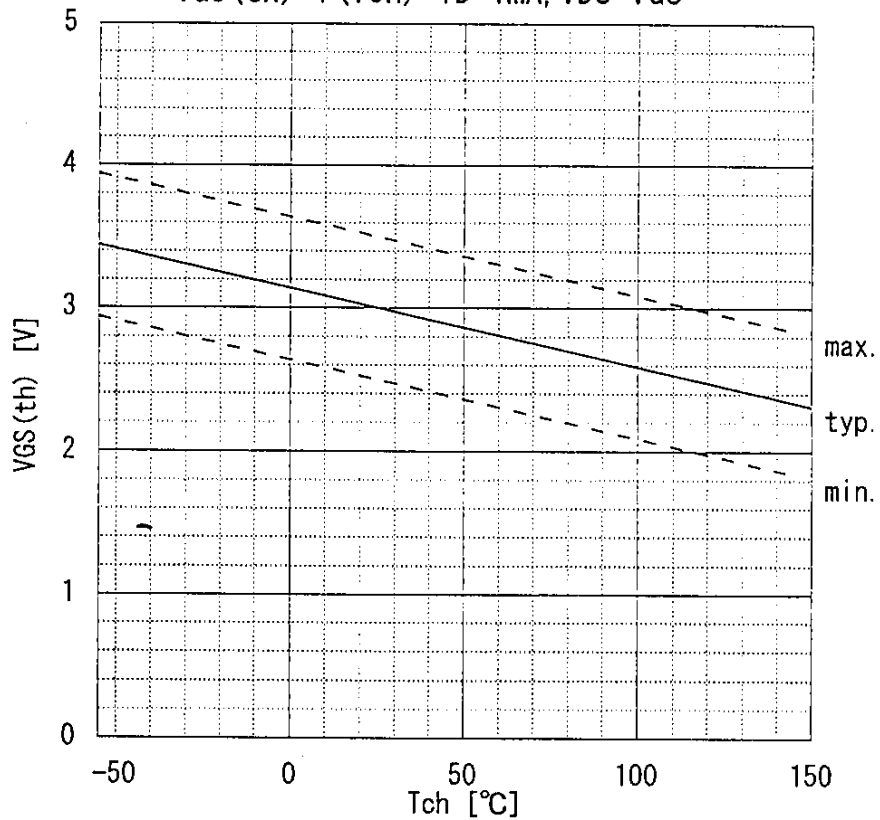
Typical drain-source on-state resistance
 $R_{DS(on)}=f(I_D)$: 80 μ s pulse test, $T_c=25^\circ C$



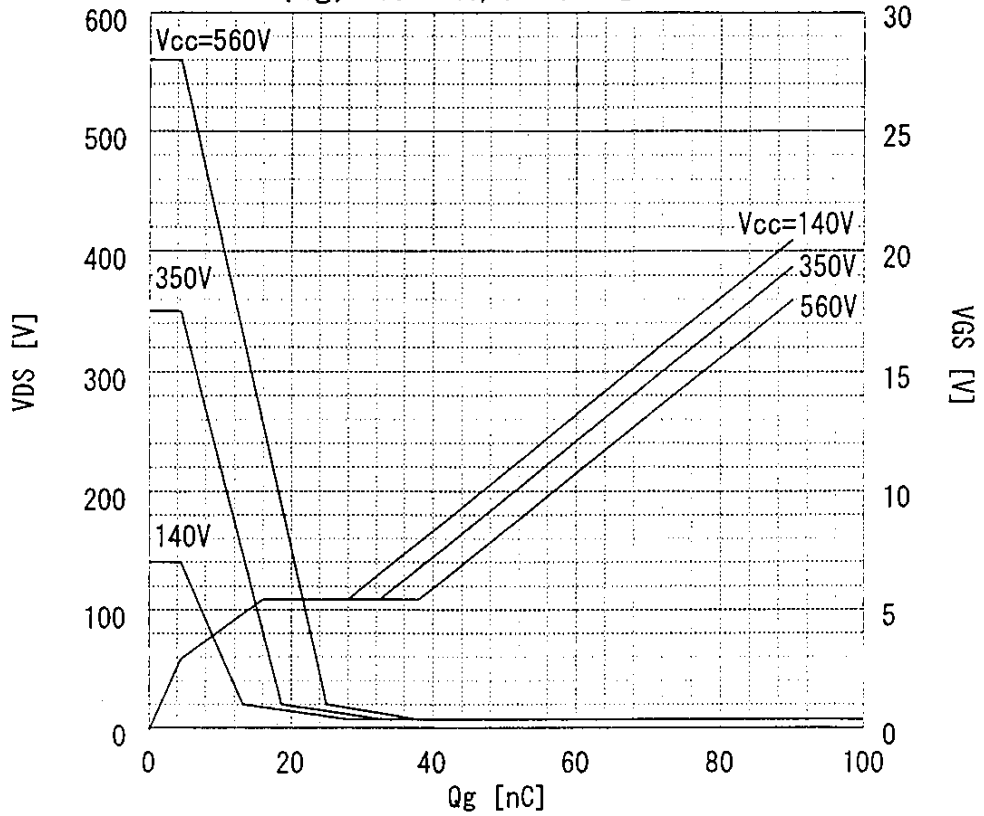
Drain-source on-state resistance
 $R_{DS(on)} = f(T_{ch}) : I_D = 2.5A, V_{GS} = 10V$



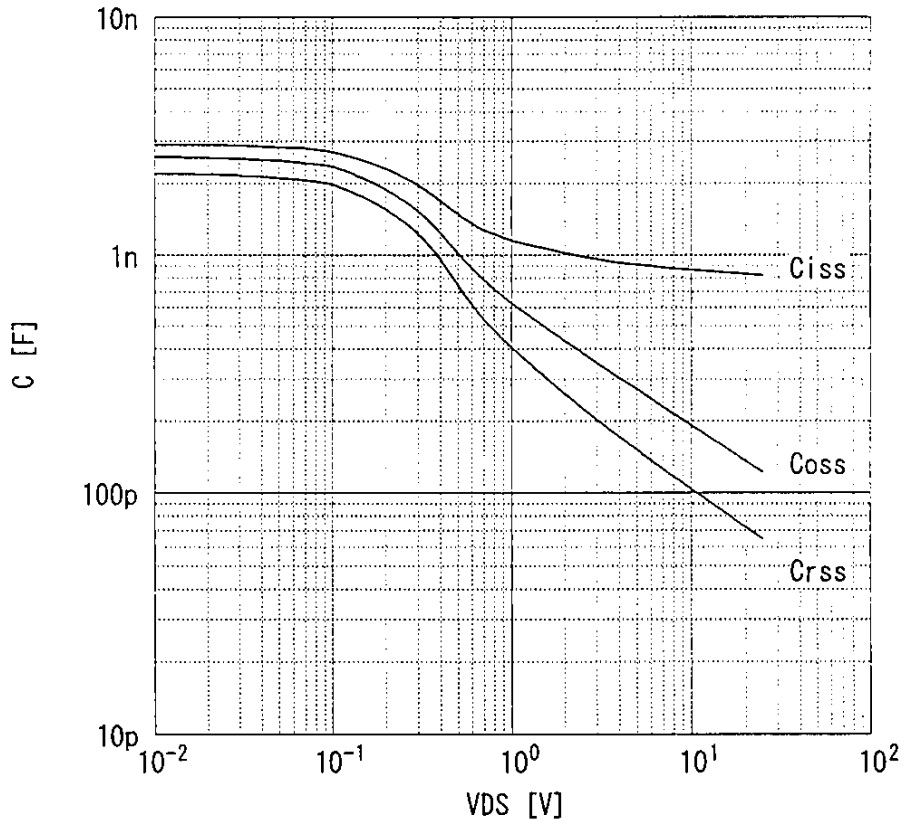
Gate threshold voltage
 $V_{GS(th)} = f(T_{ch}) : I_D = 1mA, V_{DS} = V_{GS}$



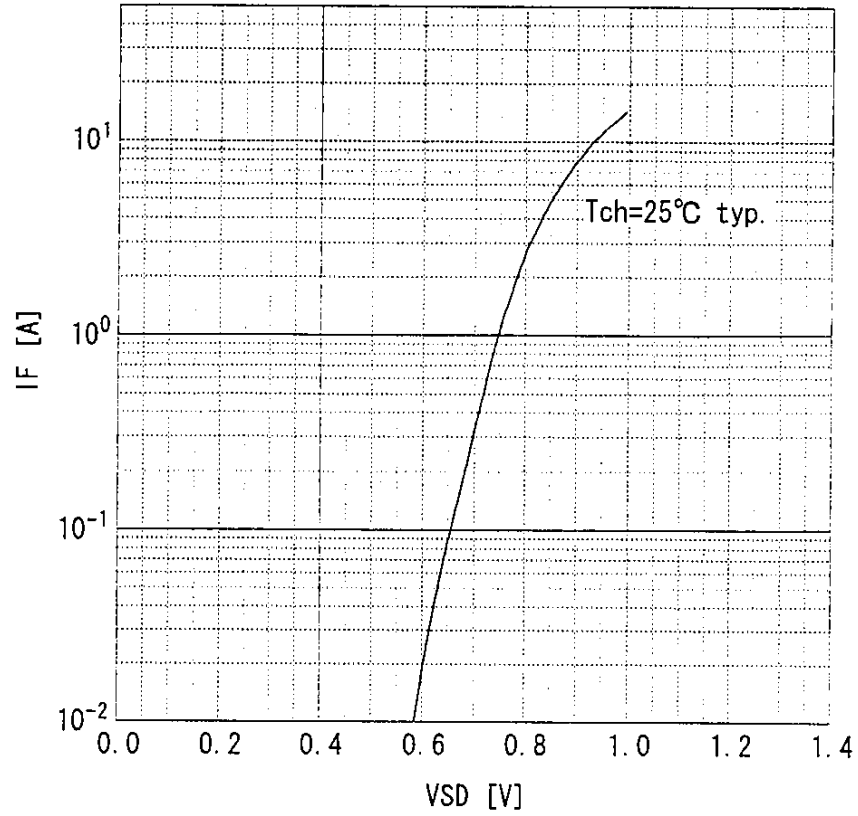
Typical gate charge characteristic
 $V_{GS} = f(Q_g) : I_D = 5A, T_c = 25^\circ C$



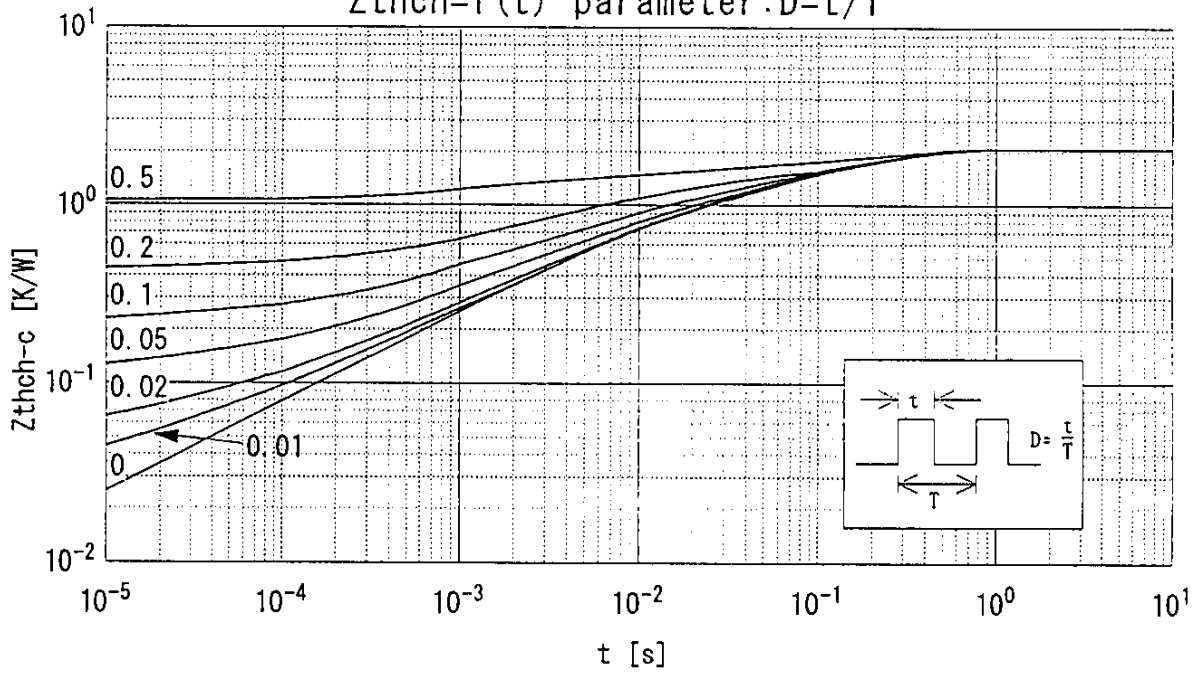
Typical capacitances
 $C = f(V_{DS}) : V_{GS} = 0V, f = 1MHz$



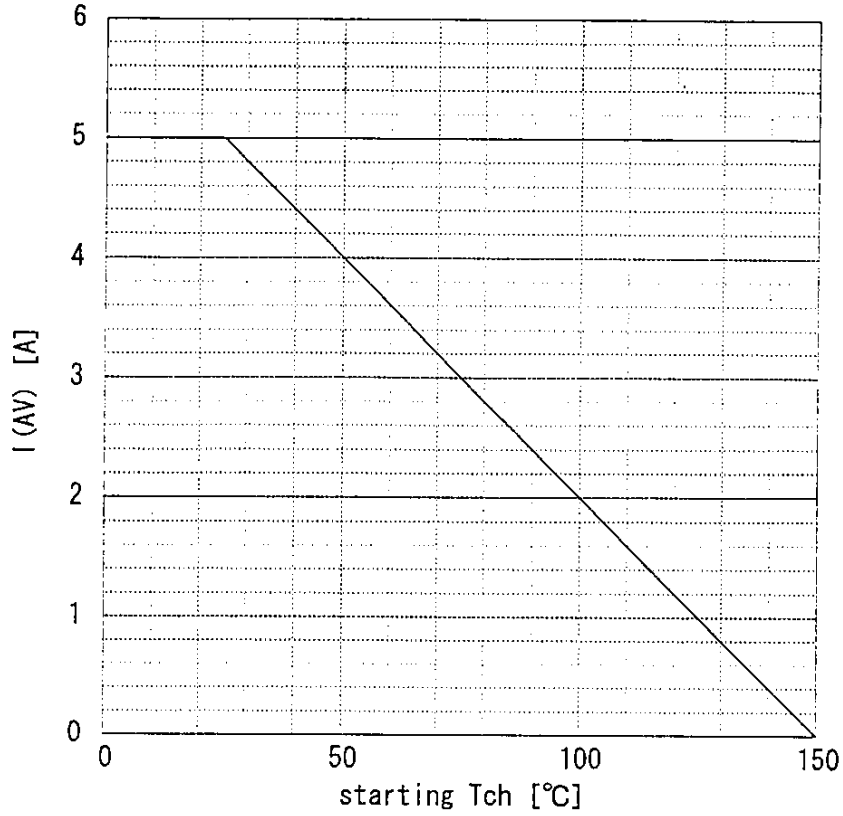
Forward characteristic of reverse of diode
 $I_F = f(V_{SD}) : 80 \mu s$ pulses test, $V_{GS} = 0V$



Transient thermal impedance
 $Z_{thc} = f(t)$ parameter: $D = t/T$



Maximum Avalanche Current vs. starting Tch
 $I(AV) = f(\text{starting Tch})$



Maximum Avalanche Energy vs. starting Tch
 $E(AV) = f(\text{starting Tch}) : V_{CC} = 70V, I(AV) \leq 5A$

