

SPECIFICATION

DEVICE NAME : Power MOSFET

TYPE NAME : 2SK2806-01

SPEC. NO. :

Fuji Electric Co.,Ltd.

This Specification is subject to change without notice.

	DATE	NAME	APPROVED	Fuji Electric Co.,Ltd.	
DRAWN:				DWG. NO.	1/13
CHECKED:					

- 1.Scope This specifies Fuji Power MOSFET 2SK2806-01
- 2.Construction N-Channel enhancement mode power MOSFET
- 3.Applications for Switching
- 4.Outview To-220 Outview See to 5/13 page

5.Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V _{DS}	30	V	
Continuous Drain Current	I _D	±35	A	
Pulsed Drain Current	I _{DP}	±140	A	
Gate-Source Voltage	V _{GS}	±16	V	
Maximum Avalanche Energy	E _{AV}	129.3	mJ	*1
Maximum Power Dissipation	P _D	30	W	
Operating and Storage	T _{ch}	150	°C	
Temperature range	T _{stg}	-55 to +150	°C	

*1 L=0.070mH,Vcc=12V

6.Electrical Characteristics at Tc=25°C (unless otherwise specified)

Static Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =1mA V _{GS} =0V	30			V
Gate Threshold Voltage	V _{GS(th)}	I _D =1mA V _{DS} =V _{GS}	1.0	1.5	2.0	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V V _{GS} =0V		10	500	μA
		T _{ch} =25°C				
		T _{ch} =125°C		0.2	1.0	mA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±16V V _{DS} =0V		10	100	nA
Drain-Source On-State Resistance	R _{DS(on)}	I _D =17.5A		22	30	mΩ
		V _{GS} =4V		14	20	
		V _{GS} =10V				

Dynamic Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g_{fs}	$I_D=17.5A$ $V_{DS}=25V$	16	33		S
Input Capacitance	C_{iss}	$V_{DS}=25V$		1100	1650	pF
Output Capacitance	C_{oss}	$V_{GS}=0V$		550	830	
Reverse Transfer Capacitance	C_{rss}	$f=1MHz$		240	360	
Turn-On Time	$t_{d(on)}$	$V_{cc}=15V$		9	15	ns
	t_r	$V_{GS}=10V$		15	23	
Turn-Off Time	$t_{d(off)}$	$I_D=35A$		75	115	
	t_f	$R_{GS}=10\Omega$		50	75	

Reverse Diode

Description	Symbol	Conditions	min.	typ.	max.	Unit
Avalanche Capability	I_{AV}	$L=100\mu H$ $T_{ch}=25^\circ C$ See Fig.1 and Fig.2	35			A
Diode Forward On-Voltage	V_{SD}	$I_F=2 \times I_{DR}$ $V_{GS}=0V$ $T_{ch}=25^\circ C$		0.98	1.71	V
Reverse Recovery Time	t_{rr}	$I_F=2 \times I_{DR}$		50		ns
Reverse Recovery Charge	Q_{rr}	$-di/dt=100A/\mu s$ $T_{ch}=25^\circ C$		0.08		μC

7. Thermal Resistance

Description	Symbol	min.	typ.	max.	Unit
Channel to Case	$R_{th(ch-c)}$			4.16	$^\circ C/W$
Channel to Ambient	$R_{th(ch-a)}$			75.0	$^\circ C/W$

Fig.1 Test circuit

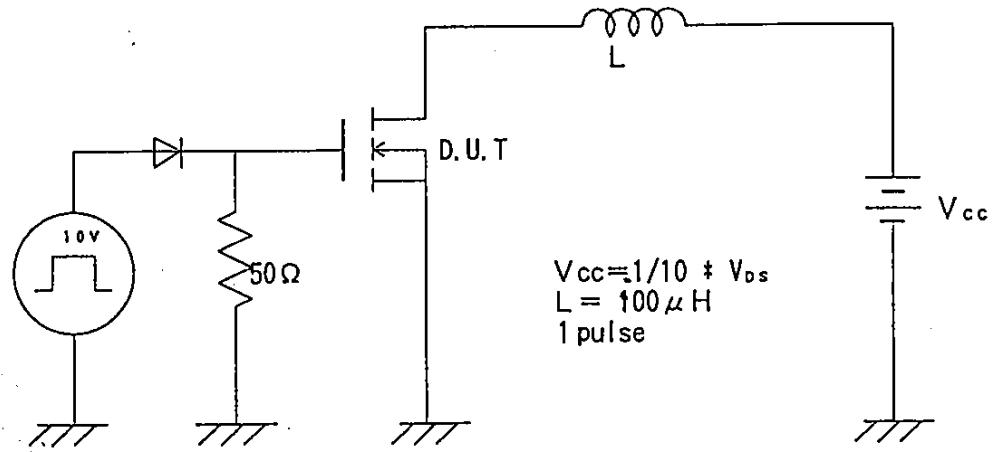
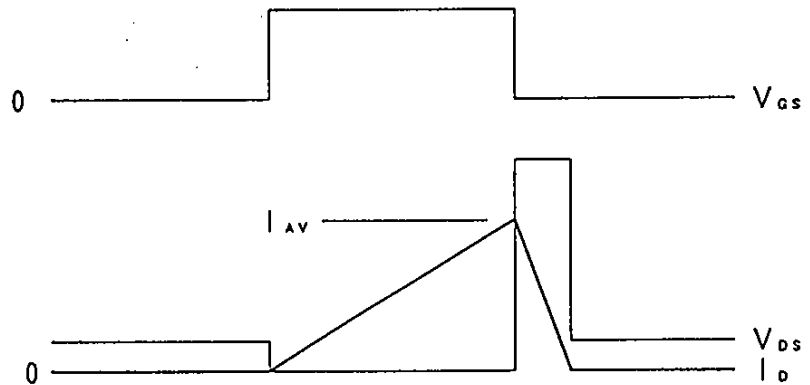
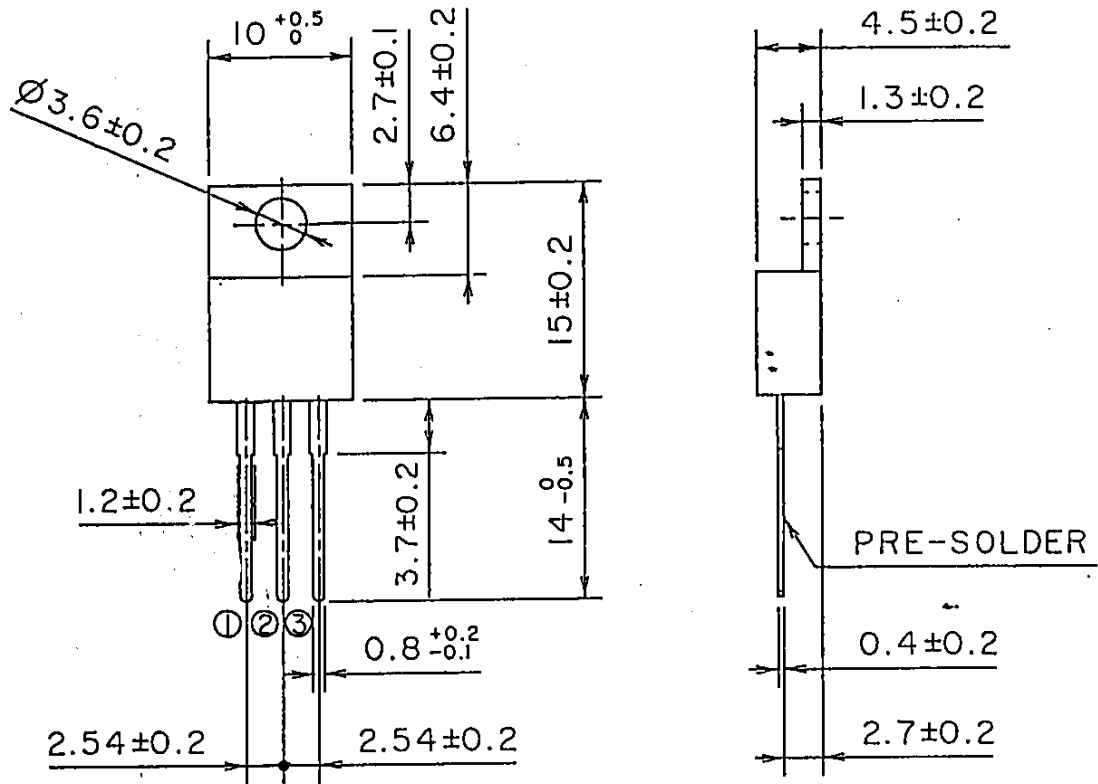


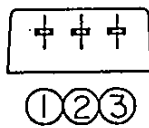
Fig.2 Operating waveforms





DIMENSIONS ARE IN MILLIMETERS.

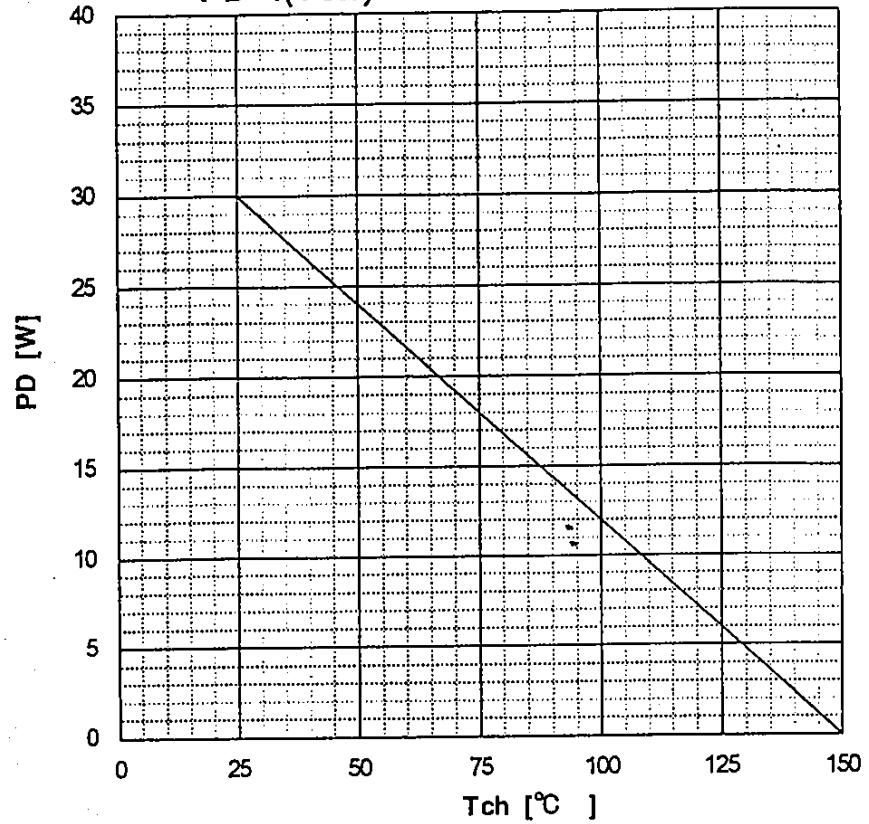
CONNECTION



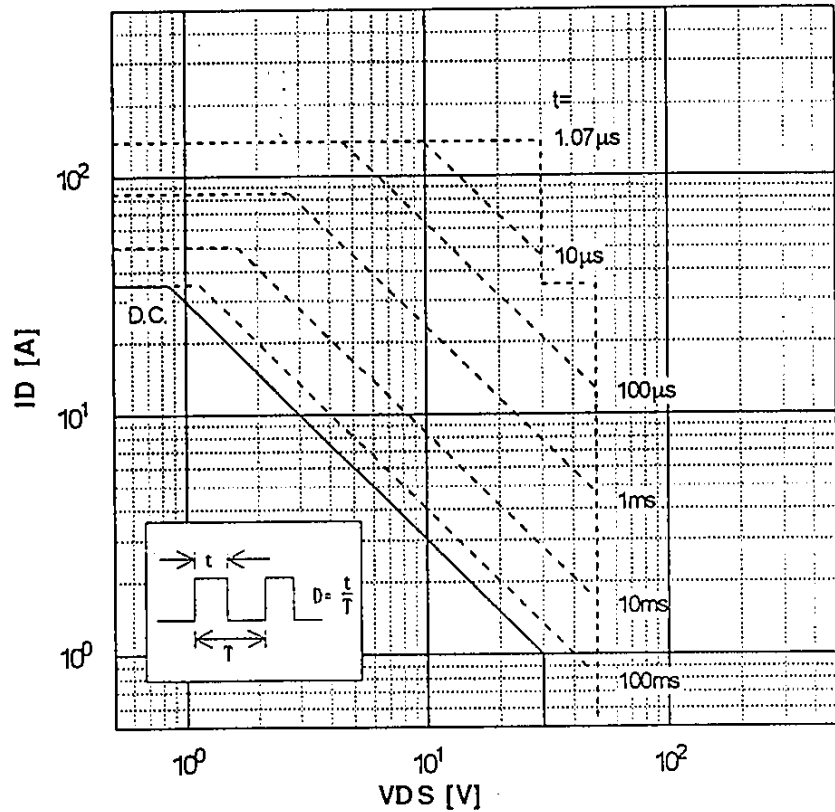
- ① GATE
- ② DRAIN
- ③ SOURCE

JEDEC : TO-220AB

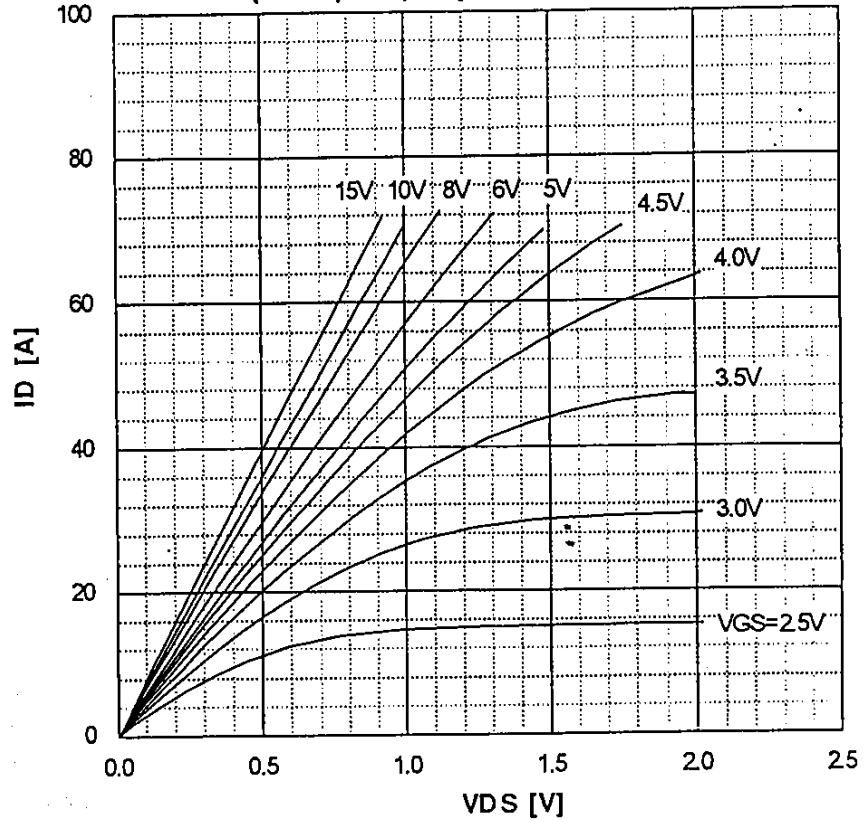
Allowable Power Dissipation vs. T_{ch}
 $PD=f(T_{ch})$



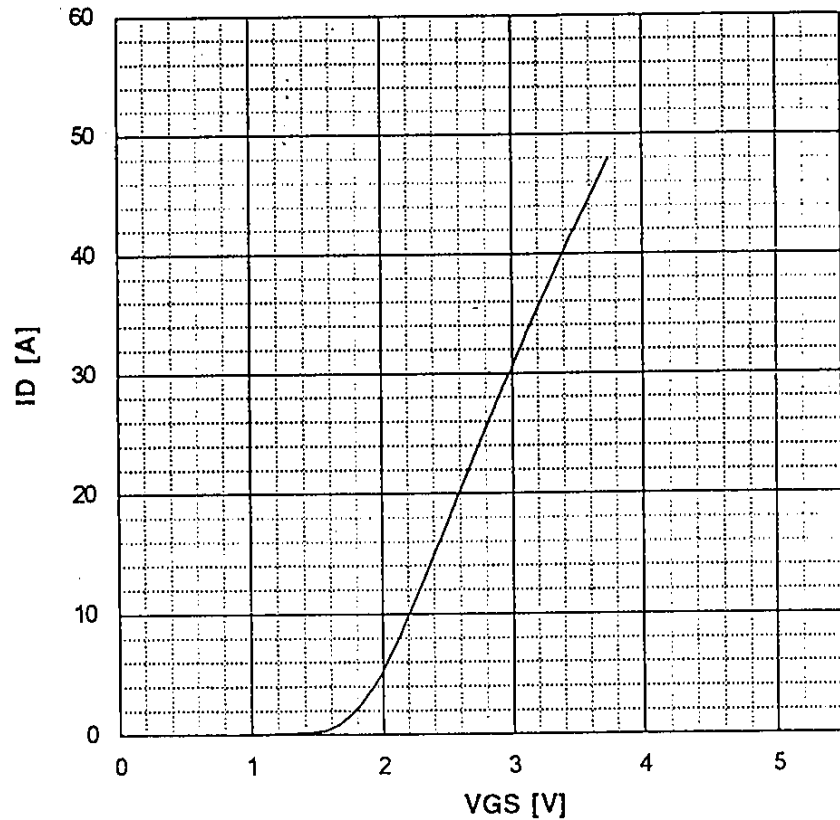
Safe operating area
 $ID=f(V_{DS}): D=0.01, T_c=25^\circ C$



Typical Output Characteristics
 $I_D = f(V_{DS})$: 80 μ s pulse test, $T_{ch} = 25^\circ\text{C}$

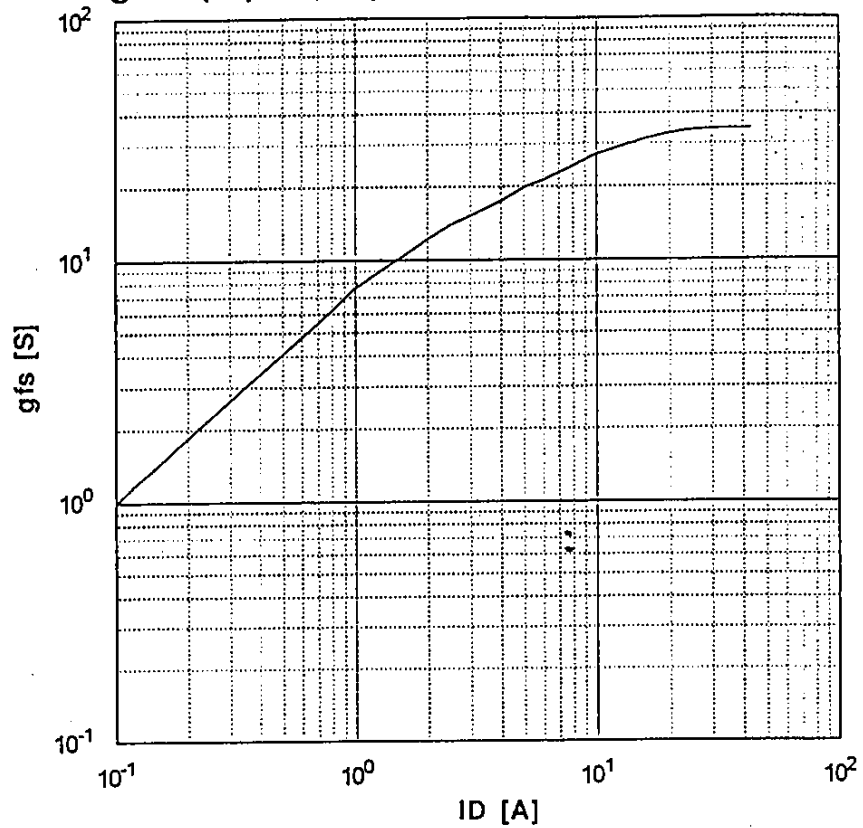


Typical Transfer Characteristic
 $I_D = f(V_{GS})$: 80 μ s pulse test, $V_{DS} = 25\text{V}$, $T_{ch} = 25^\circ\text{C}$



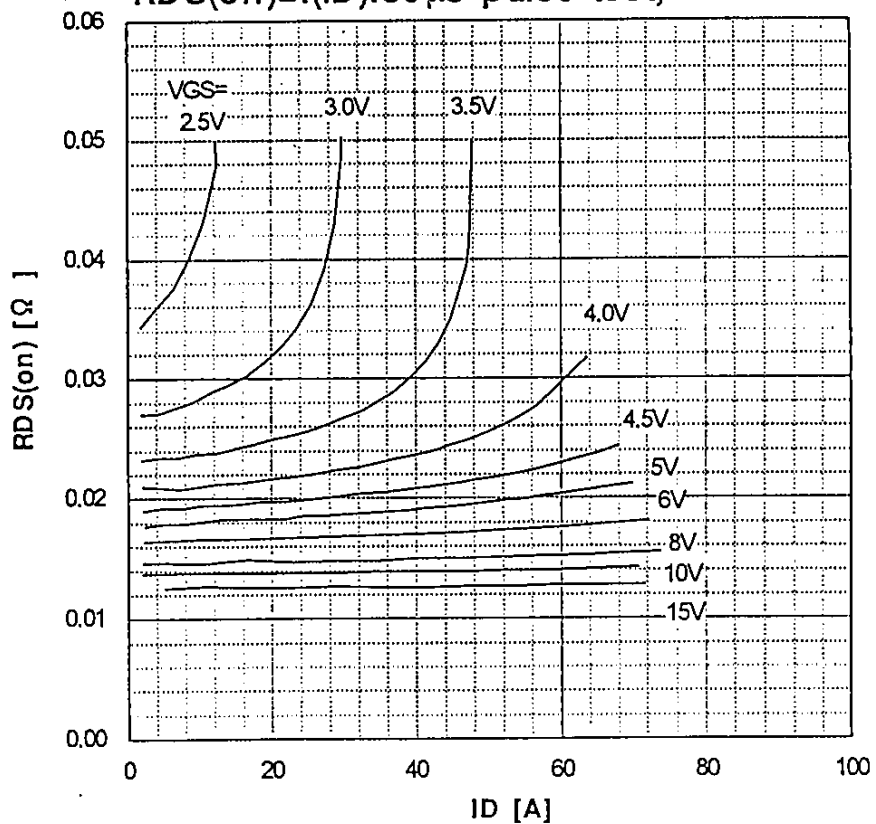
Typical Transconductance

$g_{fs}=f(I_D)$: 80 μ s pulse test, $V_{DS}=25V$, $T_{ch}=25^\circ C$

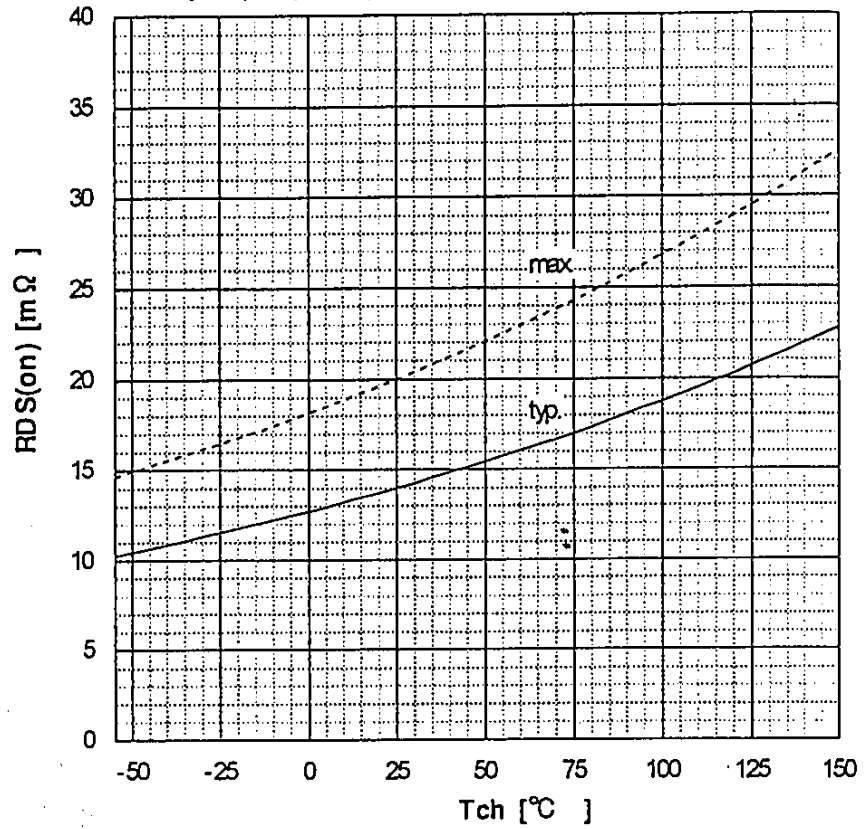


Typical Drain-Source on-state Resistance vs. I_D

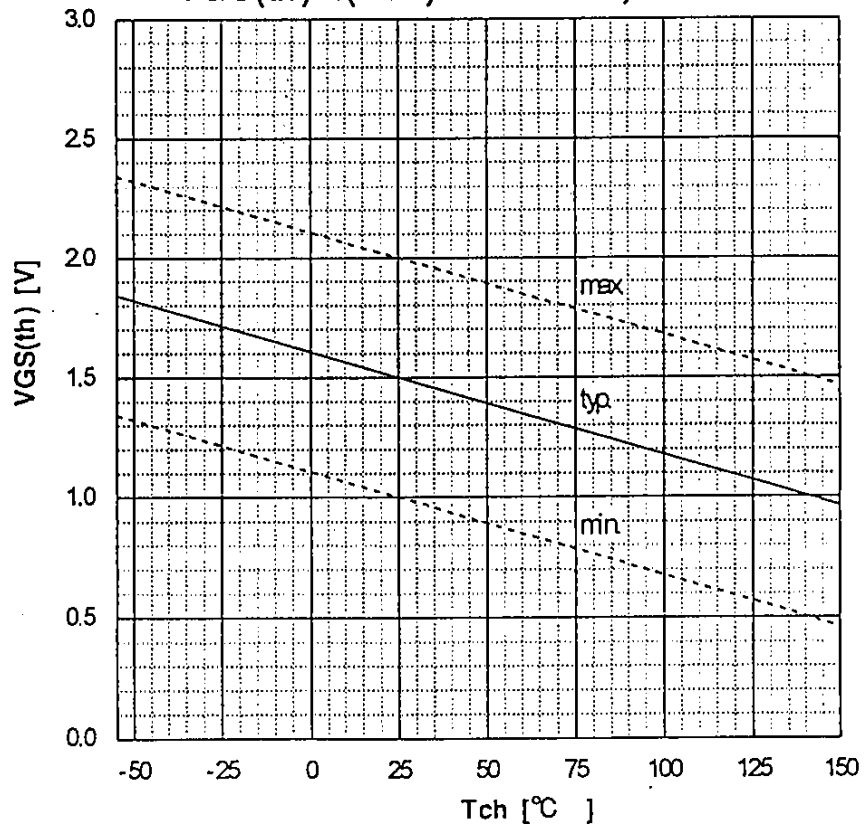
$R_{DS(on)}=f(I_D)$: 80 μ s pulse test, $T_{ch}=25^\circ C$



Drain-Source On-state Resistance vs. T_{ch}
 $R_{DS(on)}=f(T_{ch}):I_D=17.5A,V_{GS}=10V$

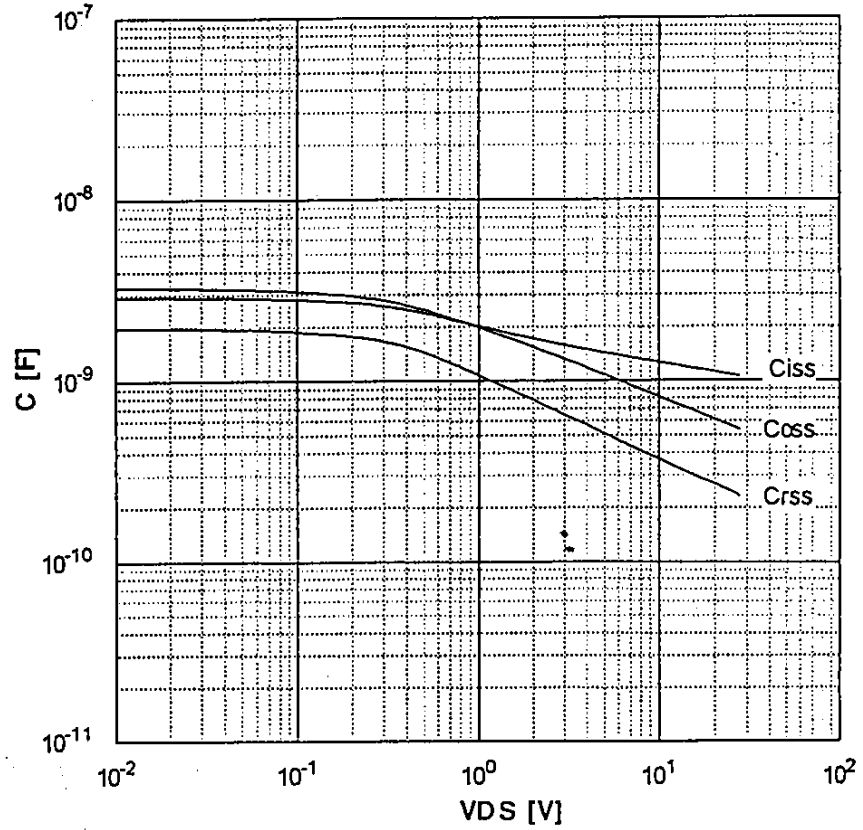


Gate Threshold Voltage vs. T_{ch}
 $V_{GS(th)}=f(T_{ch}):V_{DS}=V_{GS},I_D=1mA$



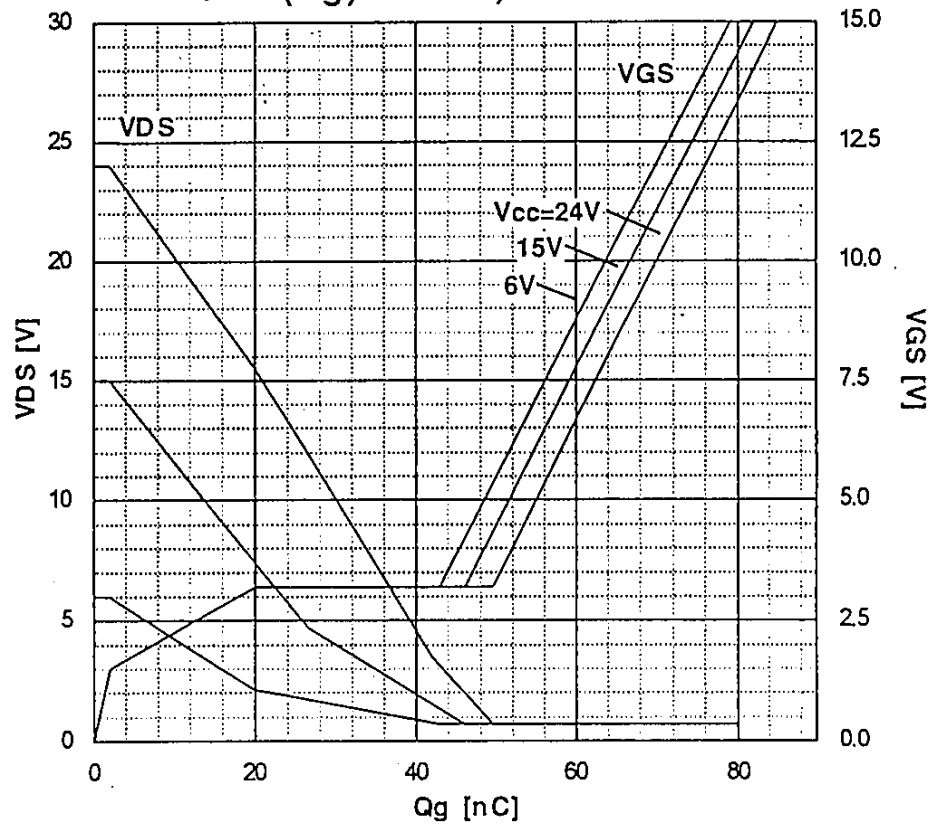
Typical Capacitances vs. VDS

$C=f(V_{DS}):V_{GS}=0V, f=1MHz$

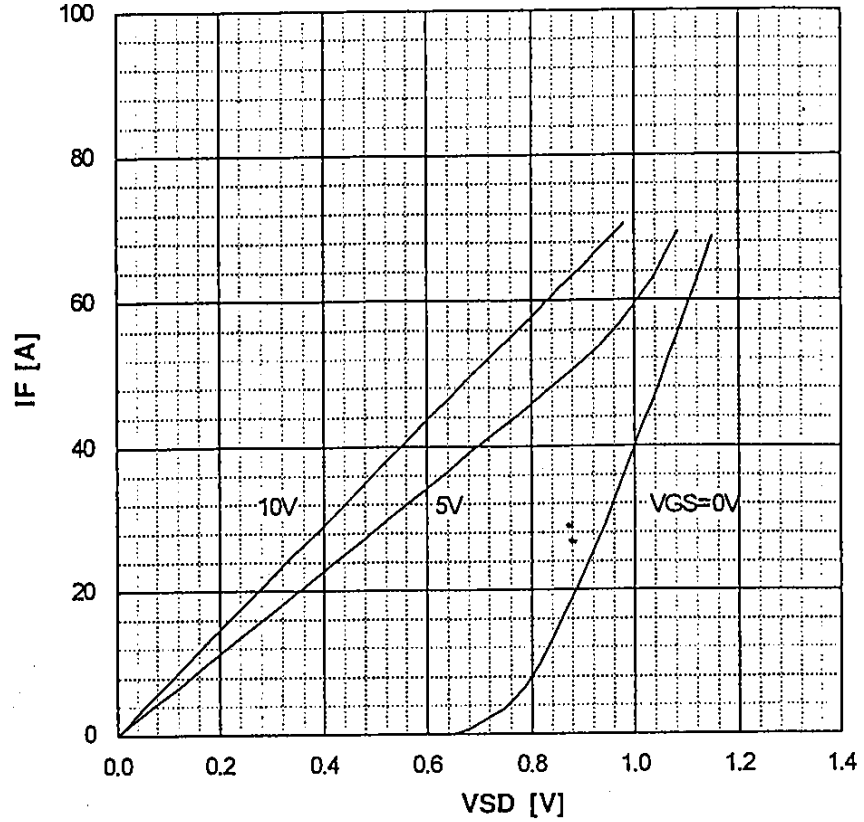


Typical Gate Charge Characteristics

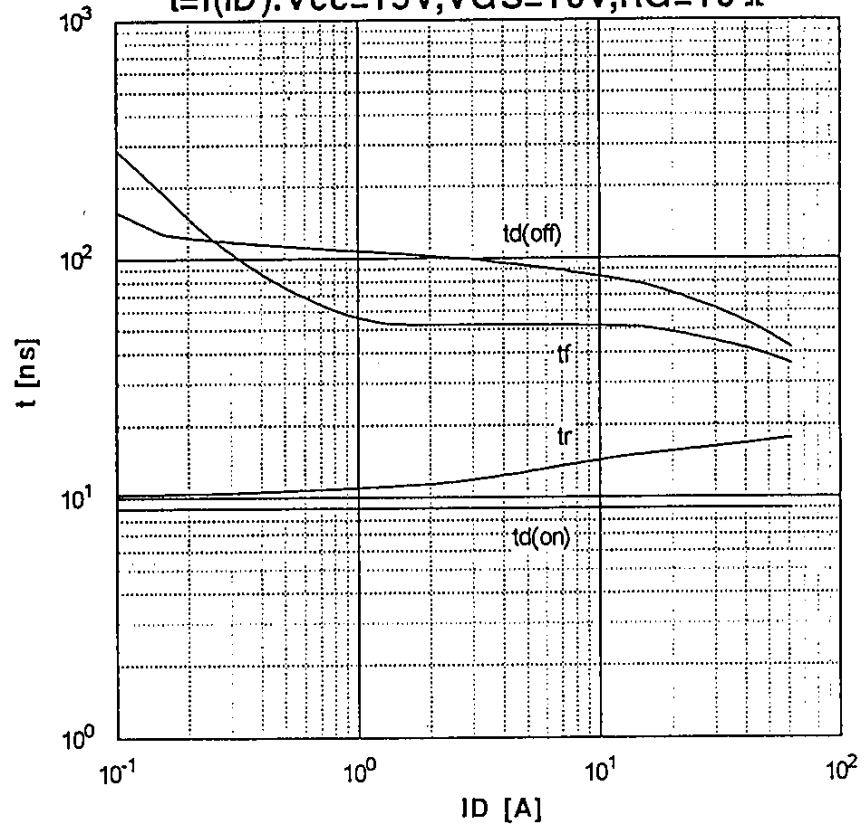
$V_{GS}=f(Q_g):I_D=35A, T_{ch}=25^\circ C$



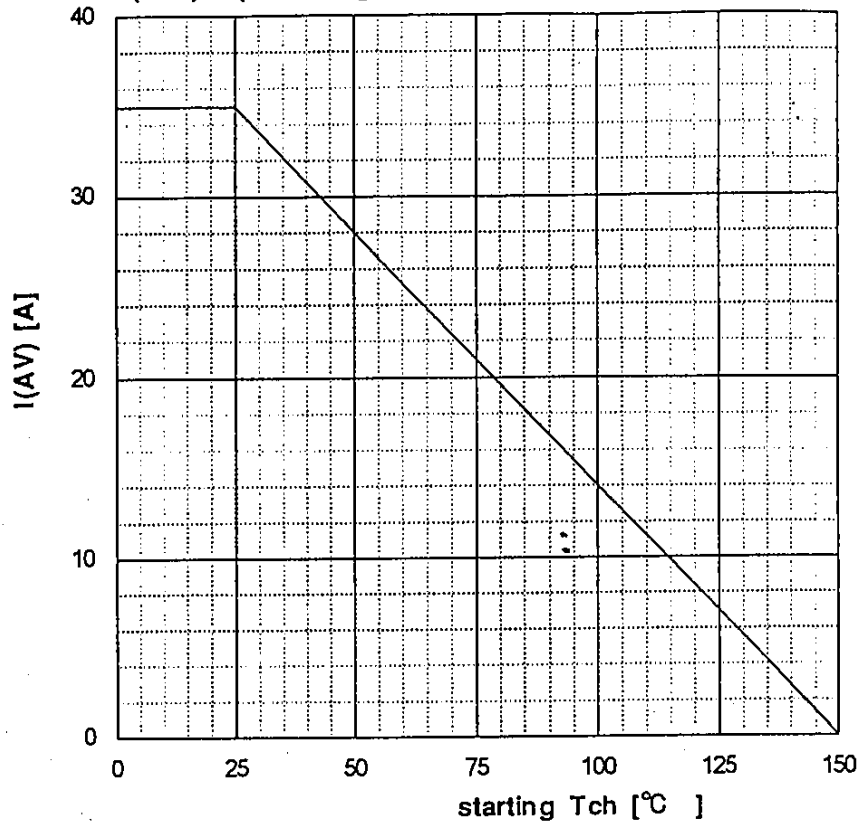
Typical Forward Characteristics of Reverse Diode
 $I_F = f(V_{SD})$: 80 μ s pulse test, $T_{ch} = 25^\circ\text{C}$



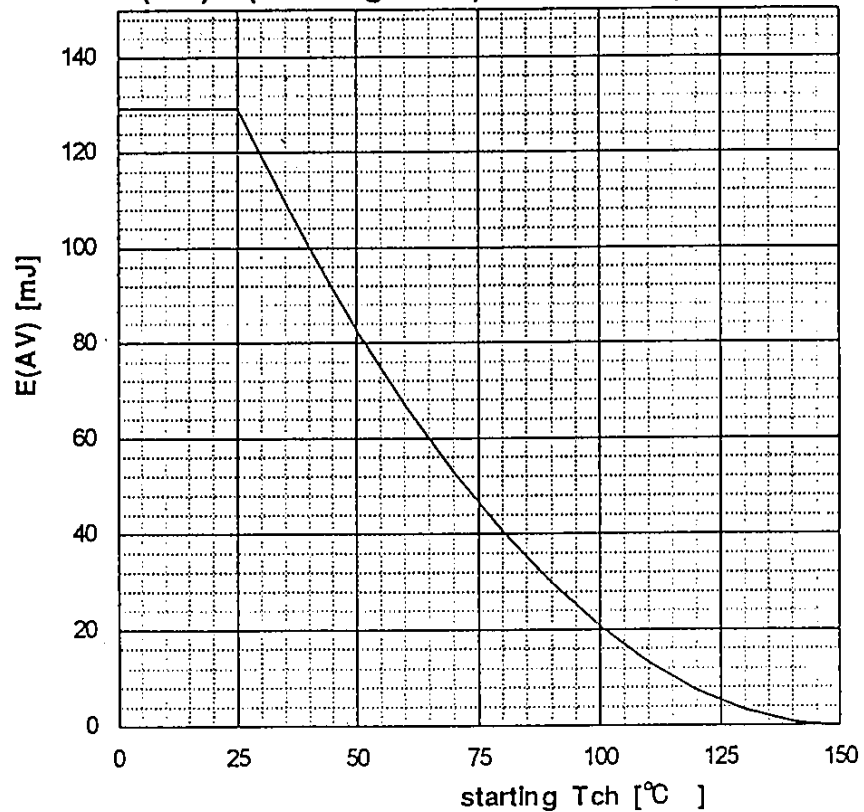
Typical Switching Characteristics vs. I_D
 $t = f(I_D)$: $V_{CC} = 15V$, $V_{GS} = 10V$, $R_G = 10 \Omega$



Maximum Avalanche Current vs. starting Tch
 $I(AV)=f(\text{starting Tch})$

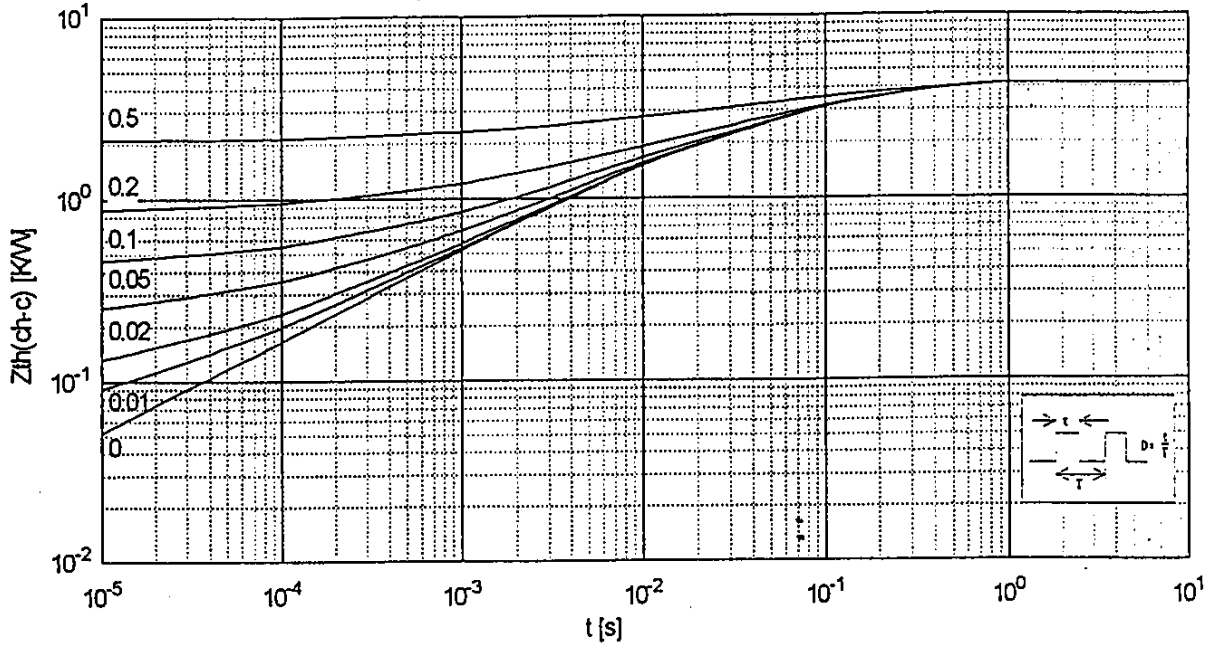


Maximum Avalanche Energy vs. starting Tch
 $E(AV)=f(\text{starting Tch}):V_{cc}=12V, I(AV) \leq 35A$



Transient Thermal Impedance

$Z_{th}(ch-c) = f(t)$: parameter $D = t/T$



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