

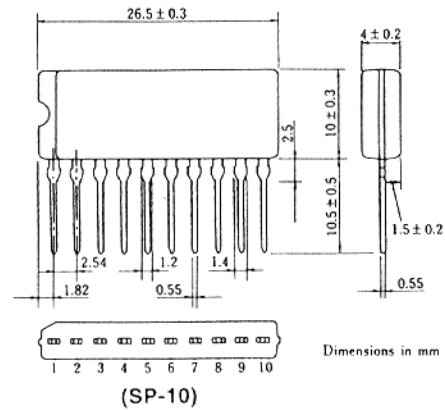
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SILICON N-CHANNEL POWER MOS FET ARRAY

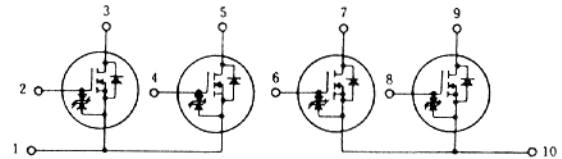
HIGH SPEED POWER SWITCHING

■ FEATURES

- Low On-Resistance
 - $R_{DS(on)} \leq 0.4 \Omega, V_{GS} = 10 V, I_D = 1.5 A$
 - $R_{DS(on)} \leq 0.55 \Omega, V_{GS} = 4 V, I_D = 1.5 A$
- Capable of 4 V Gate Drive
- Low Drive Current
- High Speed Switching
- High Density Mounting
- Suitable for Motor Driver, Solenoid Driver and Lamp Driver
- Discrete Packaged Devices of Same Die: 2SK1254 (L), 2SK1254 (S)



■ EQUIVALENT CIRCUIT



■ ABSOLUTE MAXIMUM RATINGS (Ta = 25°C) (1Unit)

Item	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current	I_D	3	A
Drain Peak Current	$I_{D(pk)}$ *	12	A
Body-Drain Diode	I_{DR}	3	A
Reverse Drain Current			
Channel Dissipation	$Pch(Tc=25°C)**$	28	W
Channel Dissipation	$Pch**$	4	W
Channel Temperature	Tch	150	°C
Storage Temperature	$Tstg$	-55 ~ +150	°C

*PW ≤ 10μs, duty cycle ≤ 1%

**4 Devices Operation

■ PIN CONNECTION

- 2. 4. 6. 8 ; Gate
- 3. 5. 7. 9 ; Drain
- 1. 10 ; Source

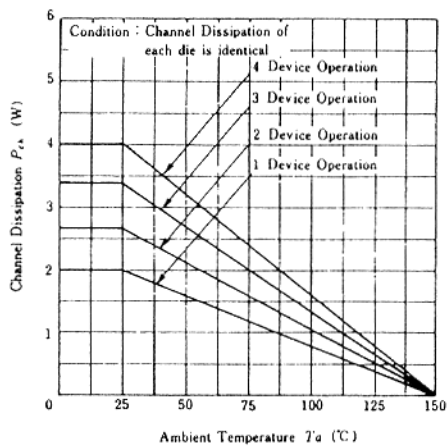
■ ELECTRICAL CHARACTERISTICS (Ta = 25°C) (1 Unit)

Item	Symbol	Test Condition	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DS}$	$I_D = 10\text{mA}, V_{GS} = 0$	120	—	—	V
Gate-Source Breakdown Voltage	$V_{(BR)GS}$	$I_G = \pm 100\ \mu\text{A}, V_{DS} = 0$	± 20	—	—	V
Gate-Source Leak Current	I_{GSS}	$V_{GS} = \pm 16\text{V}, V_{DS} = 0$	—	—	± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100\text{V}, V_{GS} = 0$	—	—	100	μA
Gate-Source Cutoff Voltage	$V_{GS(off)}$	$I_D = 1\text{mA}, V_{DS} = 10\text{V}$	1.0	—	2.0	V
Static Drain-Source On State Resistance	$R_{DS(on)}$	$I_D = 1.5\text{A}, V_{GS} = 10\text{V}^*$	—	0.3	0.4	Ω
		$I_D = 1.5\text{A}, V_{GS} = 4\text{V}^*$	—	0.35	0.55	Ω
Forward Transfer Admittance	$ y_{fs} $	$I_D = 1.5\text{A}, V_{DS} = 10\text{V}^*$	2.0	3.5	—	S
Input Capacitance	C_{iss}	$V_{DS} = 10\text{V}, V_{GS} = 0, f = 1\text{MHz}$	—	420	—	pF
Output Capacitance	C_{oss}		—	190	—	pF
Reverse Transfer Capacitance	C_{rss}		—	25	—	pF
Turn-on Delay Time	$t_d(on)$		$I_D = 1.5\text{A}, V_{GS} = 10\text{V}, R_L = 20\ \Omega$	—	5	—
Rise Time	t_r	—		20	—	ns
Turn-off Delay Time	$t_d(off)$	—		160	—	ns
Fall Time	t_f	—		40	—	ns
Body-Drain Diode Forward Voltage	V_{DF}	$I_F = 3\text{A}, V_{GS} = 0$	—	0.95	—	V
Body-Drain Diode Reverse Recovery Time	t_r	$I_F = 3\text{A}, V_{GS} = 0$ $di/dt = 50\text{A}/\mu\text{s}$	—	160	—	ns

* Pulse Test

■ See characteristic curves of 2SK1254(L), 2SK1254(S)

MAXIMUM CHANNEL DISSIPATION CURVE



MAXIMUM CHANNEL DISSIPATION CURVE

