

FAN1084

4.5A Adjustable/Fixed Low Dropout Linear Regulator

Features

- Fast transient response
- Low dropout voltage at up to 4.5A
- Load regulation: 0.5% typical
- On-chip thermal limiting
- Standard TO-220 and TO-263 center cut packages

Applications

- Desktop PCs, RISC and embedded processors' supply
- GTL, SSTL logic Reference bus supply
- Low voltage VCC logic supply
- Battery-powered circuitry
- Post regulator for switching supply
- Cable and ADSL modems' DSP core supply
- Set Top Boxes and Web Boxes modules' supply

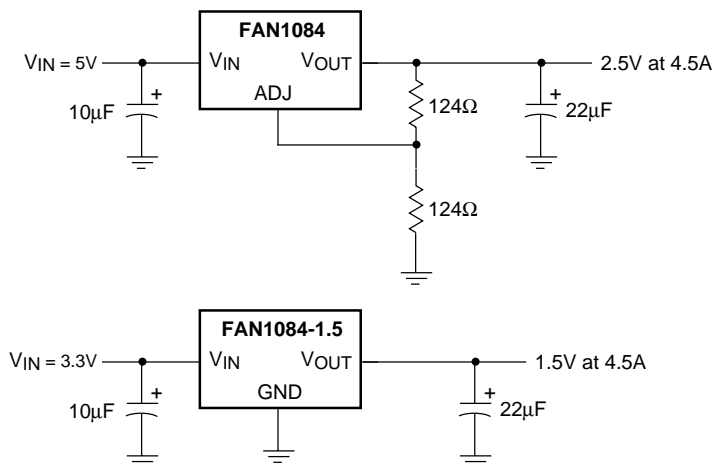
Description

The FAN1084 and FAN1084-1.5 are low dropout three-terminal regulators with 4.5A output current capability. These devices have been optimized for low voltage applications including VTT bus termination, where transient response and minimum input voltage are critical. The FAN1084 is ideal for low voltage microprocessor applications requiring a regulated output from 1.5V to 3.6A with an input supply of 5V or less. The FAN1084-1.5 offers fixed 1.5V with 4.5A current capabilities for GTL+ bus VTT termination.

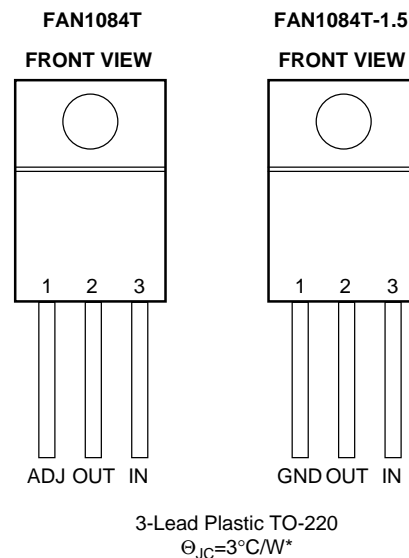
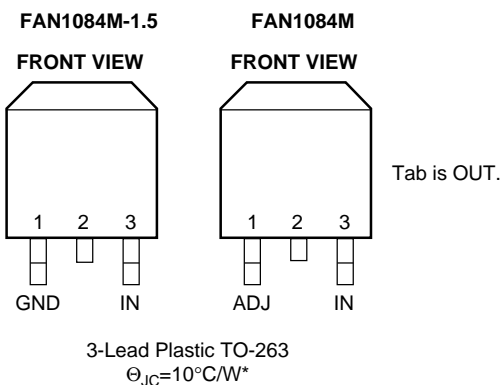
On-chip thermal limiting provides protection against any combination of overload and ambient temperature that would create excessive junction temperatures.

The FAN1084 series regulators are available in the industry-standard TO-220 and TO-263 center cut power packages.

Block Diagram



Pin Assignments



*With package soldered to 0.5 square inch copper area over backside ground plane or internal power plane, θ_{JA} can vary from 30°C/W to more than 40°C/W. Other mounting techniques may provide better power dissipation than 30°C/W.

Absolute Maximum Ratings

Parameter	Min.	Max.	Unit
V_{IN}		7	V
Operating Junction Temperature Range	0	125	°C
Storage Temperature Range	-65	150	°C
Lead Temperature (Soldering, 10 seconds)		300	°C

Preliminary Information

Electrical Characteristics

Operating Conditions: $4.75 \leq V_{IN} < 5.25V$, $T_j = 25^\circ C$ unless otherwise specified.

Parameter	Conditions	Min.	Typ.	Max	Units
Reference Voltage ³	Adj connected to ground, $I_{OUT} = 10mA$	1.23	1.250	1.27	V
Output Voltage ⁵	$I_{OUT} = 10mA$	1.475	1.5	1.525	V
Line Regulation ^{1, 2}	$I_{OUT} = 10mA$		0.5	2	%
Load Regulation ^{1, 2}	$10mA \leq I_{OUT} \leq 4.5A$		0.5	2.5	%
Dropout Voltage	$\Delta V_{REF} = 2\%$, $I_{OUT} = 4.5A$			1.5	V
Current Limit	$(V_{IN} - V_{OUT}) = 2V$		5.5		A
Adjust Pin Current ³			35	100	μA
Mimumum Load Current ⁴	$1.5V \leq (V_{IN} - V_{OUT}) \leq 5.75V$		10		mA
Quiescent Current ⁴	$V_{IN} = 5V$		4		mA
Thermal Resistance, Junction to Case	TO-220		3		$^\circ C/W$
	TO-263		10		$^\circ C/W$
Thermal Shutdown ⁴			150		$^\circ C$

Notes:

1. See thermal regulation specifications for changes in output voltage due to heating effects. Load and line regulation are measured at a constant junction temperature by low duty cycle pulse testing.
2. Line and load regulation are guaranteed up to the maximum power dissipation. Power dissipation is determined by input/output differential and the output current. Guaranteed maximum output power will not be available over the full input/output voltage range.
3. FAN1084 only.
4. Guaranteed by design.
5. FAN1084-1.5 only.

Typical Performance Characteristics

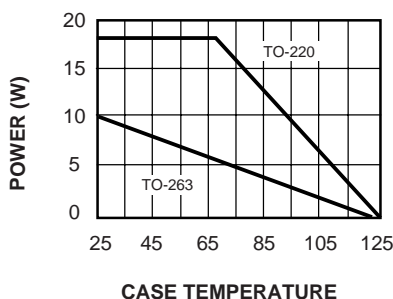


Figure 1. Maximum Power Dissipation

Applications Information

General

The FAN1084 and FAN1084-1.5 are three-terminal regulators optimized for $GTL+V_{TT}$ termination and logic applications. These devices are short-circuit protected, and offer thermal shutdown to turn off the regulator when the junction temperature exceeds about 150°C . The FAN1084 series provides low dropout voltage and fast transient response. Frequency compensation uses capacitors with low ESR while still maintaining stability. This is critical in addressing the needs of low voltage high speed microprocessor buses like $GTL+$.

Stability

The FAN1084 series requires an output capacitor as a part of the frequency compensation. It is recommended to use a $22\mu\text{F}$ solid tantalum or a $100\mu\text{F}$ aluminum electrolytic on the output to ensure stability. The frequency compensation of these devices optimizes the frequency response with low ESR capacitors. In general, it is suggested to use capacitors with an ESR of $<1\Omega$. It is also recommended to use bypass capacitors such as a $22\mu\text{F}$ tantalum or a $100\mu\text{F}$ aluminum on the adjust pin of the FAN1084 for low ripple and fast transient response. When these bypassing capacitors are not used at the adjust pin, smaller values of output capacitors provide equally good results.

Protection Diodes

In normal operation, the FAN1084 series does not require any protection diodes. For the FAN1084, internal resistors limit internal current paths on the adjust pin. Therefore, even with bypass capacitors on the adjust pin, no protection diode is needed to ensure device safety under short-circuit conditions.

A protection diode between the input and output pins is usually not needed. An internal diode between the input and the output pins on the FAN1084 series can handle microsecond surge currents of 50A to 100A. Even with large value output capacitors it is difficult to obtain those values of surge currents in normal operation. Only with large values of output capacitance, such as $1000\mu\text{F}$ to $5000\mu\text{F}$, and with the input pin instantaneously shorted to ground can damage occur. A crowbar circuit at the input can generate those levels of current; a diode from output to input is then recommended, as shown in Figure 2. Usually, normal power supply cycling or system "hot plugging and unplugging" will not generate current large enough to do any damage.

The adjust pin can be driven on a transient basis $\pm 7\text{V}$ with respect to the output, without any device degradation. As with any IC regulator, exceeding the maximum input-to-output voltage differential causes the internal transistors to break down and none of the protection circuitry is then functional.

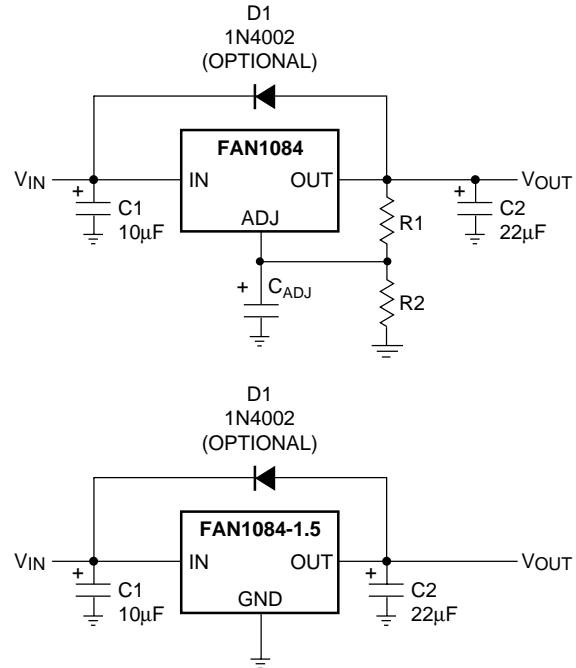


Figure 2. Optional Protection

Ripple Rejection

In applications that require improved ripple rejection, a bypass capacitor from the adjust pin of the FAN1084 to ground reduces the output ripple by the ratio of $V_{OUT}/1.25\text{V}$. The impedance of the adjust pin capacitor at the ripple frequency should be less than the value of $R1$ (typically in the range of 100Ω to 120Ω) in the feedback divider network in Figure 2. Therefore, the value of the required adjust pin capacitor is a function of the input ripple frequency. For example, if $R1$ equals 100Ω and the ripple frequency equals 120Hz , the adjust pin capacitor should be $22\mu\text{F}$. At 10kHz , only $0.22\mu\text{F}$ is needed.

Output Voltage

The FAN1084 regulator develops a 1.25V reference voltage between the output pin and the adjust pin (see Figure 3). Placing a resistor $R1$ between these two terminals causes a constant current to flow through $R1$ and down through $R2$ to set the overall output voltage. Normally, this current is the specified minimum load current of 10mA .

The current out of the adjust pin adds to the current from R1. Its output voltage contribution is small and only needs consideration when a very precise output voltage setting is required.

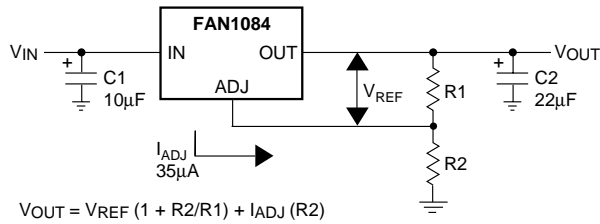


Figure 3. Basic Regulation Circuit

Load Regulation

It is not possible to provide true remote load sensing because the FAN1084 series are three-terminal devices. Load regulation is limited by the resistance of the wire connecting the regulator to the load. Load regulation per the data sheet specification is measured at the bottom of the package.

For fixed voltage devices, negative side sensing is a true Kelvin connection with the ground pin of the device returned to the negative side of the load. This is illustrated in Figure 4.

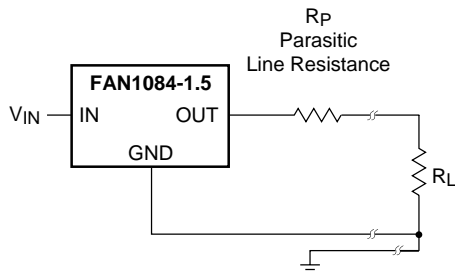


Figure 4. Connection for Best Load Regulation

For adjustable voltage devices, negative side sensing is a true Kelvin connection with the bottom of the output divider returned to the negative side of the load. The best load regulation is obtained when the top of the resistor divider R1 connects directly to the regulator output and not to the load. Figure 5 illustrates this point.

If R1 connects to the load, then the effective resistance between the regulator and the load would be:

$$R_p \times (1 + R_2/R_1), R_p = \text{Parasitic Line Resistance}$$

The connection shown in Figure 5 does not multiply R_p by the divider ration. As an example, R_p is about four milliohms per foot with 16-gauge wire. This translates to 4mV per foot at 1A load current. At higher load currents, this drop represents a significant percentage of the overall regulation. It is important to keep the positive lead between the regulator and the load as short as possible and to use large wire or PC board traces.

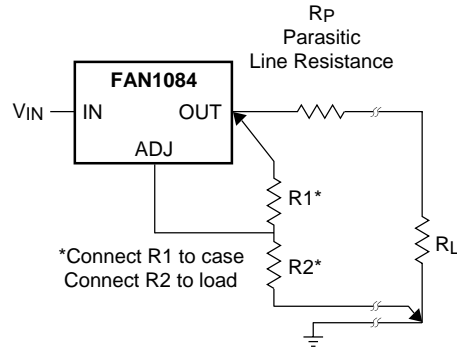


Figure 5. Connection for Best load Regulation

Thermal Conditions

The FAN1084 series protect themselves under overload conditions with internal power and thermal limiting circuitry. However, for normal continuous load conditions, do not exceed maximum junction temperature ratings. It is important to consider all sources of thermal resistance from junction-to-ambient. These sources include the junction-to-case resistance, the case-to-heatsink interface resistance, and the heat sink resistance. Thermal resistance specifications have been developed to more accurately reflect device temperature and ensure safe operating temperatures. The electrical characteristics section provides a separate thermal resistance and maximum junction temperature for both the control circuitry and the power transistor. Calculate the maximum junction temperature for both sections to ensure that both thermal limits are met.

For example, look at using an FAN1084T to generate 4.5A @ 1.5V from a 3.3V source (3.2V to 3.6V).

Assumptions

- $V_{IN} = 3.6V$ worst case
- $V_{OUT} = 1.475V$ worst case
- $I_{OUT} = 4.5A$ continuous
- $T_A = 60^\circ C$
- $\theta_{\text{Case-to-Ambient}} = 5^\circ C/W$ (assuming both a heatsink and a thermally conductive material)

The power dissipation in this application is:

$$PD = (V_{IN} - V_{OUT}) * (I_{OUT}) = (3.6 - 1.475) * (4.5) = 9.6W$$

From the specification table:

$$T_J = T_A + (PD) * (\theta_{\text{Case-to-Ambient}} + \theta_{JC}) = 60 + (9.6) * (5 + 3) = 137^\circ C$$

The junction temperature is below the maximum thermal limit.

Junction-to-case thermal resistance is specified from the IC junction to the bottom of the case directly below the die. This is the lowest resistance path for heat flow. Proper mounting ensures the best thermal flow from this area of the package to the heat sink. Use of a thermally conductive material at the

case-to-heat sink interface is recommended. Use a thermally conductive spacer if the case of the device must be electrically isolated and include its contribution to the total thermal resistance. The cases of the FAN1084 series are directly connected to the output of the device.

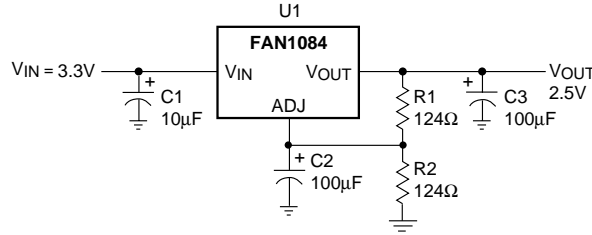


Figure 6. Application Circuit

Table 1. Bill of Materials for Application Circuit for the FAN1084

Item	Quantity	Manufacturer	Part Number	Description
C1	1	Xicon	L10V10	10µF, 10V Aluminum
C2, C3	2	Xicon	L10V100	100µF, 10V Aluminum
R1, R2	2	Generic		124Ω, 1%
U1	1	Fairchild	FAN1084T	4.5A Regulator

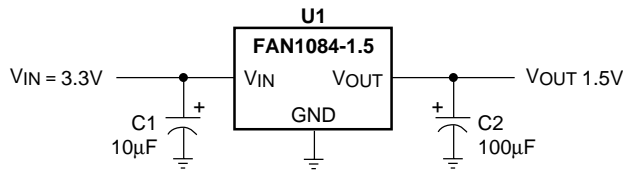


Figure 7. Application Circuit (FAN1084-1.5)

Table 2. Bill of Materials for Application Circuit for the FAN1084-1.5

Item	Quantity	Manufacturer	Part Number	Description
C1	1	Xicon	L10V10	10µF, 10V Aluminum
C2	1	Xicon	L10V100	100µF, 10V Aluminum
U1	1	Fairchild	FAN1084T-1.5	4.5A Regulator

Preliminary Information

Mechanical Dimensions

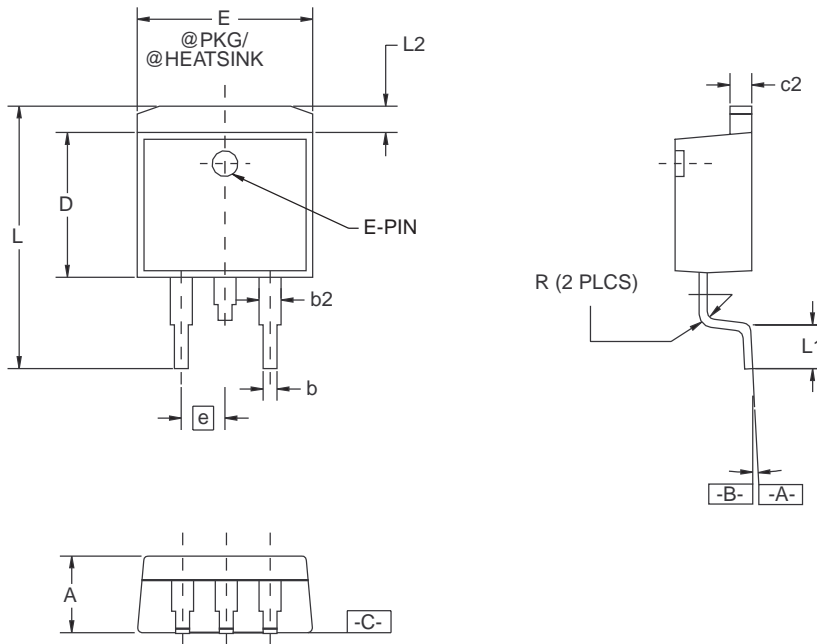
3-Lead TO-263 Center Cut Package

Symbol	Inches		Millimeters		Notes
	Min.	Max.	Min.	Max.	
A	.160	.190	4.06	4.83	
b	.020	.039	0.51	0.99	
b2	.049	.051	1.25	1.30	
c2	.045	.055	1.14	1.40	
D	.340	.380	8.64	9.65	
E	.380	.405	9.65	10.29	
e	.100 BSC		2.54 BSC		
L	.575	.625	14.61	15.88	
L1	.090	.110	2.29	2.79	
L2	—	.055	—	1.40	
R	.017	.019	0.43	0.48	
α	0°	8°	0°	8°	

Notes:

1. Dimensions are exclusive of mold flash and metal burrs.
2. Standoff-height is measured from lead tip with ref. to Datum -B-.
3. Foot length is measured with ref. to Datum -A- with lead surface (at inner R).
4. Dimension exclusive of dambar protrusion or intrusion.
5. Formed leads to be planar with respect to one another at seating place -C-.

Preliminary Information

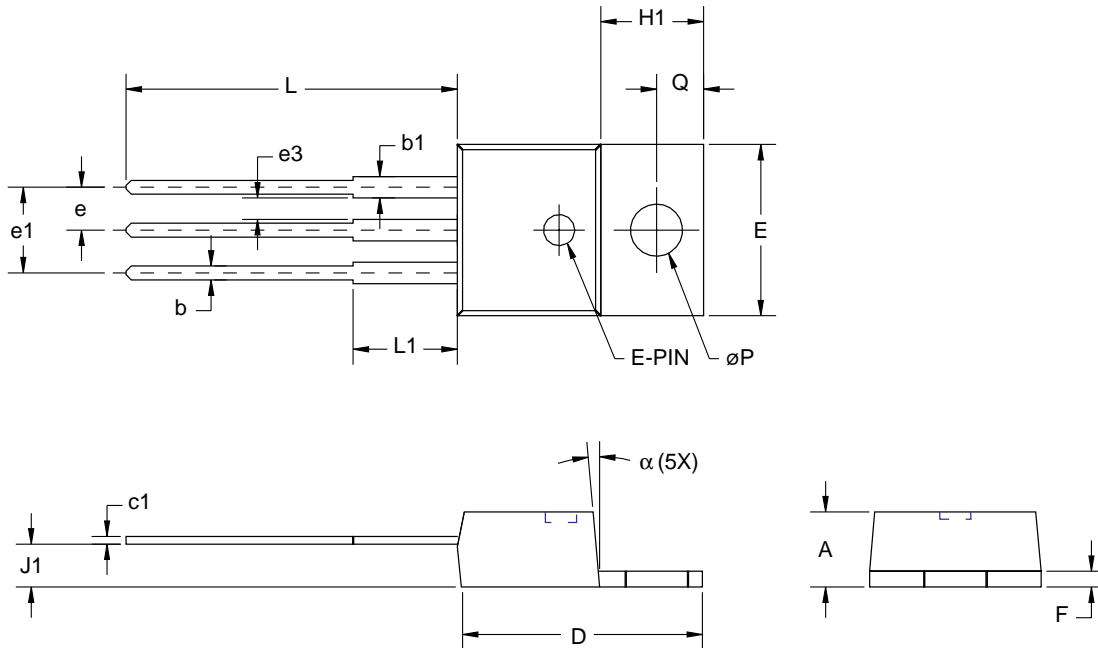


Mechanical Dimensions (continued)

3-Lead TO-220 Package

Symbol	Inches		Millimeters		Notes
	Min.	Max.	Min.	Max.	
A	.140	.190	3.56	4.83	
b	.015	.040	.38	1.02	
b1	.045	.070	1.14	1.78	
c1	.014	.022	.36	.56	
øP	.139	.161	3.53	4.09	
D	.560	.650	14.22	16.51	
E	.380	.420	9.65	10.67	
e	.090	.110	2.29	2.79	
e1	.190	.210	4.83	5.33	
e3	.045	—	1.14	—	
F	.020	.055	.51	1.40	
H1	.230	.270	5.94	6.87	
J1	.060	.115	2.04	2.92	
L	.500	.580	12.70	14.73	
L1	.250 BSC		6.35 BSC		
Q	1.00	1.35	2.54	3.43	
α	3°	7°	3°	7°	

Notes:
 1. Dimension c1 apply for lead finish.



Preliminary Information

Ordering Information

Product Number	Package
FAN1084MC	TO-263
FAN1084T	TO-220
FAN1084MC-1.5	TO-263
FAN1084T-1.5	TO-220

Preliminary Information

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