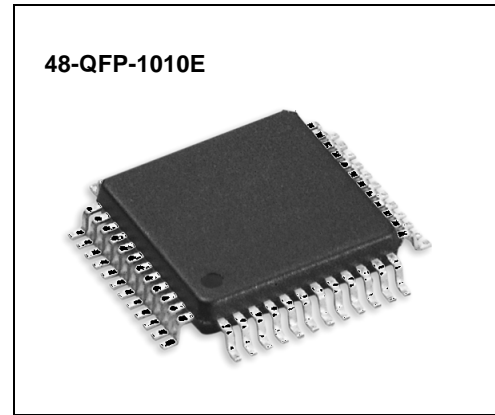


ZOOM & REEL MOTOR DRIVER AND CONTROLLER

The KA7406 is a monolithic integrated circuit, and suitable for the zoom & reel motor driver for camera, tape deck, any other consumer and industrial applications. The KA7406 has the functions which drive buffer for flash & battery check and auto-focus magnetic control.



FEATURES

- Output current up to 0.8A (Each channel).
- 4 function mode (CW, CCW, STOP and BRAKE) are controlled by 2 logic signals fed into 2 input terminals.
- Operating voltage range: $V_{CC}=2.5 \sim 7V$. (Exception battery check function)
- Built-in spark killer diode.
- Low saturation voltage (1.5V max at 600mA).

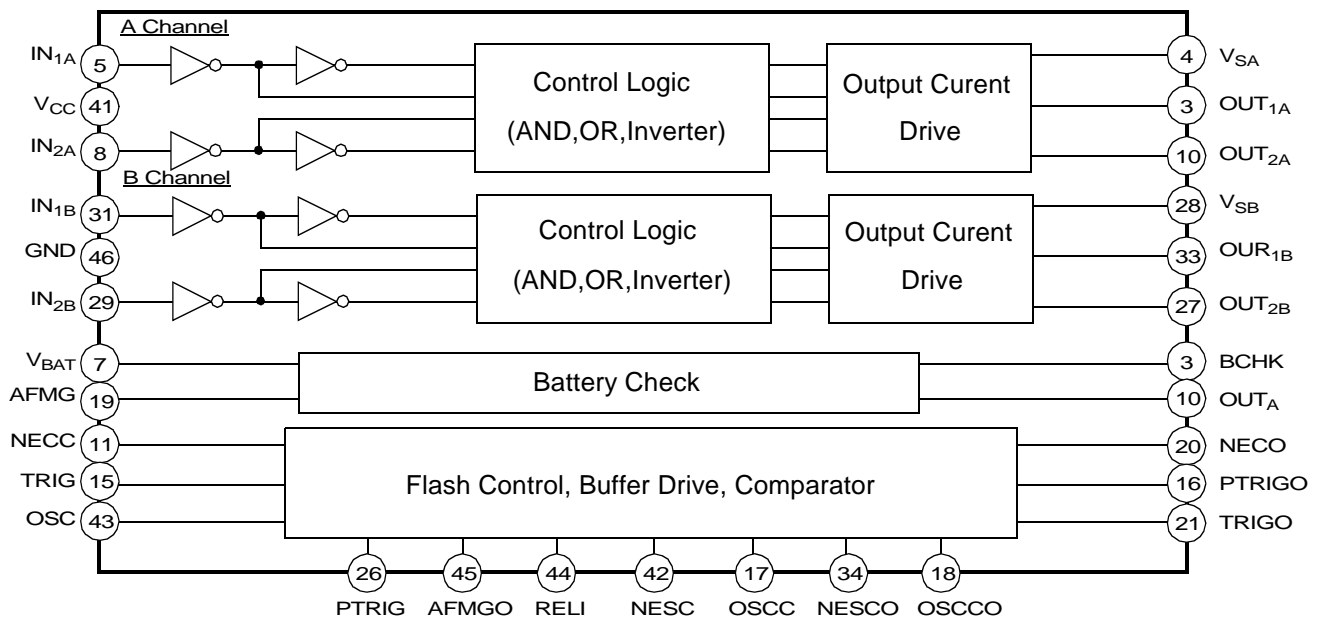
ORDERING INFORMATION

Device	Package	Operating Temperature
KA7406	48-QFP-1010E	-25°C to +75°C

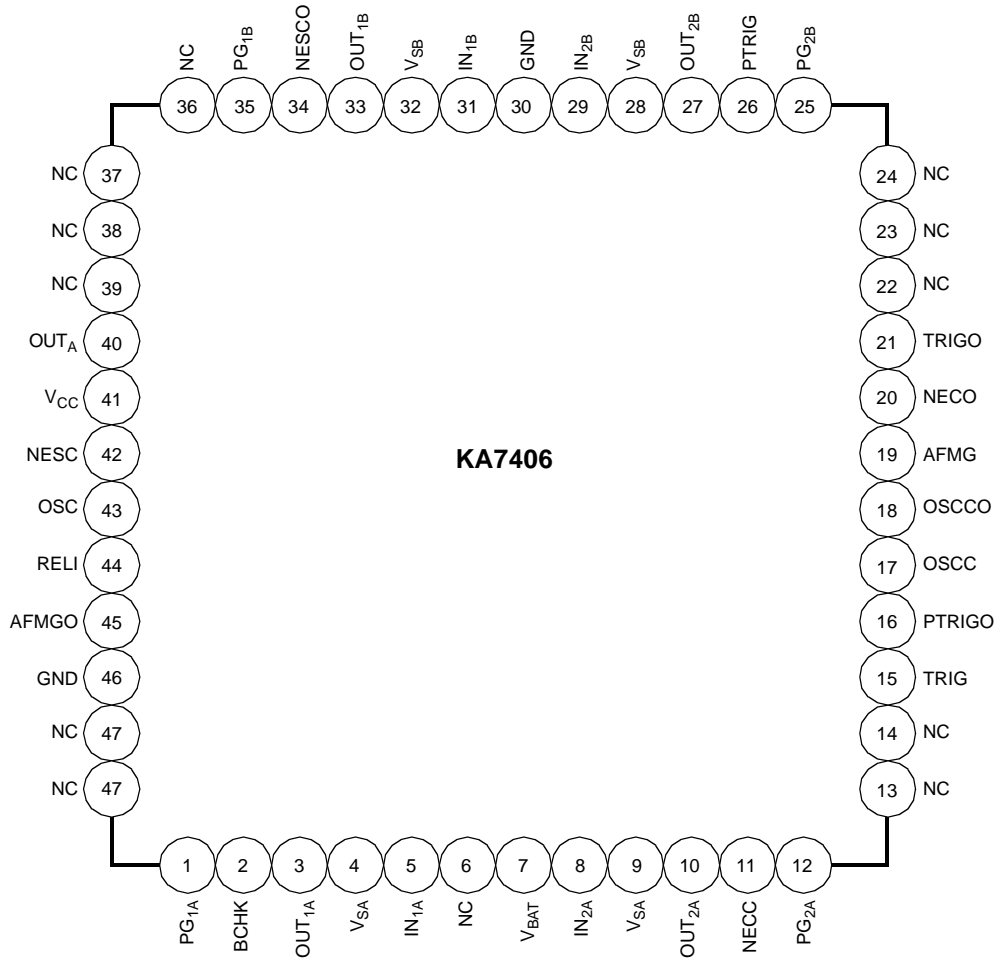
TARGET APPLICATIONS

- Camera System

BLOCK DIAGRAM



PIN CONFIGURATIONS



PIN DESCRIPTIONS

Pin No.	Symbol	I/O	Define	Remark
1	PG _{1A}	–	Power ground 1	Channel A
2	BCHK	O	Battery check output	–
3	OUT _{1A}	O	Output 1	Channel A
4	V _{SA}	–	Output supply voltage	Channel A
5	IN _{1A}	I	Input 1	Channel A
6	NC	–	No connection	–
7	V _{BAT}	–	Power supply voltage	Channel A
8	IN _{2A}	I	Input 2	Channel A
9	V _{SA}	–	Output supply voltage	Channel A
10	OUT _{2A}	O	Output 2	–
11	NECC	I	Flash charge control input	–
12	PG _{2A}	–	Power ground 2	–
13	NC	–	No connection	–
14	NC	–	No connection	–
15	TRIG	I	Flash trigger input	–
16	PTRIGO	O	Pretrigger output	–
17	OSCC	I	Flash charge control input	–
18	OSCCO	O	Flash charge control output	–
19	AFMG	I	Auto focus magnetic control input	–
20	NECO	I	Flash charge control input	–
21	TRIGO	O	Flash trigger output	–
22	NC	–	No connection	–
23	NC	–	No connection	–
24	NC	–	No connection	–
25	PG _{2B}	–	Power ground 2	Channel B
26	PTRIG	I	Flash control input	–
27	OUT _{2B}	O	Output 2	Channel B
28	V _{SB}	–	Output supply voltage	Channel B
29	IN _{2B}	I	Input 2	Channel B
30	GND	–	Signal ground	–
31	IN _{1B}	I	Input 2	Channel B
32	V _{SB}	–	Output supply voltage	Channel B

PIN DESCRIPTIONS (Continued)

Pin No.	Symbol	I/O	Define	Remark
33	OUT _{1B}	O	Output 1	Channel B
34	NESCO	I	Battery check output	–
35	PG _{1B}	–	Power supply voltage ground	Channel B
36	NC	–	No connection	–
37	NC	–	No connection	–
38	NC	–	No connection	–
39	NC	–	No connection	–
40	OUT _A	O	Battery check TR driving signal	–
41	V _{CC}	–	Regulator output voltage	Effective RELI=Low Channel
42	NESC	I	Battery check input	–
43	OSC	I	Oscillator input	–
44	RELI	I	Battery check input	–
45	AFMGO	O	Auto focus magnetic control output	–
46	GND	–	AFMG control TR ground	–
47	NC	–	No connection	–
48	NC	–	No connection	–

EQUIVALENT CIRCUITS

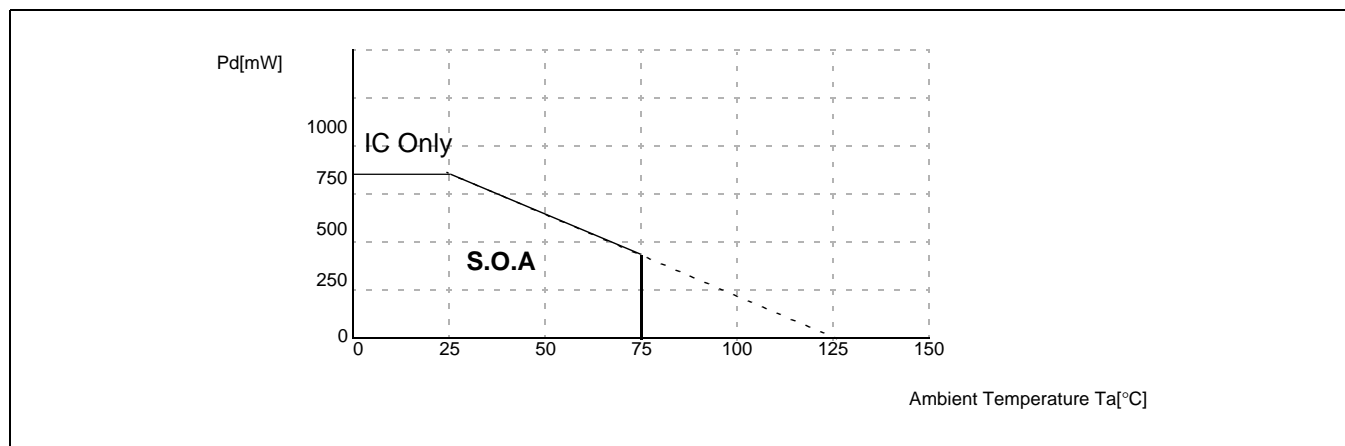
Description	Pin No.	Internal Circuit
DC Motor Control Input		
IN _{1A}	5	
IN _{2A}	8	
IN _{1B}	31	
IN _{2B}	29	
V _{CC}	41	
GND	30	
DC Motor Output		
OUT _{1A}	3	
OUT _{2A}	10	
OUT _{1B}	33	
OUT _{2B}	27	
V _{SA}	4, 9	
V _{SB}	28, 32	
PG _{1A}	1	
PG _{2A}	12	
PG _{1B}	35	
PG _{2B}	25	
Comparator		
NESC	42	
NESCO	34	
V _{BAT}	7	
GND	30	

EQUIVALENT CIRCUITS (Continued)

Description	Pin No.	Internal Circuit
Autofocus Circuit		
AFMG	19	
AFMGO	45	
VBAT	7	
GND	46	
Discreat Circuit		
PTRIG	26	
TRIG	15	
NECC	11	
PTRIGO	16	
TRIGO	21	
NECO	20	
VBAT	7	
Regulator Circuit		
RELI	44	
V _{CC}	41	
VBAT	7	
Flash Charge		
OSCC	17	
OSCCO	18	
GND	46	

ABSOLUTE MAXIMUM RATING (Ta=25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V_{BAT}	10	V
Channel supply voltage	V_S	10	V
Power dissipation	P_D	750	mW
Operating temperature	T_{OPR}	-25 ~ +75	°C
Storage temperature	T_{STG}	-40 ~ +125	°C
Output current	I_O	1	A

**RECOMMENDED OPERATING CONDITIONS (Ta=25°C)**

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Operating supply voltage	V_{CC}	2.5	-	7.0	V

ELECTRICAL CHARACTERISTICS

(V_{CC}=5V, T_a=25°C, unless otherwise specified)

Characteristic	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Supply current 1	I _{CC1}	V _{IN(all)} = 0V	–	0.1	10	μA
Supply current 2	I _{CC2}	V _{IN1} =3V	–	15	30	mA
Supply current 3	I _{CC3}	V _{IN2} =3V	–	15	30	mA
Supply current 4	I _{CC4}	V _{IN} =3V	–	30	50	mA
Input current	I _{IN}	V _{CC} =5V, V _{IN} =2V	–	45	80	μA
Leakage current	I _{IK}	V _{CC} =7V	–	0.1	10	μA
Upper spark diode forward voltage 1	V _{SF1}	I _O =500mA	–	1.0	1.7	V
Lower spark diode forward voltage 2	V _{SF2}	I _O =500mA	–	1.0	1.7	V
Output saturation voltage 1A	V _{O1A}	I _{OA} =200mA	–	0.45	0.70	V
Output saturation voltage 1B	V _{O1B}	I _{OB} =200mA	–	0.45	0.70	V
Output saturation voltage 2A	V _{O2A}	I _{OA} =400mA	–	1.0	1.5	V
Output saturation voltage 2B	V _{O2B}	I _{OB} =400mA	–	1.0	1.5	V
Output saturation voltage 3A	V _{O3A}	I _{OA} =200mA	–	0.45	0.70	V
Output saturation voltage 3B	V _{O3B}	I _{OB} =200mA	–	0.45	0.70	V
Output saturation voltage 4A	V _{O4A}	I _{OA} =400mA	–	1.0	1.5	V
Output saturation voltage 4B	V _{O4B}	I _{OB} =400mA	–	1.0	1.5	V
Output saturation voltage 5	V _{O5}	I _{OB} =400mA	–	0.6	0.8	V
Output saturation voltage 6	V _{O6}	I _O =400mA	–	0.6	0.8	V
Output saturation voltage 7	V _{O7}	I _O =800mA	–	1.2	1.6	V
Output saturation voltage 8	V _{O8}	I _O =800mA	–	1.2	1.6	V
OUTA terminal output current	I _{OUTA}	V _{OUTA} =0.7A	6	15	24	mA
Battery check time	T _{CHECK}	R _{OSC} =72k, C _{OSC} =0.1μF	8	10	12	ms
Battery check reference voltage	V _{CHECK}	V _{BAT} =4~5V	4.4	4.5	4.6	V
Comparator reference voltage	V _{TH}	V _{nesc} =1~2V	1.15	1.25	1.35	V
RELI terminal input current	I _{RELI}	V _{RELI} =0V	–	5	7	mA
V _{CC} terminal output saturation voltage	V _{VCC}	I _O =150mA	–	0.3	0.5	V
AFMG terminal input current	I _{AFMG}	V _{AFMG} =5V	–	145	190	μA
AFMGO output saturation voltage	V _{AFMGO}	I _O =100mA	–	0.3	0.5	V
PTRIG terminal input current	I _{PTRIG}	V _{PTRIG} =5V	–	145	190	μA
PTRIGO output saturation voltage	V _{PTRIGO}	I _O =10mA	–	0.3	0.5	V

ELECTRICAL CHARACTERISTICS (Continued)(V_{CC}=5V, T_a=25°C, unless otherwise specified)

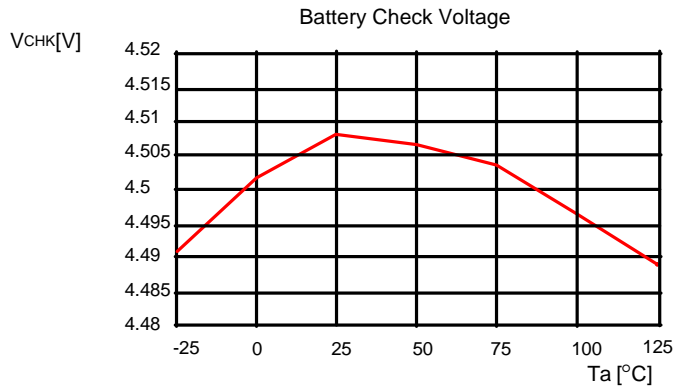
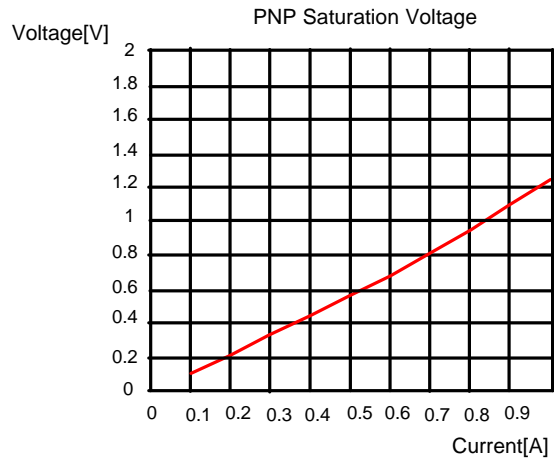
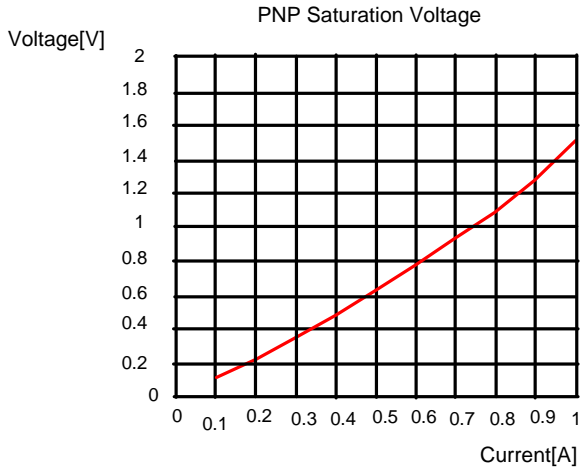
Characteristic	Symbol	Test conditions	Min.	Typ.	Max.	Unit
TRIG terminal input current	I _{TRIG}	V _{TRIG} =5V	–	145	190	μA
TRIGO terminal voltage	V _{TRIGO}	V _{TRIG} =5V	0.84	0.94	1.04	V
NECC terminal input current	I _{NECC}	V _{NECC} =5V	–	145	190	mA
NECO terminal output current	I _{NECO}	V _{NECO} =0V	1.0	1.15	1.3	V
OSCC terminal input current	I _{OSCC}	V _{OSCC} =5V	–	500	700	μA
OSCCO output saturation voltage	I _{OSCCO}	V _{OSCCO} =10mA	–	0.3	0.5	V

MOTOR OPERATION TRUTH TABLE

Motor Operation \ Input/Output	Input 1	Input 2	Output 1	Output 2	Remark
Stop	Low	Low	Off	Off	High impedance
Forward operation	Low	High	Low	High	CW / CCW
Backward operation	High	Low	High	Low	CCW / CW
Fast stop	High	High	Low	Low	Brake

CHARACTERISTIC GRAPHS

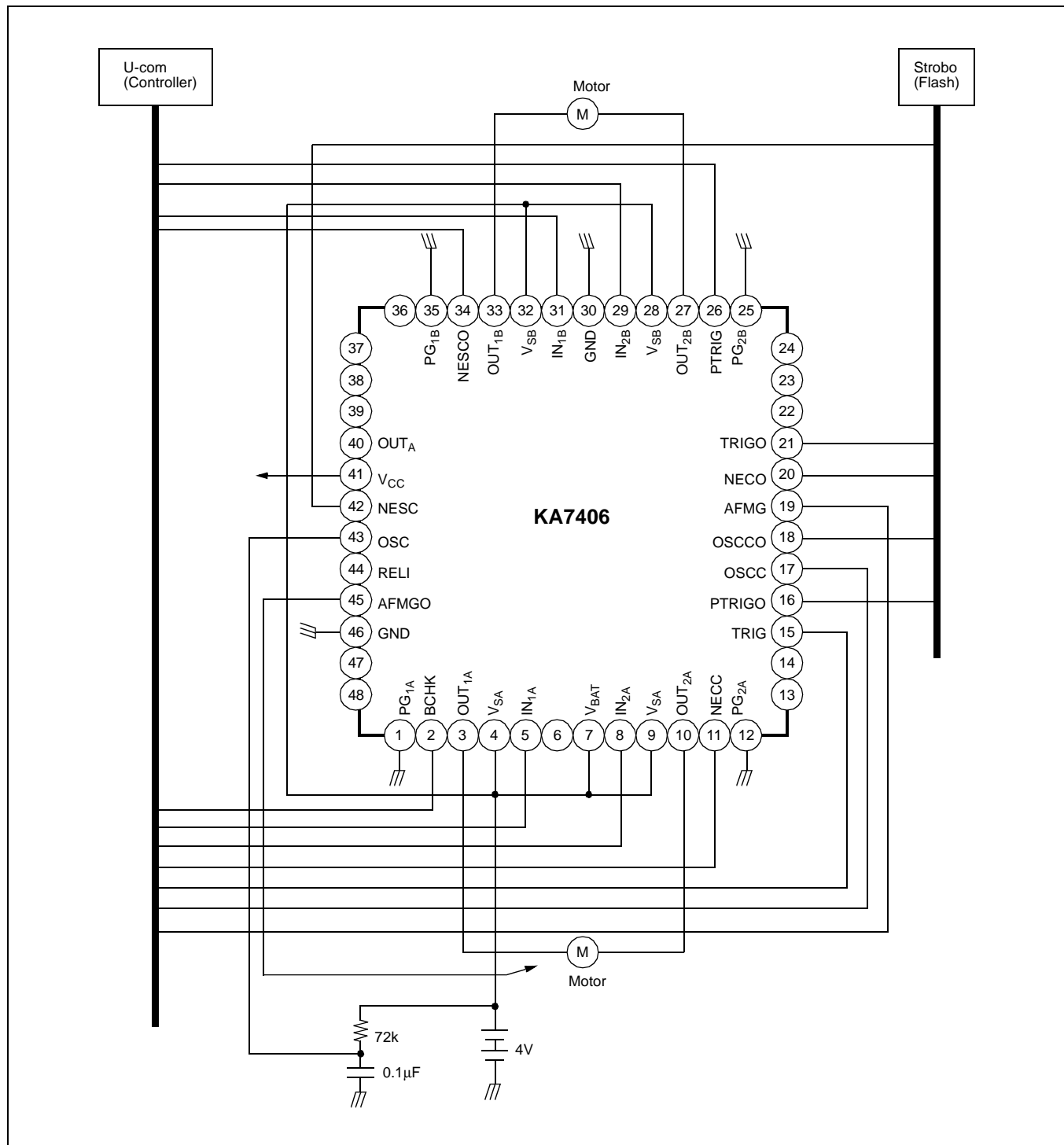
(($V_{CC}=5V$, $T_a=25^{\circ}C$, unless otherwise specified))



TEST CONDITIONS

Characteristics	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW9	SW10	Remark
I_{CC1}	Off	Off	X	b	b	Off	Off	X	X	Off	Supply current
I_{CC2}	Off	Off	On	a	b	Off	Off	X	X	Off	Supply current
I_{CC3}	Off	Off	On	b	a	Off	Off	X	X	Off	Supply current
I_{CC4}	Off	Off	On	a	a	Off	Off	X	X	Off	Supply current
I_{IN}	Off	Off	Off	a	a	Off	Off	X	X	Off	Input current
I_{LK}	Off	Off	X	b	b	Off	Off	X	X	Off	Leakage current
V_{SF1}	On	On	On	a	b	On	On	Off	Off	a	Spark diode
V_{SF2}	On	On	On	b	a	On	On	Off	Off	b	Spark diode
V_{O1A}	On	On	On	a	b	On	On	On	On	Off	Single mode
V_{O2A}	On	On	On	b	a	On	On	On	On	Off	Single mode
V_{O3A}	On	On	On	a	b	On	On	On	On	Off	Single mode
V_{O4A}	On	On	On	b	a	On	On	On	On	Off	Single mode
V_{O5}	On	On	On	a	b	On	On	On	On	Off	Parallel mode
V_{O6}	On	On	On	b	a	On	On	On	On	Off	Parallel mode
V_{O7}	On	On	On	a	b	On	On	On	On	Off	Parallel mode
V_{O8}	On	On	On	b	a	On	On	On	On	Off	Parallel mode
V_{SUS}	Off	Off	X	b	b	On	On	On	On	Off	Sustain voltage
Characteristics	SW1	SW2	SW11	SW12	–	–	–	–	–	–	Remark
I_{OUT}	On	On	a	On	–	–	–	–	–	–	–
T_{CHK}	On	On	b	Off	–	–	–	–	–	–	–
V_{CHK}	On	On	X	X	–	–	–	–	–	–	–
V_{TH}	On	On	X	X	–	–	–	–	–	–	–
I_{REL1}	On	On	X	X	–	–	–	–	–	–	–
V_{VCC}	On	On	X	X	–	–	–	–	–	–	–
I_{AFMGO}	On	On	X	X	–	–	–	–	–	–	–
V_{AFMGO}	On	On	X	X	–	–	–	–	–	–	–
I_{PTRG}	On	On	X	X	–	–	–	–	–	–	–
V_{PTRGO}	On	On	X	X	–	–	–	–	–	–	–
I_{TRIG}	On	On	X	X	–	–	–	–	–	–	–
V_{TRIGO}	On	On	X	X	–	–	–	–	–	–	–
I_{NECC}	On	On	X	X	–	–	–	–	–	–	–
I_{NECO}	On	On	X	X	–	–	–	–	–	–	–
I_{OSCC}	On	On	X	X	–	–	–	–	–	–	–
V_{OSCCO}	On	On	X	X	–	–	–	–	–	–	–

APPLICATION CIRCUIT



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