

SANYO**LB1997**

Three-Phase Brushless Motor Driver for CD-ROM Spindle Drive

Overview

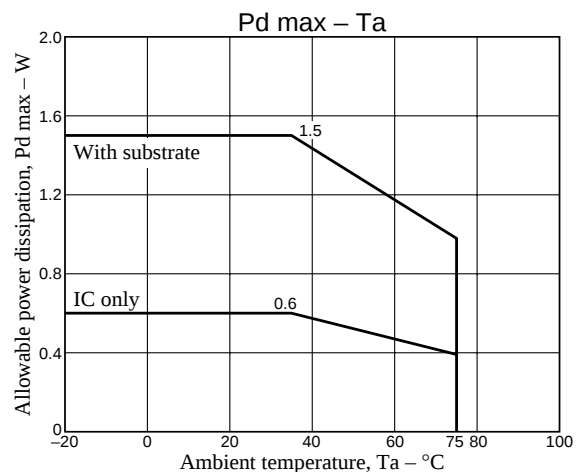
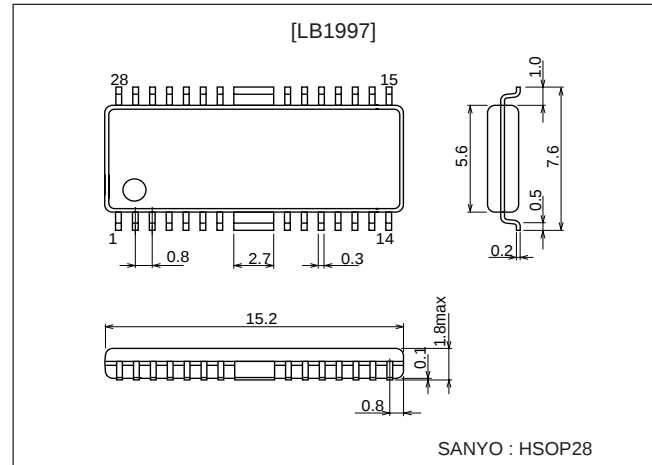
The LB1997 is a three-phase brushless motor driver especially suited for CD-ROM spindle motor drives.

Functions

- Current linear drive
- Control V type amplifier
- Separate power supply for output upper side bias circuit allows low output saturation by boosting this power supply only (useful for 5V power supply types).
- Upper side current detection technique reduces loss voltage of current detection resistor. Voltage effect of this resistor reduces internal current drain of IC.
- Built-in short braking circuit
- Built-in reverse blocking circuit
- Hall FG output
- Built-in S/S function
- Built-in current limiter circuit
- Built-in Hall power supply
- Built-in thermal shutdown circuit
- V type control/single-side control switching pin allows CLV and CAV operation.

Package Dimensions

unit: mm

3222-HSOP28

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Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	V _{CC1} max		7.0	V
	V _{CC2} max		14.4	V
	V _{CC3} max		14.4	V
Applied output voltage	V _O max		14.4	V
Applied input voltage	V _{IN} max		V _{CC1}	V
Output current	I _O max		1.3	A
Allowable power dissipation	Pd max	IC only	0.6	W
		with substrate (114.3 × 76.1 × 1.6 mm ³ , glass epoxy)	1.5	W
Operating temperature	T _{opr}		−20 to +75	°C
Storage temperature	T _{stg}		−55 to +150	°C

Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	V _{CC1}		4 to 6	V
	V _{CC2}	≥ V _{CC1}	4 to 13.6	V
	V _{CC3}		4 to 13.6	V

Sample Application at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
12V type	V _{CC1}	Regulated voltage	4 to 6	V
	V _{CC2} = V _{CC3}	Unregulated voltage	4 to 13.6	V
5V type	V _{CC1} = V _{CC3}	Regulated voltage	4 to 6	V
	V _{CC2}	Boost-up voltage or regulated voltage (Note)	4 to 13.6	V

Note: When boost-up voltage is used at V_{CC2}, output can be set to low-saturation.

Electrical Characteristics at Ta = 25°C, V_{CC1} = 5V, V_{CC2} = V_{CC3} = 12V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[Power supply current]						
Power supply current	I _{CC1}	V _C = V _{CREF}		8		mA
	I _{CC2}	V _C = V _{CREF}		0		mA
	I _{CC3}	V _C = V _{CREF}		150	250	μA
Output idle current	I _{CC1OQ}	V _{S/S} = 0V			200	μA
	I _{CC2OQ}	V _{S/S} = 0V			30	μA
	I _{CC3OQ}	V _{S/S} = 0V			30	μA
[Output]						
Saturation voltage, upper side 1 lower side 1	V _{OU1}	I _O = −0.5A, V _{CC1} = 5V, V _{CC2} = V _{CC3} = 12V		1.0		V
	V _{OD1}	I _O = 0.5A, V _{CC1} = 5V, V _{CC2} = V _{CC3} = 12V		0.3		V
Saturation voltage, upper side 2 lower side 2	V _{OU2}	I _O = −0.5A, V _{CC1} = V _{CC3} = 5V, V _{CC2} = 12V		0.3		V
	V _{OD2}	I _O = 0.5A, V _{CC1} = V _{CC3} = 5V, V _{CC2} = 12V		0.3		V
Current limiter setting voltage	V _{CL}	R _{RF} = 0.43Ω		0.37		V
[Hall amplifier]						
Common mode input voltage range	V _{HCOM}		1.2		V _{CC} −1.0	V
Input bias current	I _{HIB}			1		μA
Minimum Hall input level	V _{HIN}		60			mV _{P-P}
[S/S pin]						
High level voltage	V _{S/SH}		2.0		V _{CC1}	V
Low level voltage	V _{S/SL}				0.7	V
Input current	I _{S/SI}	V _{S/S} = 5V			200	μA
Leak current	I _{S/SL}	V _{S/S} = 0V	−30			μA
[Control]						
	I _{VC}	V _C = V _{CREF} = 2.5V		1		μA
	I _{VCREF}	V _C = V _{CREF} = 2.5V		1		μA
Voltage gain	GV _{CO}	ΔV _{RF} /ΔV _C		0.25		times
Startup voltage	V _{CTH}	V _{CREF} = 2.5V	2.35		2.65	V
Startup voltage width	ΔV _{CTH}	V _{CREF} = 2.5V	50		150	mV
[Hall power supply]						
Hall power supply voltage	V _H	I _H = 5mA		0.8		V
Allowable current	I _H		20			mA
[Thermal shutdown]						
Operating temperature	T _{TSD}	Design target value	150	180	210	°C
Hysteresis	ΔT _{TSD}	Design target value		15		°C
[Short braking]						
Brake pin at High level	V _{BRH}		4		5	V
Brake pin at Low level	V _{BRL}		0		1	V
[Control switching]						
CC pin at High level	V _{CCH}		4		5	V
CC pin at Low level	V _{CCL}		0		1	V

Note:

- During S/S OFF (standby), the Hall comparator is at High.
- Design target values are not measured.

Truth Table

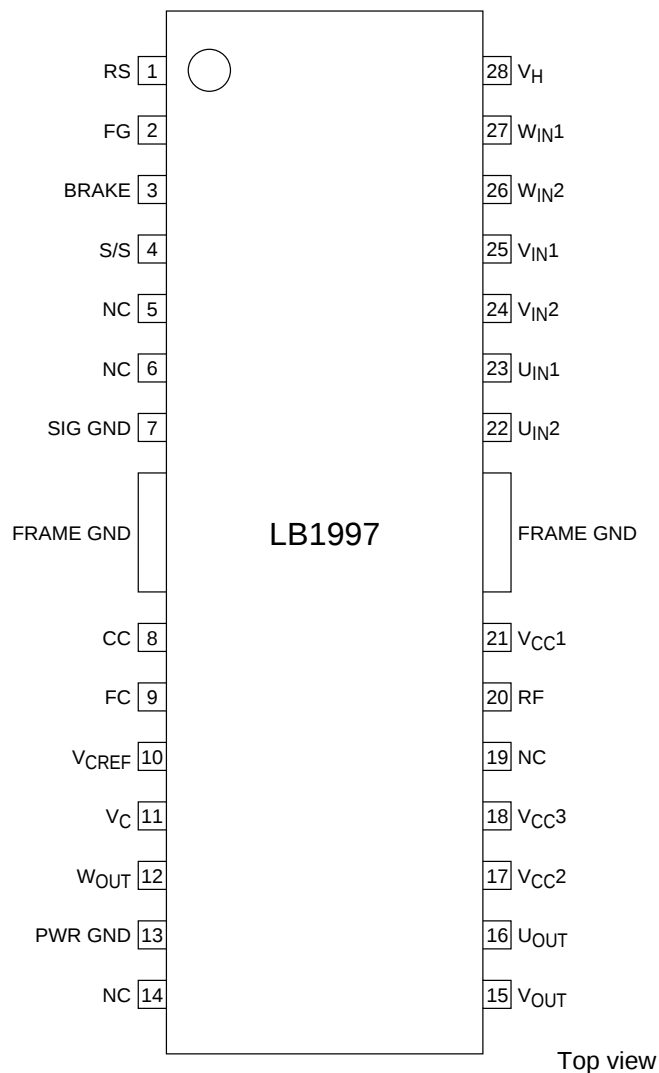
	Source → Sink	Hall input			Control V_C
		U	V	W	
1	Phase W → Phase V	H	H	L	H
	Phase V → Phase W				L
2	Phase W → Phase U	H	L	L	H
	Phase U → Phase W				L
3	Phase V → Phase W	L	L	H	H
	Phase W → Phase V				L
4	Phase U → Phase V	L	H	L	H
	Phase V → Phase U				L
5	Phase V → Phase U	H	L	H	H
	Phase U → Phase V				L
6	Phase U → Phase W	L	H	H	H
	Phase W → Phase U				L

Input:

H: Input 1 is higher in potential than input 2 by at least 0.2V.

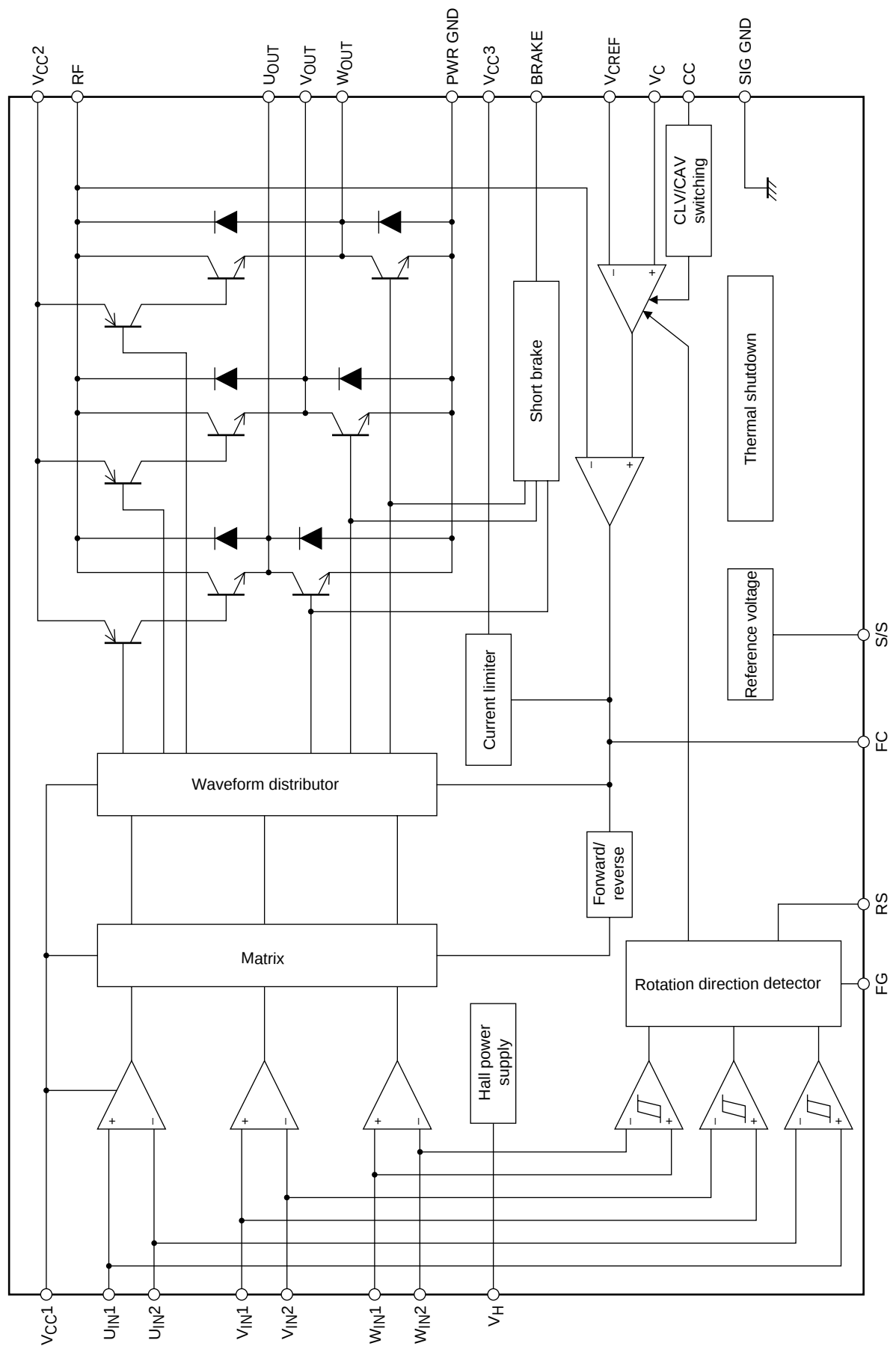
L: Input 1 is lower in potential than input 2 by at least 0.2V.

Pin Assignment

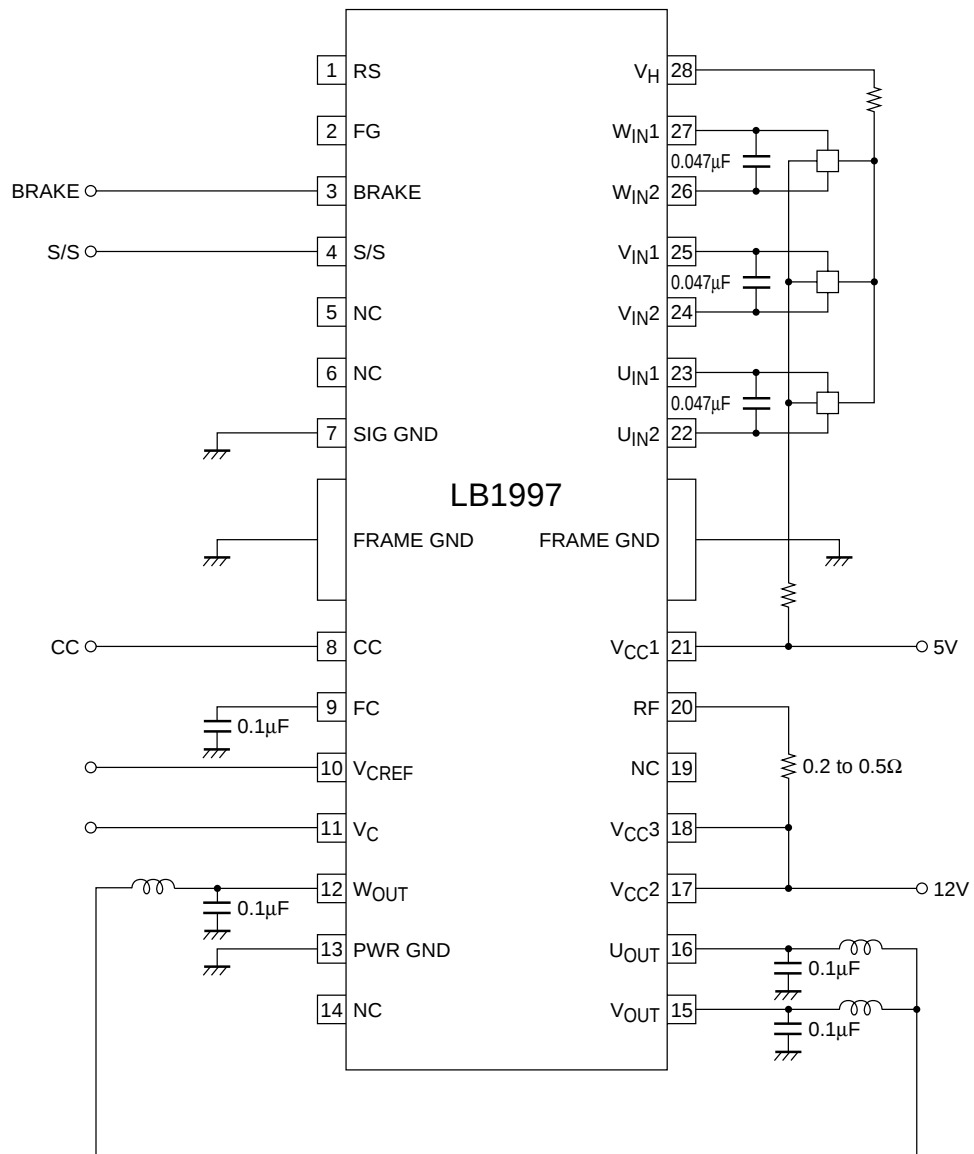


Top view

Block Diagram

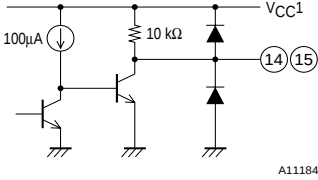
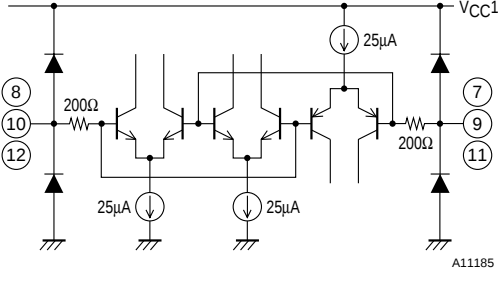
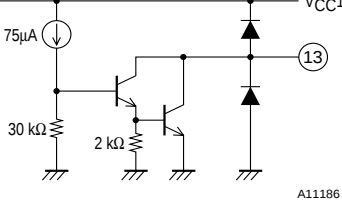
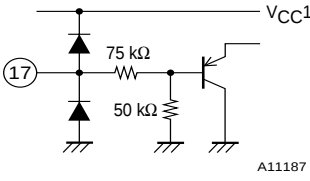
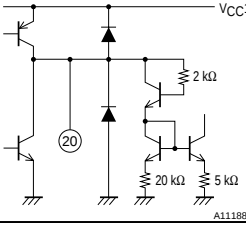


Sample Application Circuit



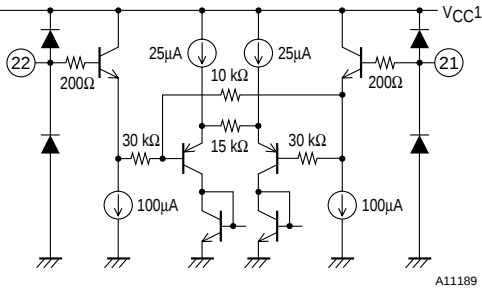
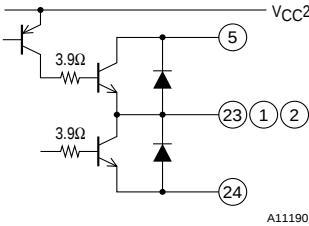
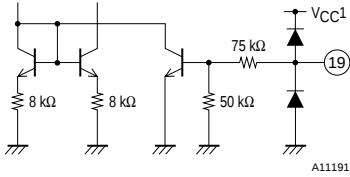
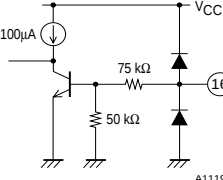
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Pin Descriptions

Pin number	Pin name	Pin voltage	Equivalent circuit	Pin function
17	V_{CC2}	4V to 13.6V		Source side predrive voltage supply pin
18	V_{CC3}	4V to 13.6V		Constant current control amplifier voltage supply pin
21	V_{CC1}	4V to 6V		Power supply pin for all circuits except output transistors, source predriver, and low current control amplifier
1	RS			Reverse detector pin Forward rotation: High Reverse rotation: Low
2	FG			1 Hall element waveform Schmitt comparator composite output
23 22	U_{IN1} U_{IN2}	1.2V to $V_{CC1}-1V$		U phase Hall element input and reverse detector U phase Schmitt comparator input pin Logic High indicates $U_{IN1} > U_{IN2}$.
25 24	V_{IN1} V_{IN2}			V phase Hall element input and reverse detector V phase Schmitt comparator input pin Logic High indicates $V_{IN1} > V_{IN2}$.
27 26	W_{IN1} W_{IN2}			W phase Hall element input and reverse detector W phase Schmitt comparator input pin Logic High indicates $W_{IN1} > W_{IN2}$.
28	V_H			Hall element lower side bias voltage supply pin
4	S/S	0V to V_{CC1}		When this pin is at 0.7V or lower, or when it is open, all circuits are inactive. When driving motor, set this pin to 2V or higher.
7	SIG GND			GND pin for all circuits except output
9	FC			Control loop frequency compensator pin. Connecting a capacitor between this pin and GND prevents closed loop oscillation in current limiting circuitry.

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Pin number	Pin name	Pin voltage	Equivalent circuit	Pin function
10	V_{CREF}	2V to 3V		Control reference voltage supply pin. Determines control start voltage.
11	V_C	0V to V_{CC1}		Speed control voltage supply pin V type control technique $V_C > V_{CREF}$: Forward $V_C < V_{CREF}$: Slowdown (Reverse-blocking circuit built in to prevent reverse rotation.)
12	W_{OUT}			W phase output
13	PWR GND			Output transistor GND
15	V_{OUT}			V phase output
16	U_{OUT}			U phase output
20	RF			Upper side output PNP transistor collector pin (common for all 3 phases). For current detection, connect resistor between V_{CC3} pin and RF pin. Constant current control and current limiter works by detecting this voltage.
8	CC			V type control/single-side control switching pin CC: High → Single-side control Low/Open → V type control
3	BRAKE			Short brake pin BRAKE: High → Short brake operation Low/Open → Motor drive operation

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