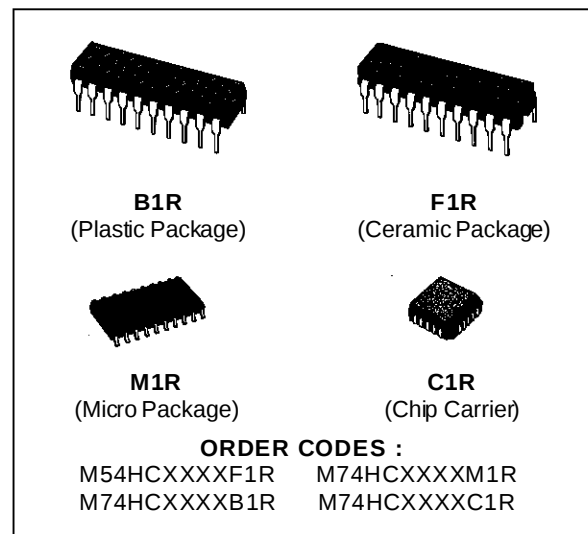


OCTAL BUS TRANSCEIVER (3-STATE): HC7645 NON INVERTING
HC7640 INVERTING , HC7643 INVERTING/NON INVERTING

- **HIGH SPEED**
 $t_{PD} = 12 \text{ ns (TYP.) at } V_{CC} = 5V$
- **LOW POWER DISSIPATION**
 $I_{CC} = 4 \mu A \text{ (MAX.) at } T_A = 25^\circ C$
- **HIGH NOISE IMMUNITY**
 $V_H = 1.1 \text{ (TYP.) at } V_{CC} = 5V$
- **OUTPUT DRIVE CAPABILITY**
15 LSTTL LOADS
- **SYMMETRICAL OUTPUT IMPEDANCE**
 $|I_{OH}| = I_{OL} = 6 \text{ mA (MIN)}$
- **BALANCED PROPAGATION DELAYS**
 $t_{PLH} = t_{PHL}$
- **WIDE OPERATING RANGE**
 $V_{CC} \text{ (OPR)} = 2V \text{ to } 6V$
- **PIN AND FUNCTION COMPATIBLE**
WITH 54/74LS245/640/643

DESCRIPTION

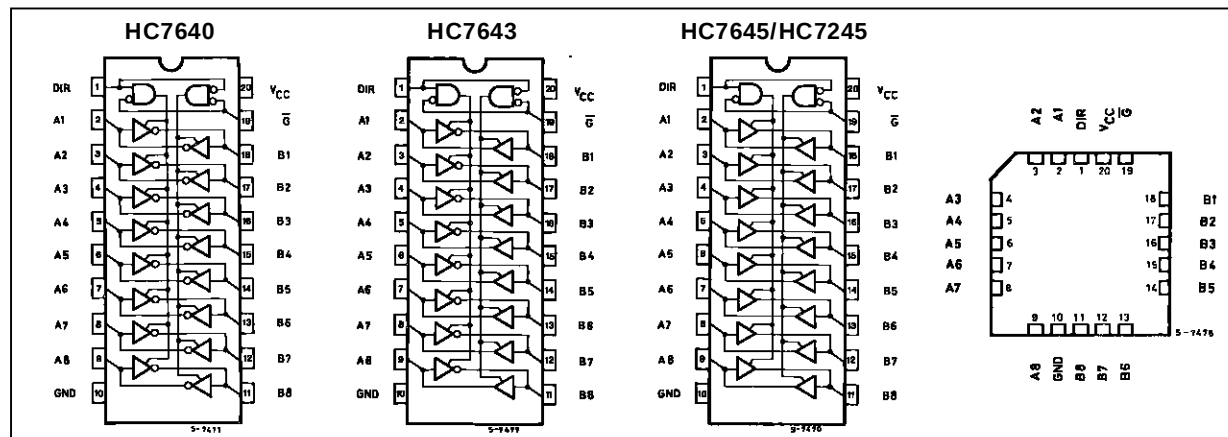
The M54/74HC7245 HC7640, HC7643 and HC7645 utilise silicon gate C²MOS technology to achieve operating speeds equivalent to LSTTL devices. Along with the low power dissipation and high noise immunity of standard C²MOS integrated circuit, it possesses the capability to drive 15 LSTTL loads. These IC's are intended for two-way asynchronous communication between data buses, and the direction of data transmission is determined by DIR input. The enable input ($\bar{G}$) can be used to disable the device so that the buses are effectively isolated. The HC7245/640/7643/7645 have the same pin



configuration and function as the HC245/640/643 and they have a hysteresis characteristics with each input, so HC7245/7640/7643/7645 can be used as a line receiver, etc. All input are equipped with protection circuits against static discharge and transient excess voltage.

IT IS PROHIBITED TO APPLY A SIGNAL TO A BUS TERMINAL WHEN IT IS IN OUTPUT MODE AND WHEN A BUS TERMINAL IS FLOATING (HIGH IMPEDANCE STATE), IT IS REQUESTED TO FIX THE INPUT LEVEL BY MEANS OF EXTERNAL PULL DOWN OR PULL UP RESISTOR.

PIN CONNECTION (top view)



HC7245 is electrically and functionally the same as the HC7645

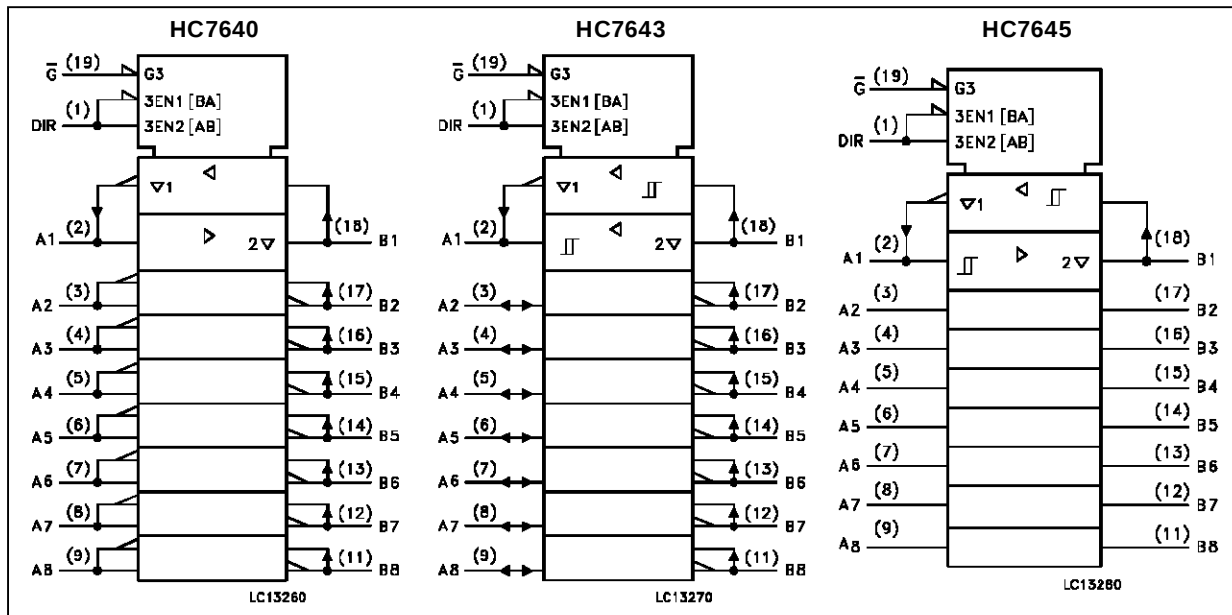
INPUT AND OUTPUT EQUIVALENT CIRCUIT



PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1	DIR	Directional Control
2, 3, 4, 5, 6, 7, 8, 9	A1 to A8	Data Inputs/Outputs
18, 17, 16, 15, 14, 13, 12, 11	B1 to B8	Data Inputs/Outputs
19	$\overline{G}$	Output Enabel Input (Active LOW)
10	GND	Ground (0V)
20	Vcc	Positive Supply Voltage

IEC LOGIC SYMBOLS



TRUTH TABLE

INPUT		FUNCTION		OUTPUT		
$\overline{G}$	DIR	A BUS	B BUS	HC7640	HC7643	HC7645/7245
L	L	OUTPUT	INPUT	$A = \overline{B}$	$A = B$	$A = B$
L	H	INPUT	OUTPUT	$B = \overline{A}$	$B = \overline{A}$	$B = A$
H	X	Z	Z	Z	Z	Z

X: "H" or "L"

Z: High impedance

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	-0.5 to +7	V
V_I	DC Input Voltage	-0.5 to $V_{CC} + 0.5$	V
V_O	DC Output Voltage	-0.5 to $V_{CC} + 0.5$	V
I_{IK}	DC Input Diode Current	± 20	mA
I_{OK}	DC Output Diode Current	± 20	mA
I_O	DC Output Source Sink Current Per Output Pin	± 35	mA
I_{CC} or I_{GND}	DC V_{CC} or Ground Current	± 70	mA
P_D	Power Dissipation	500 (*)	mW
T_{stg}	Storage Temperature	-65 to +150	°C
T_L	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

(*) 500 mW: $\equiv 65^\circ\text{C}$ derate to 300 mW by 10mW/°C: 65°C to 85°C

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	2 to 6	V
V_I	Input Voltage	0 to V_{CC}	V
V_O	Output Voltage	0 to V_{CC}	V
T_{op}	Operating Temperature: M54HC Series M74HC Series	-55 to +125 -40 to +85	°C °C

DC SPECIFICATIONS

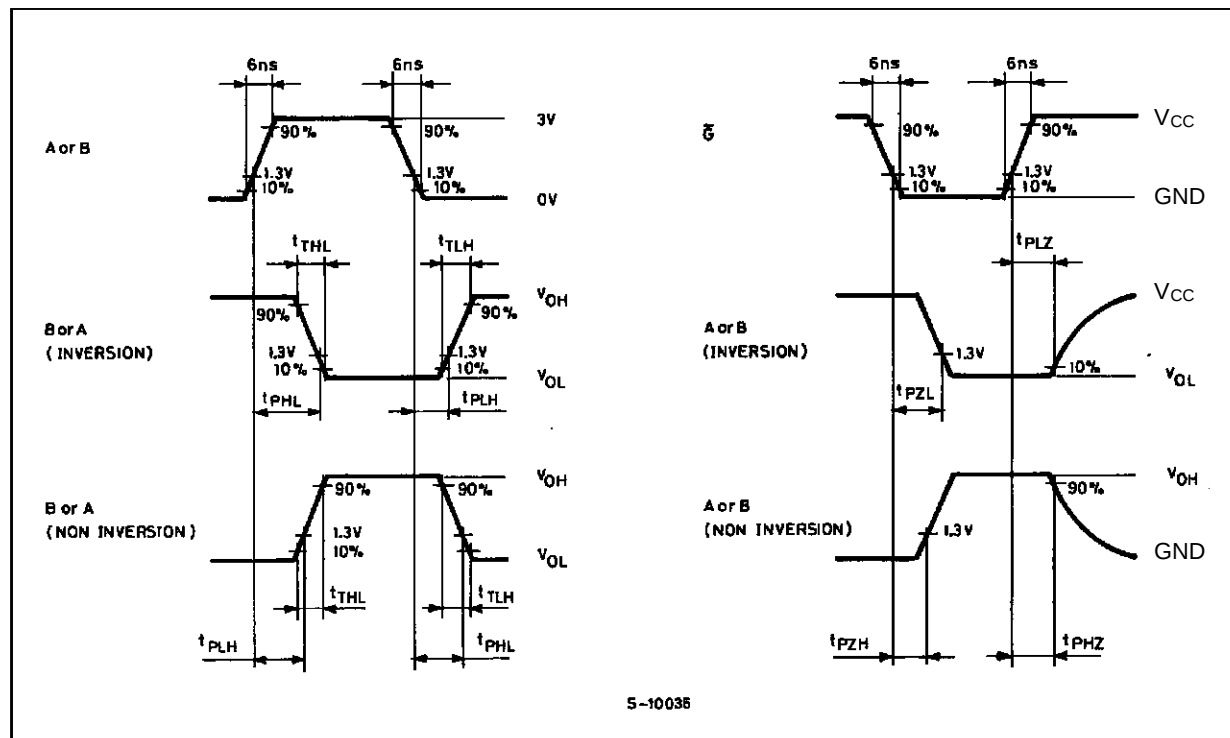
Symbol	Parameter	Test Conditions		Value							Unit	
		V _{CC} (V)		T _A = 25 °C 54HC and 74HC			-40 to 85 °C 74HC		-55 to 125 °C 54HC			
				Min.	Typ.	Max.	Min.	Max.	Min.	Max.		
V _P	Positive Threshold Voltage (An, Bn inputs)	2.0		1	1.25	1.5	1	1.5	1	1.5	V	
		4.5		2.3	2.7	3.15	2.3	3.15	2.3	3.15		
		6.0		3	3.5	4.2	3	4.2	3	4.2		
V _N	Negative Threshold Voltage (An, Bn inputs)	2.0		0.3	0.65	0.9	0.3	0.9	0.3	0.9	V	
		4.5		1.13	1.6	2	1.13	2	1.13	2		
		6.0		1.5	2.3	2.6	1.5	2.6	1.5	2.6		
V _H	Hysteresis Voltage (An, Bn inputs)	2.0		0.3	0.6	1	0.3	1	0.3	1	V	
		4.5		0.6	1.1	1.4	0.6	1.4	0.6	1.4		
		6.0		0.8	1.2	1.7	0.8	1.7	0.8	1.7		
V _{IH}	High Level Input Voltage (DIR, G inputs)	2.0		1.5			1.5		1.5		V	
		4.5		3.15			3.15		3.15			
		6.0		4.2			4.2		4.2			
V _{IL}	Low Level Input Voltage (DIR, G inputs)	2.0				0.5		0.5		0.5	V	
		4.5				1.35		1.35		1.35		
		6.0				1.8		1.8		1.8		
V _{OH}	High Level Output Voltage	2.0	V _I = V _{IH} or V _{IL}	I _O =-20 μA	1.9	2.0		1.9		1.9		V
		4.5			4.4	4.5		4.4		4.4		
		6.0			5.9	6.0		5.9		5.9		
		4.5		I _O =-6.0 mA	4.18	4.31		4.13		4.10		
		6.0		I _O =-7.8 mA	5.68	5.8		5.63		5.60		
V _{OL}	Low Level Output Voltage	2.0	V _I = V _{IH} or V _{IL}	I _O = 20 μA		0.0	0.1		0.1		0.1	V
		4.5				0.0	0.1		0.1		0.1	
		6.0				0.0	0.1		0.1		0.1	
		4.5		I _O = 6.0 mA		0.17	0.26		0.33		0.40	
		6.0		I _O = 7.8 mA		0.18	0.26		0.33		0.40	
I _I	Input Leakage Current	6.0	V _I = V _{CC} or GND			±0.1		±1		±1	μA	
I _{OZ}	Quiescent Supply Current	6.0	V _I = V _{CC} or GND			æ0.5		±5		±1	μA	
I _{CC}	Quiescent Supply Current	6.0	V _I = V _{CC} or GND			4		40		80	μA	

AC ELECTRICAL CHARACTERISTICS ($C_L = 50$ pF, Input $t_r = t_f = 6$ ns)

Symbol	Parameter	Test Conditions		Value						Unit	
		V _{CC} (V)		T _A = 25 °C 54HC and 74HC			-40 to 85 °C 74HC		-55 to 125 °C 54HC		
				Min.	Typ.	Max.	Min.	Max.	Min.		Max.
t _{TLH} t _{THL}	Output Transition Time	2.0			25	60		75		90	ns
		4.5			7	12		15		18	
		6.0			6	10		13		15	
t _{PLH} t _{PHL}	Propagation Delay Time	2.0			61	125		156		190	ns
		4.5			15	25		31		38	
		6.0			13	21		27		32	
t _{PZL} t _{PZH}	Output Enable Time	2.0	R _L = 1 KΩ		77	150		188		225	ns
		4.5			19	30		38		45	
		6.0			16	26		32		38	
t _{PLZ} t _{PHZ}	Output Disable Time	2.0	R _L = 1 KΩ		84	150		188		225	ns
		4.5			21	30		38		45	
		6.0			18	26		32		38	
C _{IN}	Input Capacitance				5	10		10		10	pF
C _{OUT}	Output Capacitance										pF
C _{PD} (*)	Power Dissipation Capacitance										pF

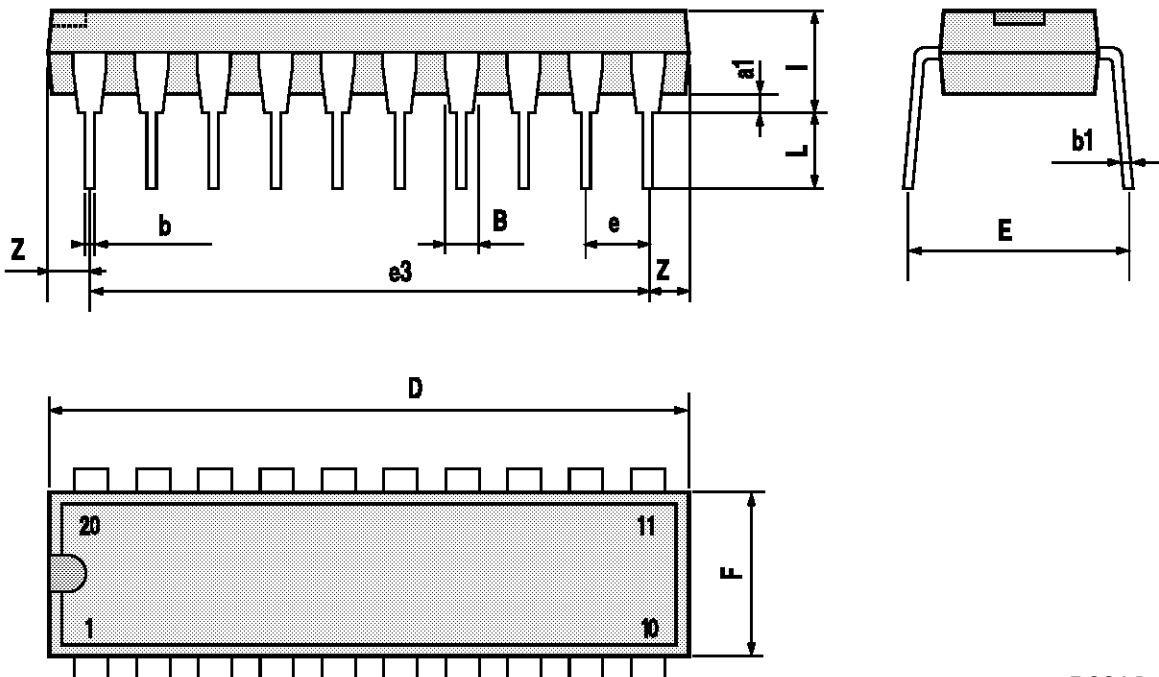
(*) C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per Gate)

SWITCHING CHARACTERISTICS TEST WAVEFORM



Plastic DIP20 (0.25) MECHANICAL DATA

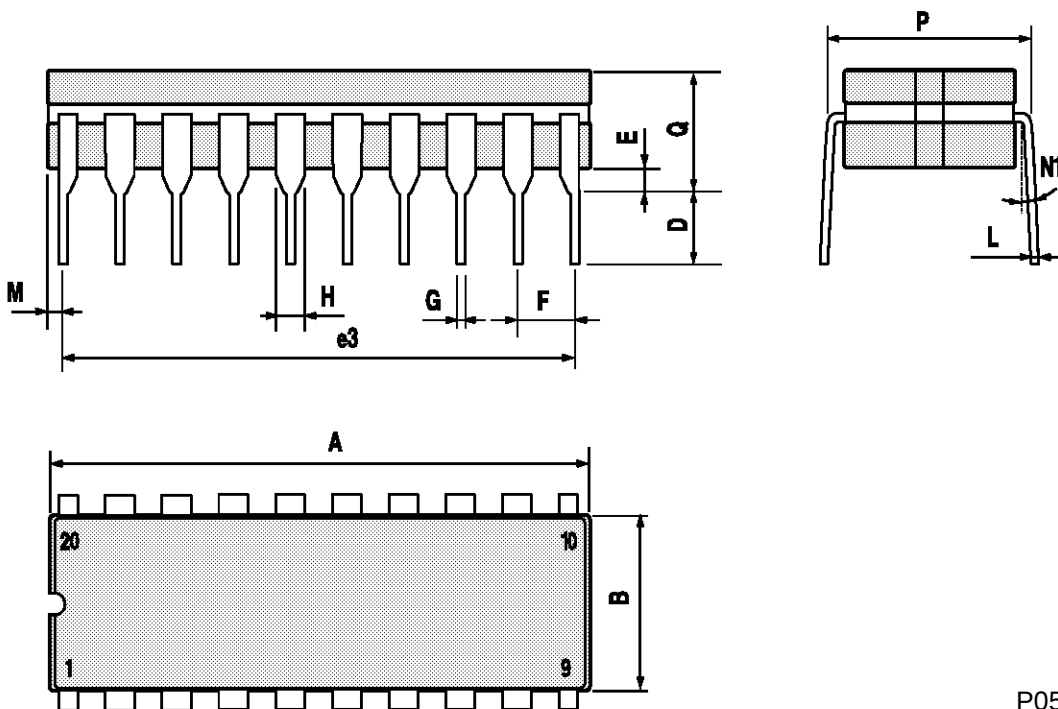
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.254			0.010		
B	1.39		1.65	0.055		0.065
b		0.45			0.018	
b1		0.25			0.010	
D			25.4			1.000
E		8.5			0.335	
e		2.54			0.100	
e3		22.86			0.900	
F			7.1			0.280
I			3.93			0.155
L		3.3			0.130	
Z			1.34			0.053



P001J

Ceramic DIP20 MECHANICAL DATA

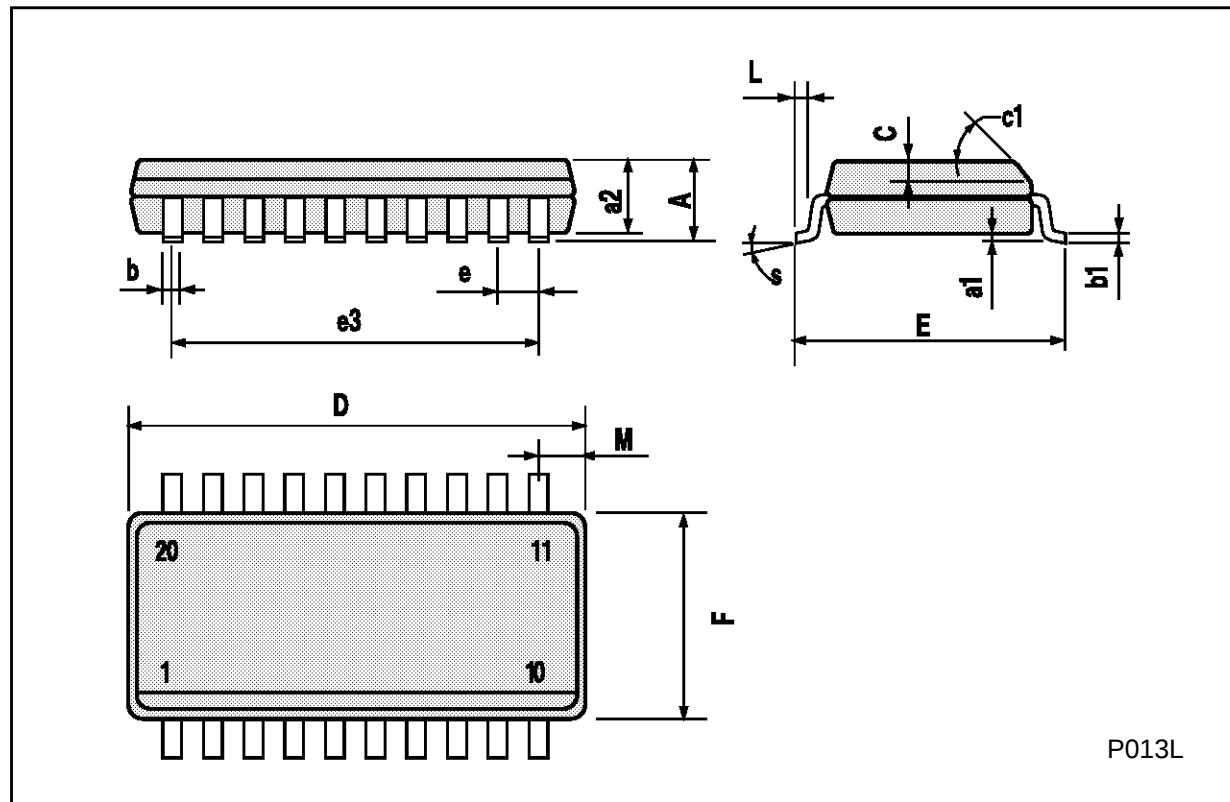
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			25			0.984
B			7.8			0.307
D		3.3			0.130	
E	0.5		1.78	0.020		0.070
e3		22.86			0.900	
F	2.29		2.79	0.090		0.110
G	0.4		0.55	0.016		0.022
I	1.27		1.52	0.050		0.060
L	0.22		0.31	0.009		0.012
M	0.51		1.27	0.020		0.050
N1	4° (min.), 15° (max.)					
P	7.9		8.13	0.311		0.320
Q			5.71			0.225



P057H

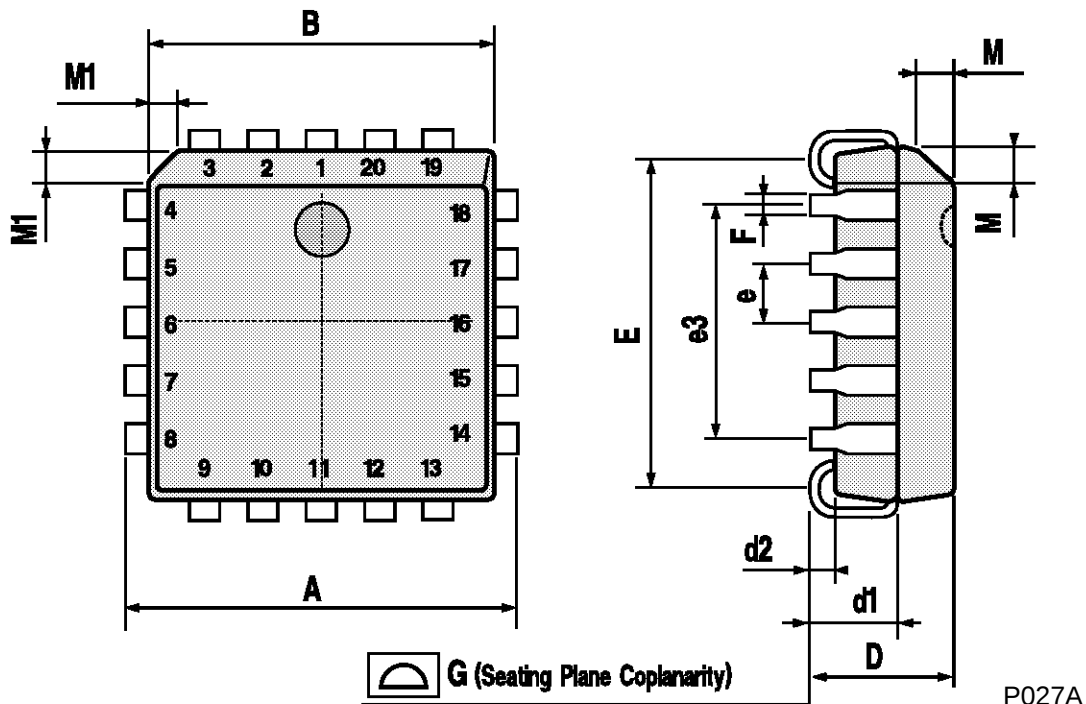
SO20 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			2.65			0.104
a1	0.10		0.20	0.004		0.007
a2			2.45			0.096
b	0.35		0.49	0.013		0.019
b1	0.23		0.32	0.009		0.012
C		0.50			0.020	
c1	45° (typ.)					
D	12.60		13.00	0.496		0.512
E	10.00		10.65	0.393		0.419
e		1.27			0.050	
e3		11.43			0.450	
F	7.40		7.60	0.291		0.299
L	0.50		1.27	0.19		0.050
M			0.75			0.029
S	8° (max.)					



PLCC20 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	9.78		10.03	0.385		0.395
B	8.89		9.04	0.350		0.356
D	4.2		4.57	0.165		0.180
d1		2.54			0.100	
d2		0.56			0.022	
E	7.37		8.38	0.290		0.330
e		1.27			0.050	
e3		5.08			0.200	
F		0.38			0.015	
G			0.101			0.004
M		1.27			0.050	
M1		1.14			0.045	



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