

# NJM2043

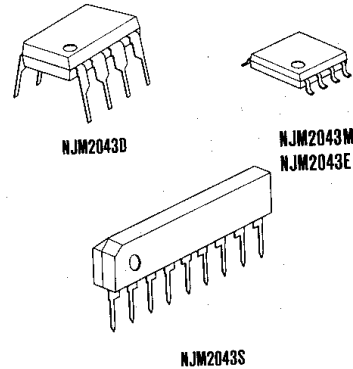
NJM2043 is bipolar operational amplifier which is designed as low noise version of the NJM4558 with high output current and fast slew rate (6V/μs) and wide unity bandwidth (14MHz) constructed using New JRC Planar epitaxial process.

### Absolute Maximum Ratings (Ta=25°C)

Supply Voltage	V <sup>+</sup> /V <sup>-</sup>	±22V
Differential Input Voltage	V <sub>ID</sub>	±30V
Input Voltage (note)	V <sub>I</sub>	±15V
Power Dissipation	P <sub>D</sub> (D, S-Type)	500mW
	(M,E Type)	300mW
Operating Temperature Range	T <sub>opr</sub>	-20~+75°C
Storage Temperature Range	T <sub>stg</sub>	-40~+125°C

(note) For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

### Package Outline

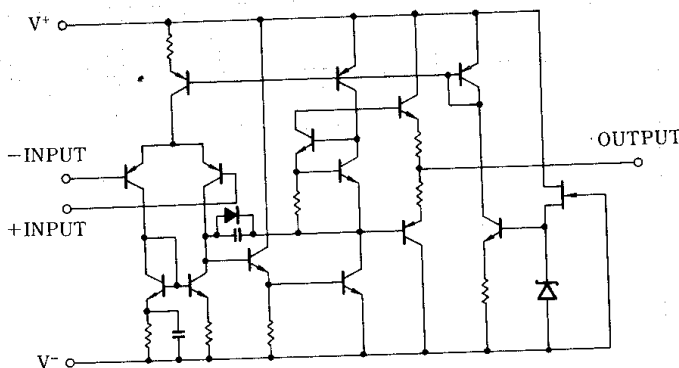


### Electrical Characteristics (Ta=25°C, V<sup>+</sup>=15V, V<sup>-</sup>=-15V)

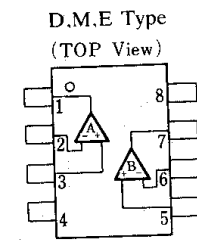
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input Offset Voltage	V <sub>IO</sub>	R <sub>S</sub> ≤ 10kΩ	—	0.3	3	mV
Input Offset Current	I <sub>IO</sub>		—	10	200	nA
Input Bias Current	I <sub>IB</sub>		—	400	1000	nA
Input Resistance	R <sub>IN</sub>		30	100	—	kΩ
Large-signal Voltage Gain	A <sub>V</sub>	R <sub>L</sub> ≥ 2kΩ, V <sub>O</sub> = ±10V	20	100	—	×10 <sup>3</sup>
Maximum Output Voltage Swing 1	V <sub>OM 1</sub>	R <sub>L</sub> ≥ 10kΩ	±12	±14	—	V
Maximum Output Voltage Swing 2	V <sub>OM 2</sub>	I <sub>O</sub> = 25mA	±10	±11.5	—	V
Input Common Mode Voltage Range	V <sub>ICM</sub>		±12	±14	—	V
Common Mode Rejection Ratio	CMR	R <sub>S</sub> ≤ 10kΩ	70	100	—	dB
Supply Voltage Rejection Ratio	SVR	R <sub>S</sub> ≤ 10kΩ	—	10	150	μV/V
Supply Current	I <sub>CC</sub>		—	6	8	mA
Slew Rate	SR	R <sub>L</sub> ≥ 2kΩ	—	6	—	V/μs
Unity Gain Bandwidth	f <sub>T</sub>	A <sub>V</sub> = 1	—	14	—	MHz
Equivalent Input Noise Voltage 1	V <sub>NI 1</sub> *	RIAA R <sub>S</sub> = 2.2kΩ, 30kHz LPF	—	0.9	1.4	μV
Equivalent Input Noise Voltage 2	V <sub>NI 2</sub>	FLAT + JISA R <sub>S</sub> = 300Ω	—	0.4	0.51	μV

\* Applies to noise D rank only.  
Closed loop gain should be more than 20dB at use.

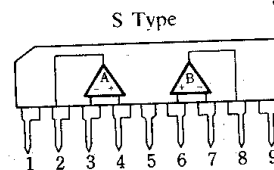
### Equivalent Circuit (1/2 Shown)



### Connection Diagram



- PIN FUNCTION
1. A OUTPUT
  2. A-INPUT
  3. A+INPUT
  4. V<sup>-</sup>
  5. B+INPUT
  6. B-INPUT
  7. B OUTPUT
  8. V<sup>+</sup>



- PIN FUNCTION
1. V<sup>+</sup>
  2. A OUTPUT
  3. A-INPUT
  4. A+INPUT
  5. V<sup>-</sup>
  6. B+INPUT
  7. B-INPUT
  8. B OUTPUT
  9. V<sup>+</sup>