

# PK(PD,PE)130FG

**TOP**

UL:E76102(M)

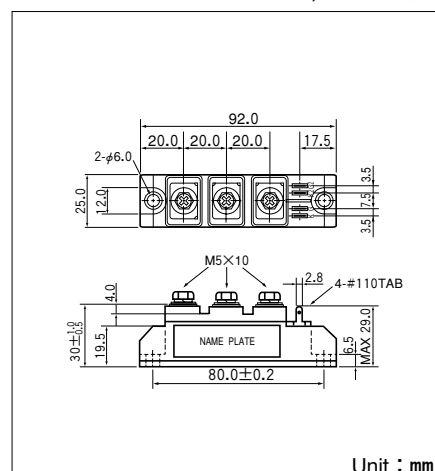
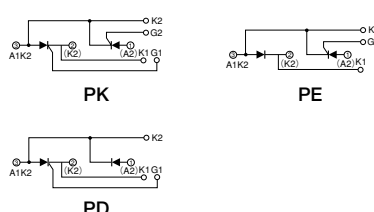
Power Thyristor/Diode Module **PK130FG** series are designed for various rectifier circuits and power controls. For your circuit application, following internal connections and wide voltage ratings up to 1600V are available. and electrically isolated mounting base make your mechanical design easy.

- $I_{T(AV)}$  130A,  $I_{T(RMS)}$  205A,  $I_{TSM}$  3500A
- $di/dt$  100A/ $\mu$ s
- $dv/dt$  1000V/ $\mu$ s

(Applications)

- Various rectifiers
- AC/DC motor drives
- Heater controls
- Light dimmers
- Static switches

## Internal Configurations



Unit : mm

### ■ Maximum Ratings

(Tj=25°C unless otherwise specified)

| Symbol           | Item                                  | Ratings   |           |            |            | Unit |
|------------------|---------------------------------------|-----------|-----------|------------|------------|------|
|                  |                                       | PK130FG40 | PK130FG80 | PK130FG120 | PK130FG160 |      |
| V <sub>RRM</sub> | * Repetitive Peak Reverse Voltage     | 400       | 800       | 1200       | 1600       | V    |
| V <sub>RSM</sub> | * Non-Repetitive Peak Reverse Voltage | 480       | 960       | 1300       | 1700       | V    |
| V <sub>DRM</sub> | * Repetitive Peak off-state Voltage   | 400       | 800       | 1200       | 1600       | V    |

| Symbol              | Item                                      |               | Conditions                                                                               | Ratings     | Unit             |
|---------------------|-------------------------------------------|---------------|------------------------------------------------------------------------------------------|-------------|------------------|
| I <sub>T(AV)</sub>  | * Average On-state Current                |               | Single phase, half wave, 180° conduction, T <sub>c</sub> =83°C                           | 130         | A                |
| I <sub>T(RMS)</sub> | * R.M.S. On-state Current                 |               | Single phase, half wave, 180° conduction, T <sub>c</sub> =83°C                           | 205         | A                |
| I <sub>TSM</sub>    | * Surge On-state Current                  |               | 1/2 Cycle, 50/60Hz, Peak Value, non-repetitive                                           | 3200/3500   | A                |
| I <sup>2</sup> t    | * I <sup>2</sup> t                        |               | Value for one cycle surge current                                                        | 51040       | A <sup>2</sup> S |
| P <sub>GM</sub>     | Peak Gate Power Dissipation               |               |                                                                                          | 10          | W                |
| P <sub>G(AV)</sub>  | Average Gate Power Dissipation            |               |                                                                                          | 1           | W                |
| I <sub>FGM</sub>    | Peak Gate Current                         |               |                                                                                          | 3           | A                |
| V <sub>FGM</sub>    | Peak Gate Voltage (Forward)               |               |                                                                                          | 10          | V                |
| V <sub>RGM</sub>    | Peak Gate Voltage (Reverse)               |               |                                                                                          | 5           | V                |
| di/dt               | Critical Rate of Rise of On-state Current |               | I <sub>G</sub> =100mA, V <sub>D</sub> =1/2V <sub>DRM</sub> , di <sub>G</sub> /dt=0.1A/μs | 100         | A/μs             |
| V <sub>ISO</sub>    | * Isolation Breakdown Voltage (R.M.S.)    |               | A.C. 1minute                                                                             | 2500        | V                |
| T <sub>j</sub>      | * Operating Junction Temperature          |               |                                                                                          | −40 to +125 | °C               |
| T <sub>stg</sub>    | * Storage Temperature                     |               |                                                                                          | −40 to +125 | °C               |
|                     | Mounting Torque                           | Mounting (M5) | Recommended Value 1.5-2.5 (15-25)                                                        | 2.7 (28)    | N·m<br>(kgf·cm)  |
|                     |                                           | Terminal (M5) | Recommended Value 1.5-2.5 (15- 25)                                                       | 2.7 (28)    |                  |
|                     | Mass                                      |               | Typical Value                                                                            | 170         | g                |

## ■ Electrical Characteristics

| Symbol        | Item                                           | Conditions                                           | Ratings | Unit                        |
|---------------|------------------------------------------------|------------------------------------------------------|---------|-----------------------------|
| $I_{DRM}$     | Repetitive Peak off-state Current,max          | $T_j=125^{\circ}\text{C}$ , $V_D=V_{DRM}$            | 35      | mA                          |
| $I_{RRM}$     | * Repetitive Peak Reverse Current,max          | $T_j=125^{\circ}\text{C}$ , $V_D=V_{DRM}$            | 35      | mA                          |
| $V_{TM}$      | * Gn-state Voltage,max                         | $I_T=390\text{A}$                                    | 1.6     | V                           |
| $I_{GT}$      | Gate Trigger Current,max                       | $V_D=6\text{V}$ , $I_T=1\text{A}$                    | 50      | mA                          |
| $V_{GT}$      | Gate Trigger Voltage,max                       | $V_D=6\text{V}$ , $I_T=1\text{A}$                    | 3       | V                           |
| $V_{GD}$      | Gate Non-Trigger Voltage,min                   | $T_j=125^{\circ}\text{C}$ , $V_D=\frac{1}{2}V_{DRM}$ | 0.25    | V                           |
| $dv/dt$       | Critical Rate of Rise of off-state Voltage,min | $T_j=125^{\circ}\text{C}$ , $V_D=\frac{2}{3}V_{DRM}$ | 1000    | $\text{V}/\mu\text{s}$      |
| $R_{th(j-c)}$ | * Thermal Impedance,max                        | Junction to case                                     | 0.2     | $^{\circ}\text{C}/\text{W}$ |

\* mark : Thyristor and Diode part. No mark : Thyristor part

