

STK430 SERIES

• Single Power Supply • 2ch Amplifier in a Packaging • Other Specification Typical Value $I_{CCO}=60mA, V_{NO}=0.5mA$

Type No Series Name シリーズ名	Maximum Ratings 最大定格		Recommended Operation Condition 推奨動作条件			Operation Characteristics(Vcc:Recommended Condition 動作特性(推奨条件) $RL=8\Omega$ 1KHz							
	Vcc max	Tc max	Supply Vol (Vcc) ($RL=8\Omega$)	Supply Vol (Vcc) ($RL=4\Omega$)	Closed Loop Gain	Closed Loop Gain	Po max / $RL=8\Omega$		Po max / $RL=4\Omega$		THD Po=0.1 max	PBW THD=1% min	Po
							min	THD	min	THD			
	V	°C	V	V	dB	dB	W	%	W	%	%	Hz	W
STK 433A	32	105	23	20	26~45	40	5	1.0	7	1.0	0.5	60~10K	2.5
STK 435A	39	105	27	24	26~45	40	7	1.0	10	1.0	0.5	40~20K	3.5
STK 436A	50	105	32	27	26~45	40	10	1.0	12	1.0	0.3	30~20K	5.0
STK 437	50	90	33	30	26~45	40	10	1.0	14	1.0	0.2	30~20K	5.0
STK 439	56	85	39	35	26~45	40	15	1.0	20	1.0	0.2	30~20K	7.5
STK 441	63	85	44	40	26~45	40	20	1.0	24	1.0	0.3	30~20K	10.0
STK 443	70	85	49	44	26~45	40	25	1.0	30	1.0	0.3	30~20K	13.0

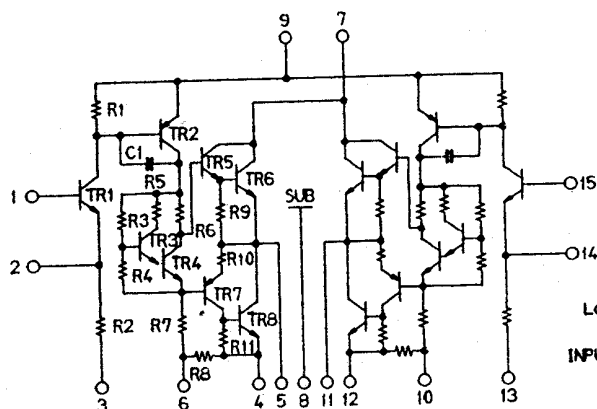
Note 1. At 4Ω load use, change a resistor from $390K\Omega$ to $330K\Omega$ in an application circuit in order to compensate unbalanced clipping.

Note 1. $RL=4\Omega$ 使用時アンバランスの補正のため $390K\Omega$ から $330K\Omega$ に変更して下さい。

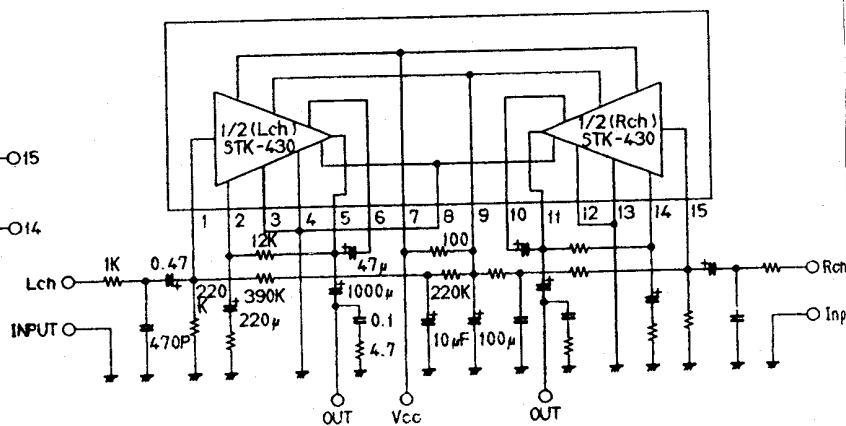
Note 2. Add a capacitor of 0.1 to $1\mu F$ between pins 7 of IC and ground. in order to improve an oscillation stability(e.g. in use of voltage gain below 30dB)

Note 2. 発振安定度改良の時は、7ピンとアースに $0.1 \sim 1\mu F$ のコンデンサを加えて下さい。(30dB以下使用時)

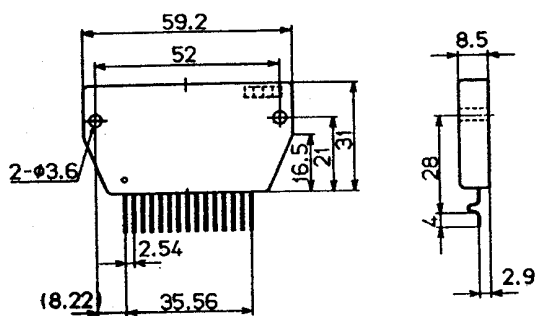
EQUIVALENT CIRCUIT



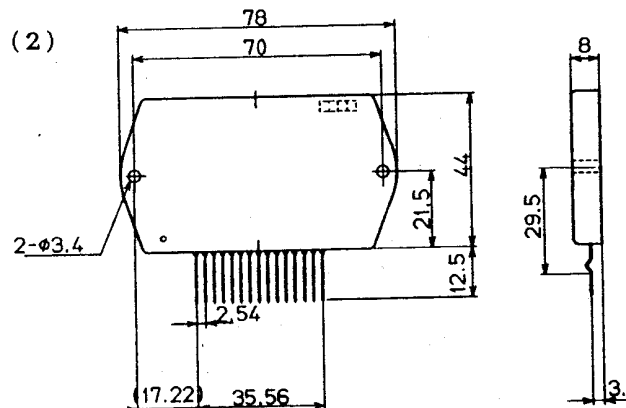
APPLICATION



PACKAGE DIMENSION (1)



(STK433A, 435A, 436A)



(STK437, 439, 441, 443)