

Description

The μPD71011 is a clock pulse generator/driver for the V20™/V30™ microprocessors and their peripherals using NEC's high-speed CMOS technology.

Features

- ☐ CMOS technology
- ☐ Clock pulse generator/driver for μPD70108/70116 or other CMOS or NMOS CPUs and their peripherals
- ☐ 50% duty cycle
- ☐ Frequency source can be crystal or external clock input
- ☐ Reset signal with Schmitt-trigger circuit for CPU or peripherals

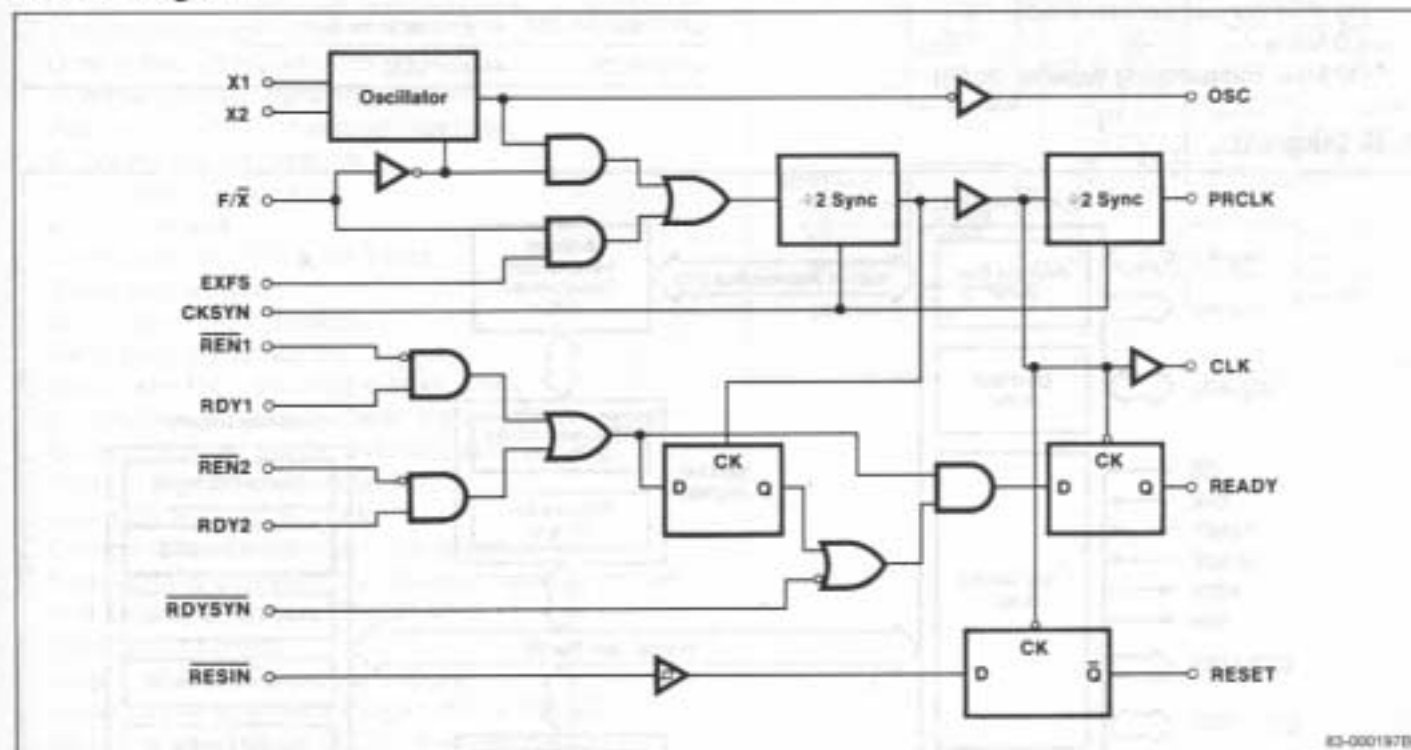
- ☐ Bus ready signal with two-bus system synchronization
- ☐ Clock synchronization with other μPD71011s
- ☐ Single +5-volt ±10% power supply
- ☐ Industrial temperature range: -40 to +85°C

Ordering Information

Part Number	Package	Maximum Clockout Frequency
μPD71011C	18-pin plastic DIP	8 MHz
C-10	18-pin plastic DIP	10 MHz
G	20-pin plastic SO	8 MHz
G-10	20-pin plastic SO	10 MHz

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Block Diagram



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