

DATA SHEET

NEC

GaAs INTEGRATED CIRCUIT μPG2137T5A

L-BAND PA DRIVER AMPLIFIER

DESCRIPTION

The μPG2137T5A is GaAs MMIC for PA driver amplifier which were developed for mobile phone and another L-band application.

This device is housed in a 16-pin TSON (Thin small out-line non-leaded) package. And this package is able to high-density surface mounting.

FEATURES

- Operation frequency : $f_{opt1} = 893$ to 960 MHz (0.8 GHz Band side)
: $f_{opt2} = 1\,429$ to $1\,453$ MHz (1.5 GHz Band side)
- Supply voltage : $V_{DD1,3} = 2.55$ to 2.85 V (2.7 V TYP.)
: $V_{DD2,4} = 3.0$ to 4.3 V (3.2 V TYP.)
- Circuit current : $I_{DD1} = 28$ mA TYP. @ $V_{DD1} = 2.7$ V, $V_{DD2} = 3.2$ V, $V_{AGC} = 2.5$ V (0.8 GHz Band side)
: $I_{DD2} = 33$ mA TYP. @ $V_{DD3} = 2.7$ V, $V_{DD4} = 3.2$ V, $V_{AGC} = 2.5$ V (1.5 GHz Band side)
- Power gain : $GP1 = 27$ dB TYP. @ $V_{DD1} = 2.7$ V, $V_{DD2} = 3.2$ V, $V_{AGC} = 2.5$ V (0.8 GHz Band side)
: $GP2 = -13$ dB TYP. @ $V_{DD1} = 2.7$ V, $V_{DD2} = 3.2$ V, $V_{AGC} = 0.5$ V (0.8 GHz Band side)
: $GP3 = 30$ dB TYP. @ $V_{DD3} = 2.7$ V, $V_{DD4} = 3.2$ V, $V_{AGC} = 2.5$ V (1.5 GHz Band side)
: $GP4 = -10$ dB TYP. @ $V_{DD3} = 2.7$ V, $V_{DD4} = 3.2$ V, $V_{AGC} = 0.5$ V (1.5 GHz Band side)
- Low distortion : $P_{adj1,3} = -60$ dBc TYP. @ $V_{DD1,3} = 2.7$ V, $V_{DD2,4} = 3.2$ V, $V_{AGC} = 2.5$ V, $P_{out} = +11$ dBm,
: $\Delta f = \pm 50$ kHz, 21 kHz Bandwidth (0.8/1.5 GHz Band side)
- High-density surface mounting : 16-pin TSON package ($3.3 \times 2.3 \times 0.6$ mm)

APPLICATION

- Digital Cellular: PDC 0.8/1.5 GHz Dual Band etc.

ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
μPG2137T5A-E1	16-pin TSON	2137	<ul style="list-style-type: none"> • Embossed tape 12 mm wide • Pin 8, 9 face the perforation side of the tape • Qty 3 kpcs/reel

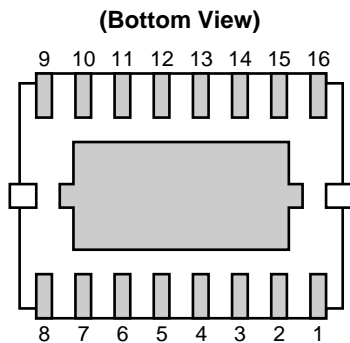
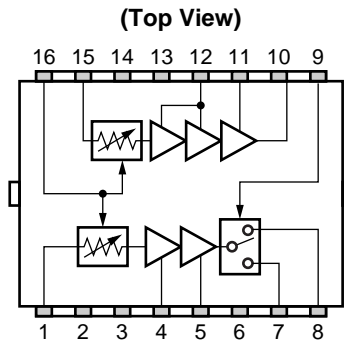
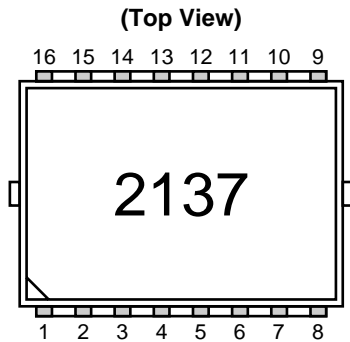
Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: μPG2137T5A

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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Not all devices/types available in every country. Please check with local NEC Compound Semiconductor Devices representative for availability and additional information.

PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	INPUT1 (0.8 GHz Band side)
2	GND
3	GND
4	V _{DD1} (0.8 GHz Band side)
5	V _{DD2} (0.8 GHz Band side)
6	GND
7	OUTPUT1 (0.8 GHz Band side)
8	OUTPUT2 (0.8 GHz Band side)
9	V _{sw} (0.8 GHz Band side)
10	OUTPUT3 (1.5 GHz Band side)
11	V _{DD4} (1.5 GHz Band side)
12	V _{DD3} (1.5 GHz Band side)
13	GND
14	GND
15	INPUT2 (1.5 GHz Band side)
16	V _{AGC}

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Supply Voltage _{1, 2, 3, 4}	V _{DD1, 2, 3, 4}	5.0	V
Gain Control Voltage	V _{AGC}	5.0	V
Switch Control Voltage	V _{sw}	5.0	V
Input Power 1 (1 pin)	P _{in1}	0	dBm
Input Power 2 (15 pin)	P _{in2}	0	dBm
Power Dissipation	P _D	140 ^{Note}	mW
Operating Ambient Temperature	T _A	-30 to +90	°C
Storage Temperature	T _{stg}	-40 to +150	°C

Note Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB, T_A = +85°C

RECOMMENDED OPERATING RANGE (TA = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency 1	f _{opt1}	893	–	960	MHz
Operating Frequency 2	f _{opt2}	1 429	–	1 453	MHz
Supply Voltage1, 3	V _{DD1, 3}	2.55	2.7	2.85	V
Supply Voltage2, 4	V _{DD2, 4}	3.0	3.2	4.3	V
Gain Control Voltage	V _{AGC}	0.5	–	2.5	V
Switch Control Voltage	V _{SW}	0	–	2.85	V
Input Power 1 (1 pin)	P _{in1}	–	–17	–10	dBm
Input Power 2 (15 pin)	P _{in2}	–	–20	–10	dBm

ELECTRICAL CHARACTERISTICS (TA = +25°C, V_{DD1, 3} = 2.7 V, V_{DD2, 4} = 3.2 V, π/4DQPSK modulated signal input, with external input and output matching, unless otherwise specified)

0.8 GHz Band side

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Operating Frequency 1	f _{opt1}		893	–	960	MHz
Circuit Current 1	I _{DD1}	P _{in} = –17 dBm, V _{AGC} = 2.5 V	–	28	35	mA
Power Gain 1	G _{P1}	P _{in} = –17 dBm, V _{AGC} = 2.5 V	25	27	–	dB
Power Gain 2	G _{P2}	P _{in} = –17 dBm, V _{AGC} = 0.5 V	–	–13	–10	dB
Adjacent Channel Power Leakage 1	P _{adj1}	P _{out} = +11 dBm, V _{AGC} = 2.5 V, Δf = ±50 kHz, 21 kHz Bandwidth	–	–60	–55	dBc
Adjacent Channel Power Leakage 2	P _{adj2}	P _{out} = +11 dBm, V _{AGC} = 2.5 V, Δf = ±100 kHz, 21 kHz Bandwidth	–	–70	–65	dBc
Noise Figure 1	NF ₁		–	3	–	dB

1.5 GHz Band side

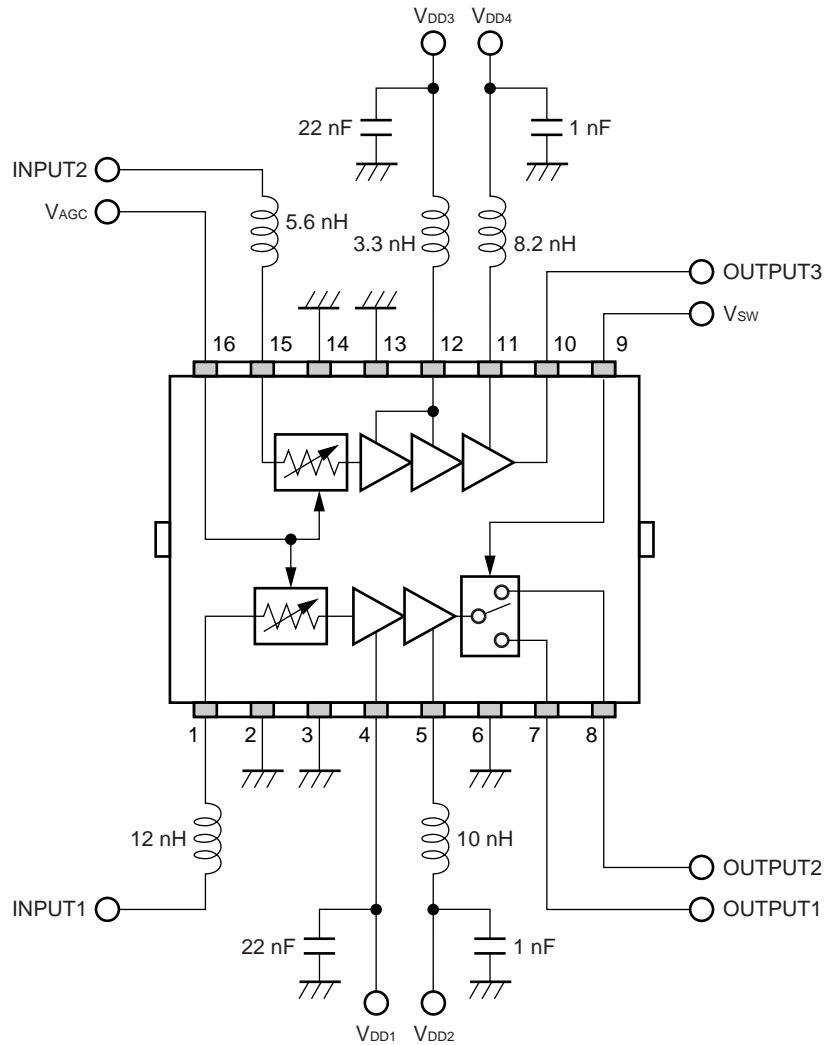
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Operating Frequency 2	f _{opt2}		1 429	–	1 453	MHz
Circuit Current 2	I _{DD2}	P _{in} = –20 dBm, V _{AGC} = 2.5 V	–	33	40	mA
Power Gain 3	G _{P3}	P _{in} = –20 dBm, V _{AGC} = 2.5 V	28	30	–	dB
Power Gain 4	G _{P4}	P _{in} = –20 dBm, V _{AGC} = 0.5 V	–	–10	–7	dB
Adjacent Channel Power Leakage 3	P _{adj3}	P _{out} = +11 dBm, V _{AGC} = 2.5 V, Δf = ±50 kHz, 21 kHz Bandwidth	–	–60	–55	dBc
Adjacent Channel Power Leakage 4	P _{adj4}	P _{out} = +11 dBm, V _{AGC} = 2.5 V, Δf = ±100 kHz, 21 kHz Bandwidth	–	–70	–65	dBc
Noise Figure 2	NF ₂		–	5	–	dB

0.8 GHz/1.5 GHz Band

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Gain Control Current	I_{AGC}	$V_{DD1,3} = 2.7/0\text{ V}$, $V_{DD2,4} = 3.2/0\text{ V}$, $V_{AGC} = 0.5/2.5\text{ V}$	-200	-	300	μA
Switch Control Current	I_{SW}	$V_{SW} = 2.7/0\text{ V}$	-10	-	100	μA

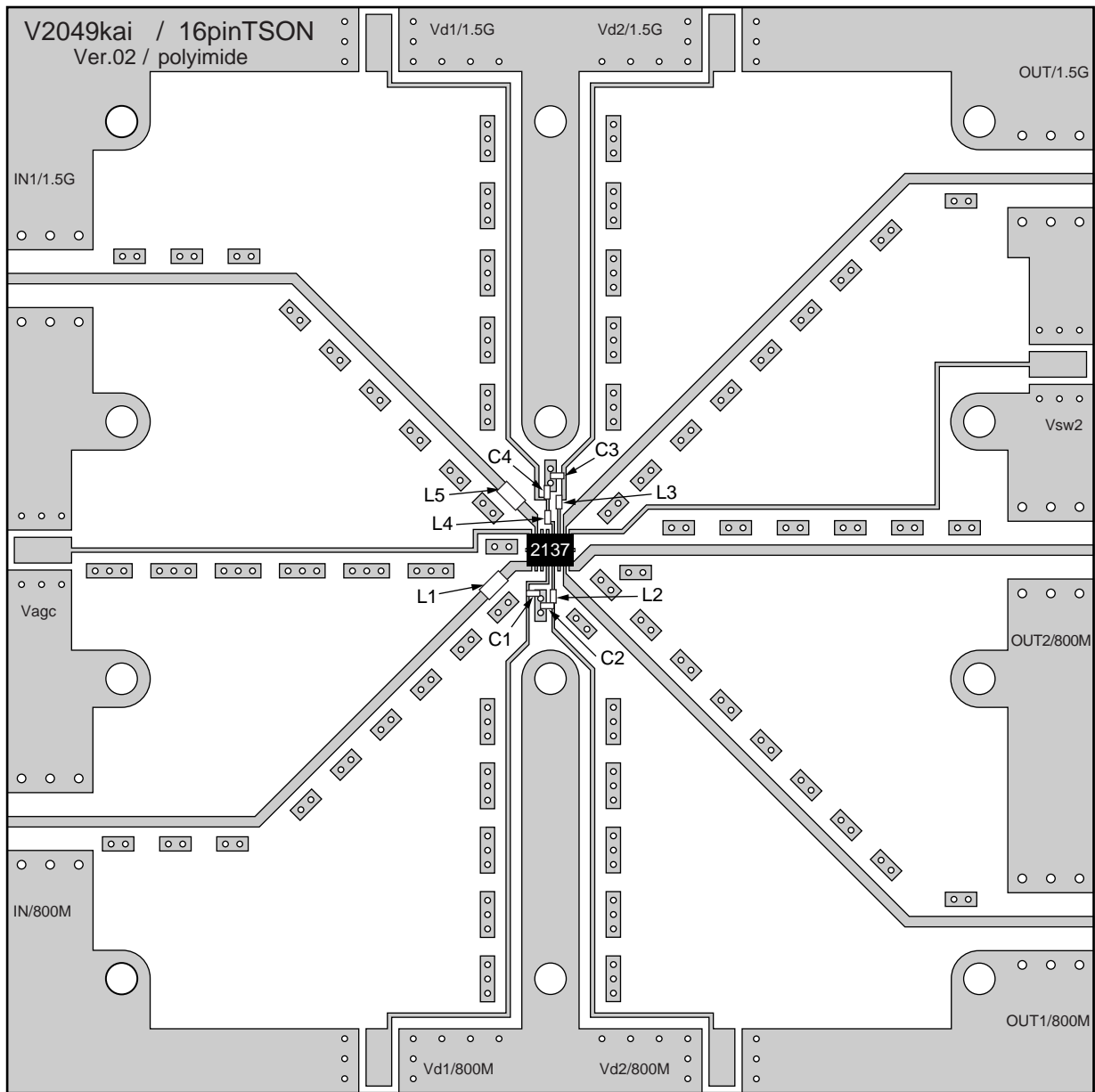
EVALUATION CIRCUIT

($f_{opt1} = 893$ to 960 MHz, $f_{opt2} = 1\,429$ to $1\,453$ MHz, $V_{DD1,3} = 2.7$ V, $V_{DD2,4} = 3.2$ V)



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

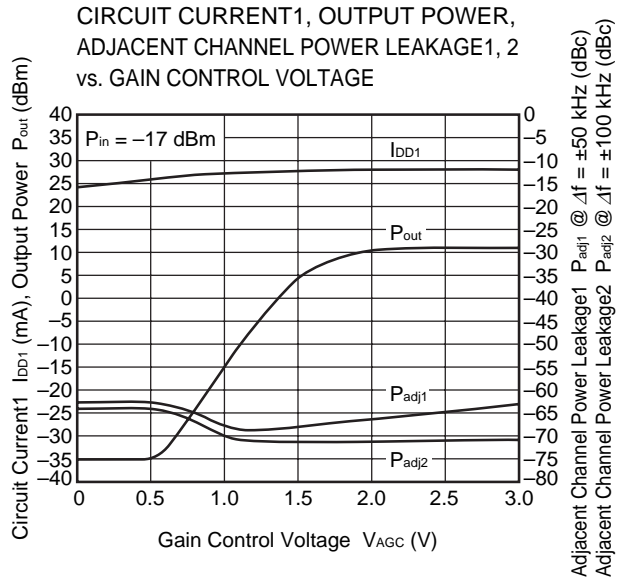
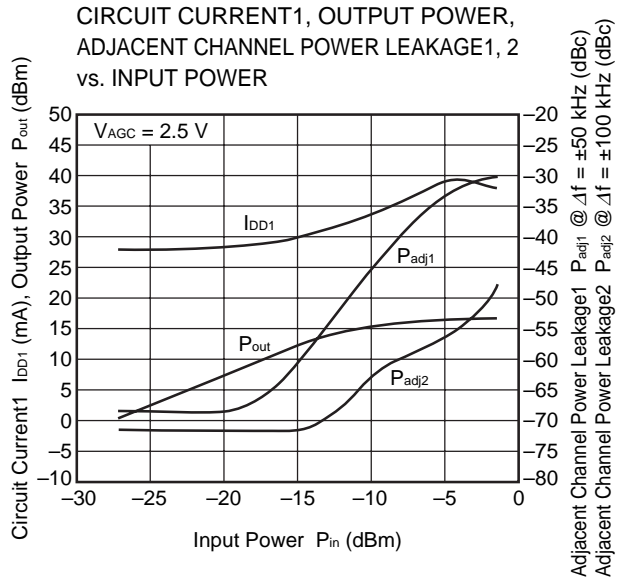


USING THE NEC EVALUATION BOARD

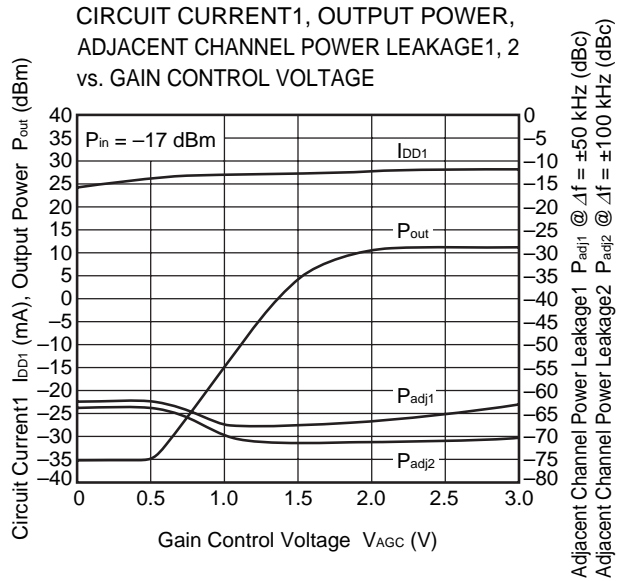
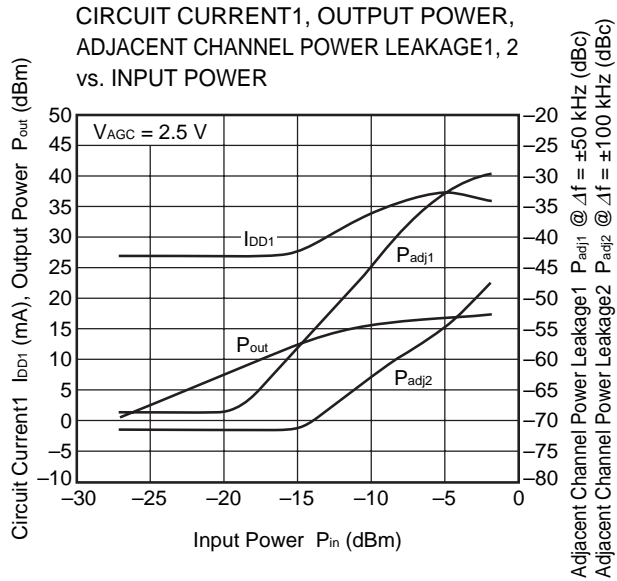
Symbol	Values
L1	12 nH
L2	10 nH
L3	8.2 nH
L4	3.3 nH
L5	5.6 nH
C1, C4	22 nF
C2, C3	1 nF

TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

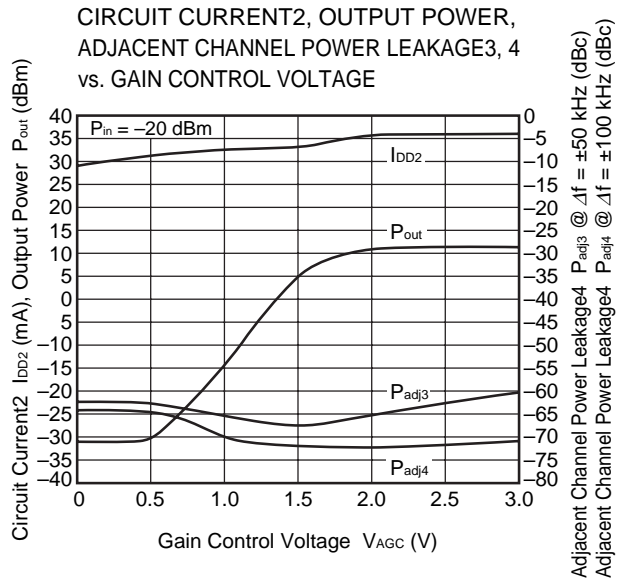
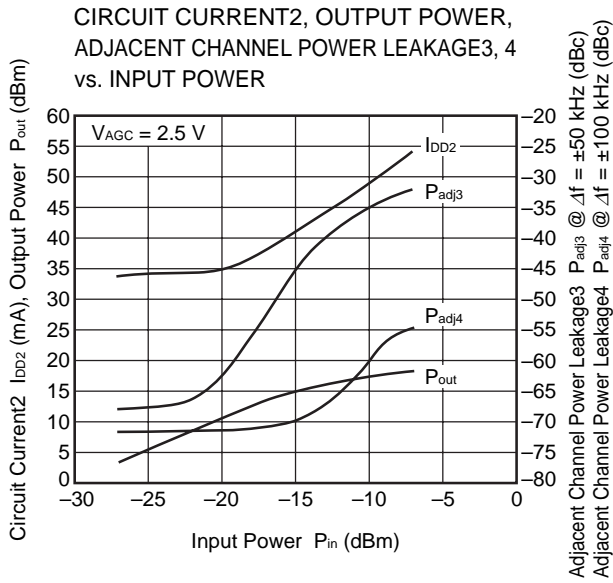
0.8 GHz Band side (INPUT1 – OUTPUT1, f = 960 MHz, V_{DD1} = 2.7 V, V_{DD2} = 3.2 V, V_{SW} = 0 V)



0.8 GHz Band side (INPUT1 – OUTPUT2, f = 960 MHz, V_{DD1} = 2.7 V, V_{DD2} = 3.2 V, V_{SW} = V_{DD1})



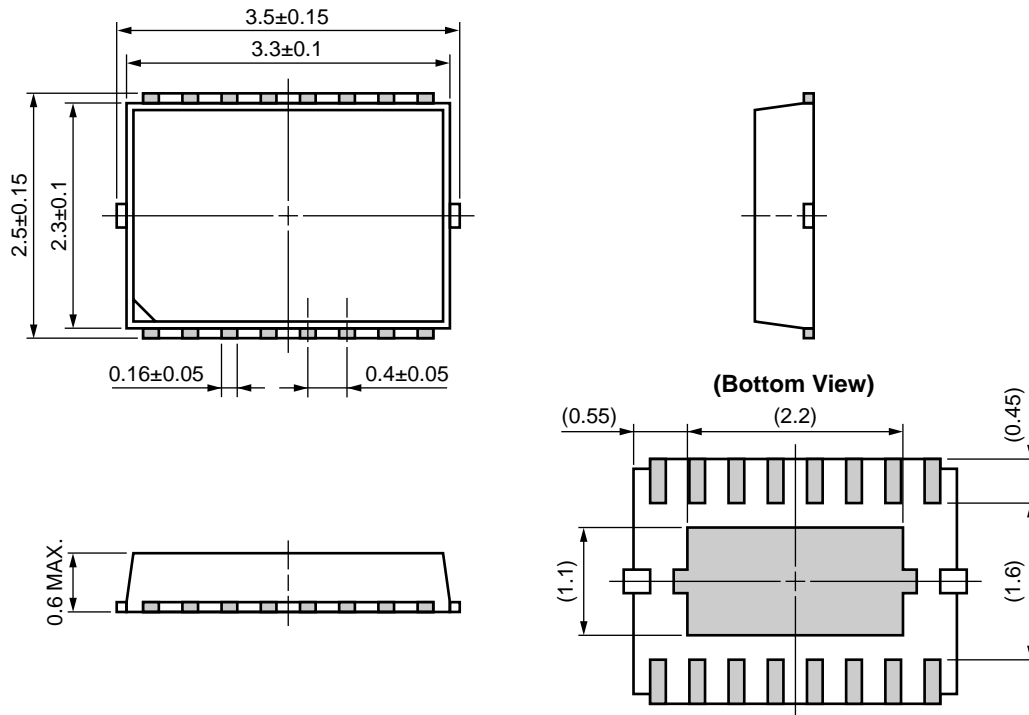
1.5 GHz Band side (INPUT2 – OUTPUT3, $f = 1\,453\text{ MHz}$, $V_{DD3} = 2.7\text{ V}$, $V_{DD4} = 3.2\text{ V}$)



Remark The graphs indicate nominal characteristics

PACKAGE DIMENSIONS

16-PIN PLASTIC TSON (UNIT: mm)



Remark (): Reference value

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) : 215°C or below Time at temperature of 200°C or higher : 25 to 40 seconds Preheating time at 120 to 150°C : 30 to 60 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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M8E 00.4-0110

<p>Caution</p>	<p>GaAs Products</p>	<p>This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.</p> <ul style="list-style-type: none"> • Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below. <ol style="list-style-type: none"> 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials. 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal. • Do not burn, destroy, cut, crush, or chemically dissolve the product. • Do not lick the product or in any way allow it to enter the mouth.
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► For further information, please contact

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