DATA SHEET

BUW84; BUW85
Silicon diffused power transistors

Product specification
Supersedes data of February 1996
File under Discrete Semiconductors, SC06

1997 Aug 14

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Silicon diffused power transistors BUW84; BUW85

DESCRIPTION

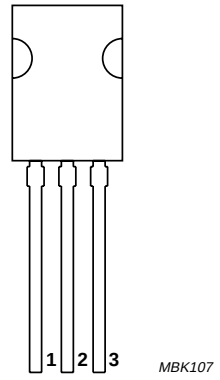
High-voltage, high-speed, glass-passivated NPN power transistor in a SOT82 package.

APPLICATIONS

- Converters
- Inverters
- Switching regulators
- Motor control systems
- Switching applications.

PINNING

PIN	DESCRIPTION
1	base
2	collector; connected to mounting base
3	emitter



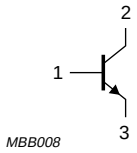


Fig.1 Simplified outline (SOT82) and symbol.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	collector-emitter peak voltage	V_{BE} = 0			
	BUW84		–	800	V
	BUW85		–	1000	V
V_{CEO}	collector-emitter voltage	open base			
	BUW84		–	400	V
	BUW85		–	450	V
V_{CEsat}	collector-emitter saturation voltage	I_C = 1 A; I_B = 200 mA; see Fig.7	–	1	V
I_C	collector current (DC)	see Figs 4 and 5	–	2	A
I_{CM}	collector current (peak value)	see Figs 4 and 5	–	3	A
P_{tot}	total power dissipation	$T_{mb} \leq 25$ °C; see Fig.8	–	50	W
t_f	fall time	resistive load; see Fig.11	0.4	–	µs

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j\text{-}mb}$	thermal resistance from junction to mounting base	2.1	K/W
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient in free air	100	K/W

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	collector-emitter peak voltage	V_{BE} = 0			
	BUW84		–	800	V
	BUW85		–	1000	V
V_{CEO}	collector-emitter voltage	open base			
	BUW84		–	400	V
	BUW85		–	450	V
V_{EBO}	emitter-base voltage	open collector	–	5	V
I_C	collector current (DC)	see Figs 4 and 5	–	2	A
I_{CM}	collector current (peak value)	t_p = 2 ms; see Figs 4 and 5	–	3	A
I_B	base current (DC)		–	0.75	A
I_{BM}	base current (peak value)		–	1	A
I_{BM}	base current (reversed; peak value)	turn-off current	–	–1	A
P_{tot}	total power dissipation	$T_{mb} \leq 25$ °C; see Fig.8	–	50	W
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C

CHARACTERISTICS

T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{CEOsust}$	collector-emitter sustaining voltage	I_C = 100 mA; I_{Boff} = 0; L = 25 mH; see Figs 2 and 3				
	BUW84		400	–	–	V
	BUW85		450	–	–	V
V_{CEsat}	collector-emitter saturation voltage	I_C = 0.3 A; I_B = 30 mA; see Fig.7	–	–	0.8	V
		I_C = 1 A; I_B = 200 mA; see Fig.7	–	–	1	V
V_{BEsat}	base-emitter saturation voltage	I_C = 1 A; I_B = 200 mA	–	–	1.1	V
I_{CES}	collector-emitter cut-off current	$V_{CEM} = V_{CEMSmax}$; V_{BE} = 0; note 1	–	–	200	µA
		$V_{CEM} = V_{CEMSmax}$; V_{BE} = 0; T_j = 125 °C; note 1	–	–	1.5	mA
I_{EBO}	emitter-base cut-off current	V_{EB} = 5 V; I_C = 0	–	–	1	mA
h_{FE}	DC current gain	V_{CE} = 5 V; I_C = 5 A; see Fig.10	15	–	–	
		V_{CE} = 5 V; I_C = 100 mA; see Fig.10	20	50	100	
f_T	transition frequency	V_{CE} = 10 V; I_C = 200 mA; f = 1 MHz	–	20	–	MHz

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Switching times in horizontal deflection circuit (see Fig.11)						
t_{on}	turn-on time	I_{Con} = 1 A; I_{Bon} = 200 mA; I_{Boff} = −400 mA; V_{CC} = 250 V	–	0.2	0.5	µs
t_s	storage time	I_{Con} = 1 A; I_{Bon} = 200 mA; I_{Boff} = −400 mA; V_{CC} = 250 V	–	2	3.5	µs
t_f	fall time	I_{Con} = 1 A; I_{Bon} = 200 mA; I_{Boff} = −400 mA; V_{CC} = 250 V	–	0.4	–	µs
		I_{Con} = 1 A; I_{Bon} = 200 mA; I_{Boff} = −400 mA; V_{CC} = 250 V; T_{mb} = 95 °C	–	–	1.4	µs

Note

1. Measured with a half-sinewave voltage (curve tracer).

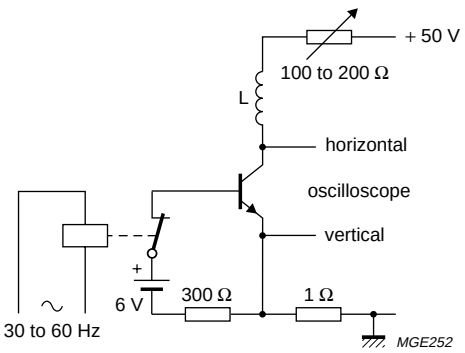


Fig.2 Test circuit for collector-emitter sustaining voltage.

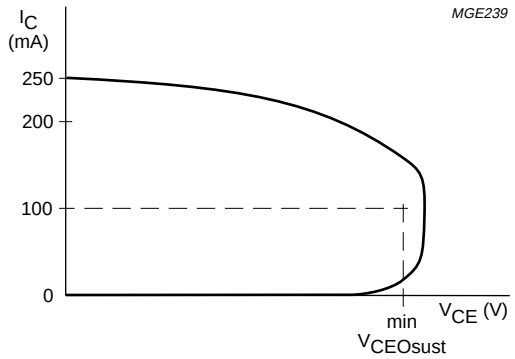
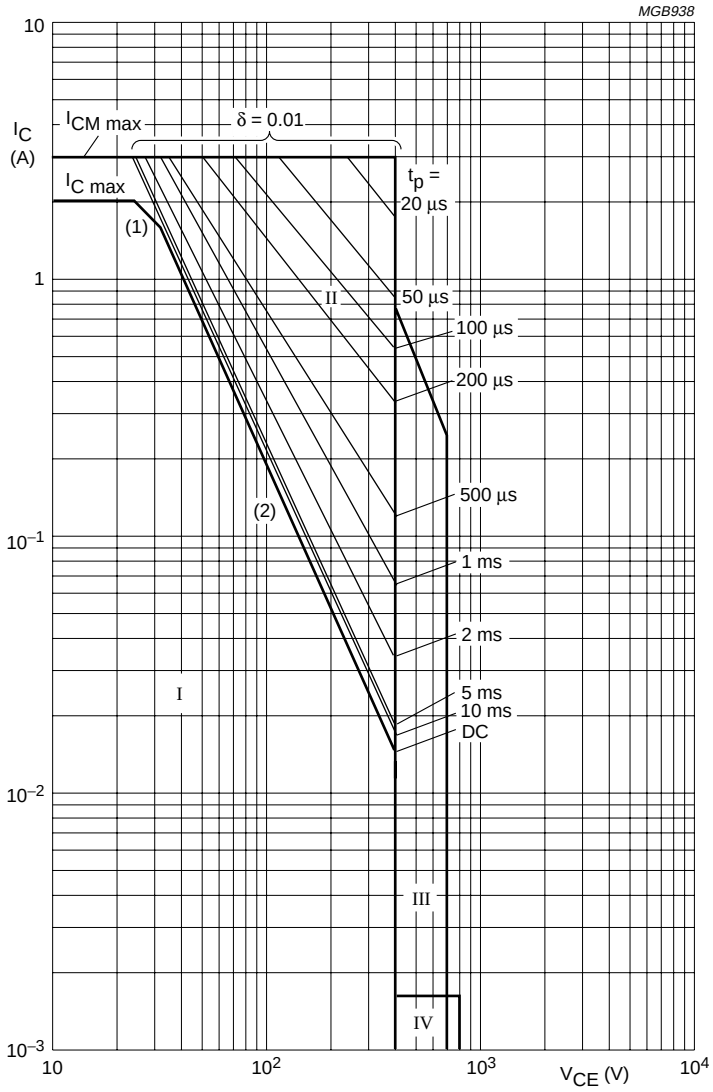


Fig.3 Oscilloscope display for collector-emitter sustaining voltage.



BUW84.

$T_{mb} \leq 25$ °C.

I - Region of permissible DC operation.

II - Permissible extension for repetitive pulse operation.

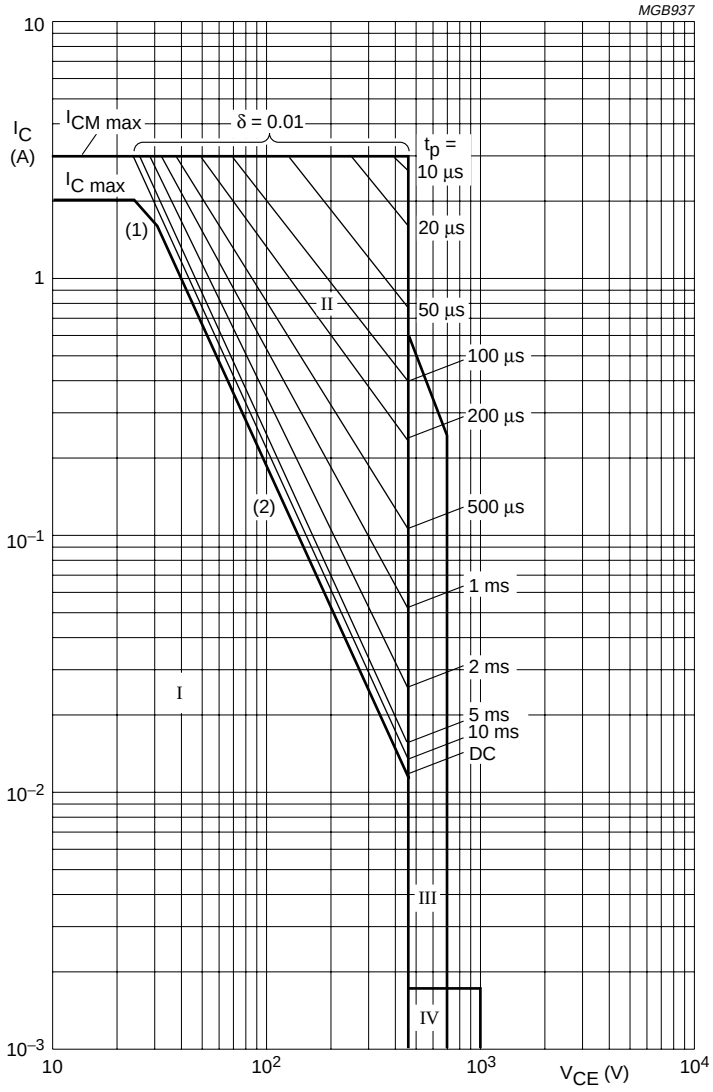
III - Area of permissible operation during turn-on in single transistor converters, provided $R_{BE} \leq 100\ \Omega$ and $t_p \leq 0.6$ µs.

IV - Repetitive pulse operation in this region is permissible provided $V_{BE} \leq 0$ and $t_p \leq 2$ ms.

(1) $P_{tot\ max}$ line.

(2) Second breakdown limits.

Fig.4 Forward bias SOAR.



BUW85.

$T_{mb} \leq 25$ °C.

I - Region of permissible DC operation.

II - Permissible extension for repetitive pulse operation.

III - Area of permissible operation during turn-on in single transistor converters, provided $R_{BE} \leq 100\ \Omega$ and $t_p \leq 0.6$ µs.

IV - Repetitive pulse operation in this region is permissible provided $V_{BE} \leq 0$ and $t_p \leq 2$ ms.

(1) $P_{tot\ max}$ line.

(2) Second breakdown limits.

Fig.5 Forward bias SOAR.

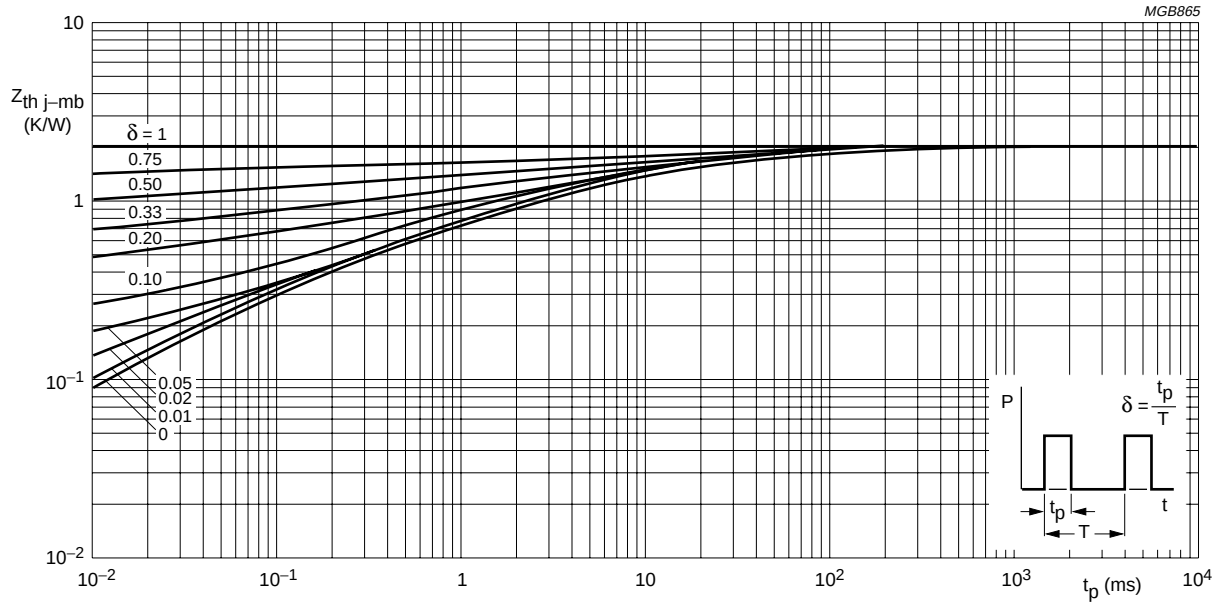
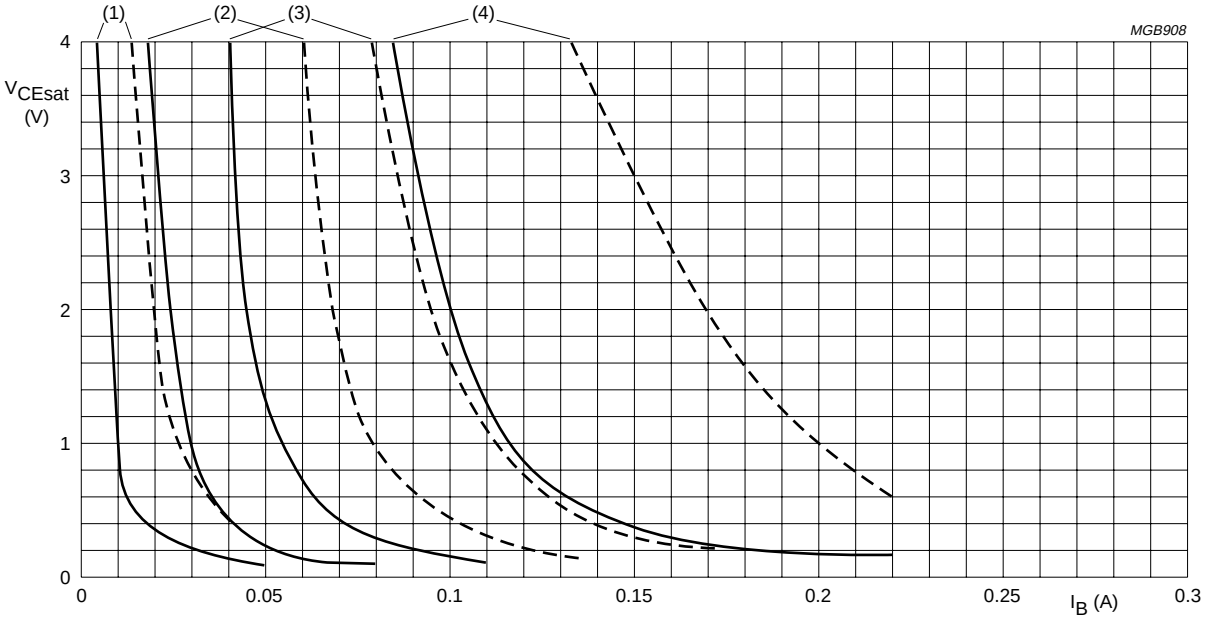


Fig.6 Transient thermal impedance.



(1) I_C = 0.3 A. (2) I_C = 0.5 A. (3) I_C = 0.7 A. (4) I_C = 1 A. T_j = 25 °C; solid line: typical values; dotted line: maximum values.

Fig.7 Collector-emitter saturation voltage as a function of base current; typical values.

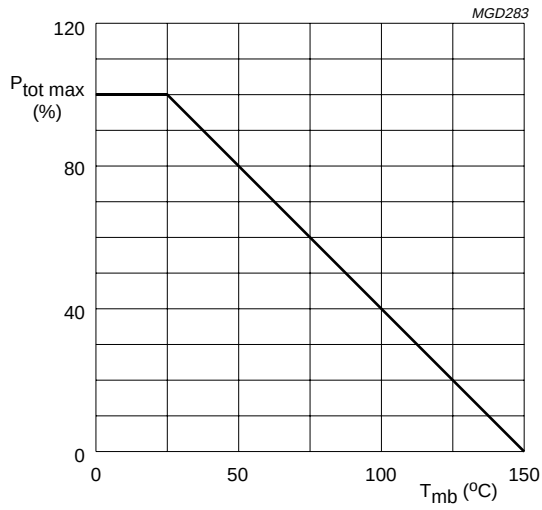
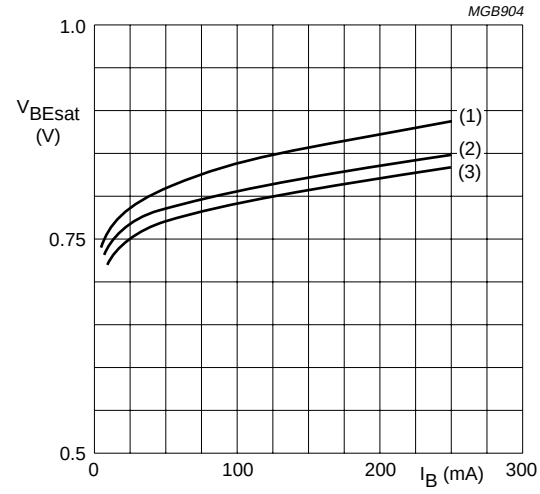


Fig.8 Power derating curve.



T_j = 25 °C.
(1) I_C = 1 A.
(2) I_C = 0.5 A.
(3) I_C = 0.3 A.

Fig.9 Base-emitter saturation voltage as a function of emitter current; typical values.

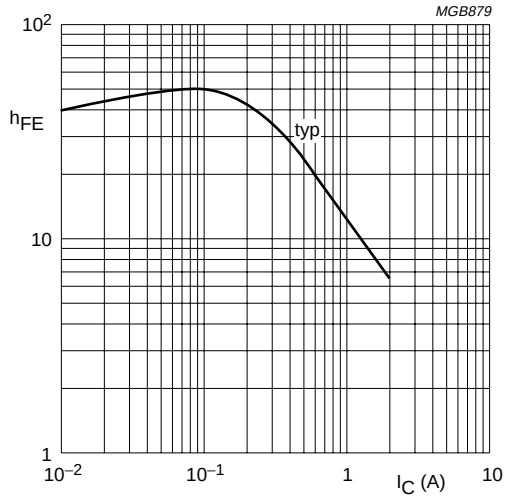


Fig.10 DC current gain; typical values.

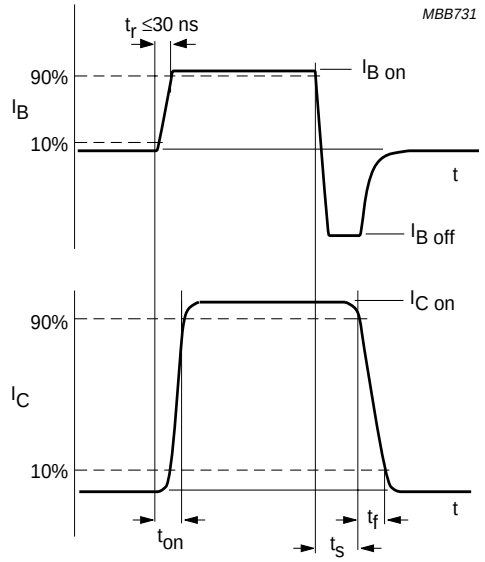


Fig.11 Switching time waveforms with resistive load.

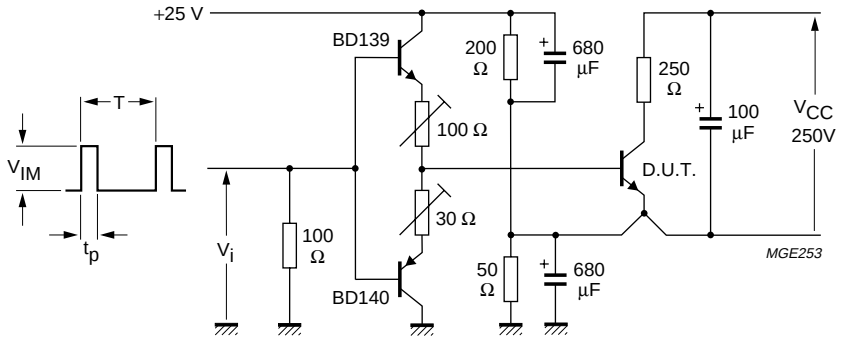
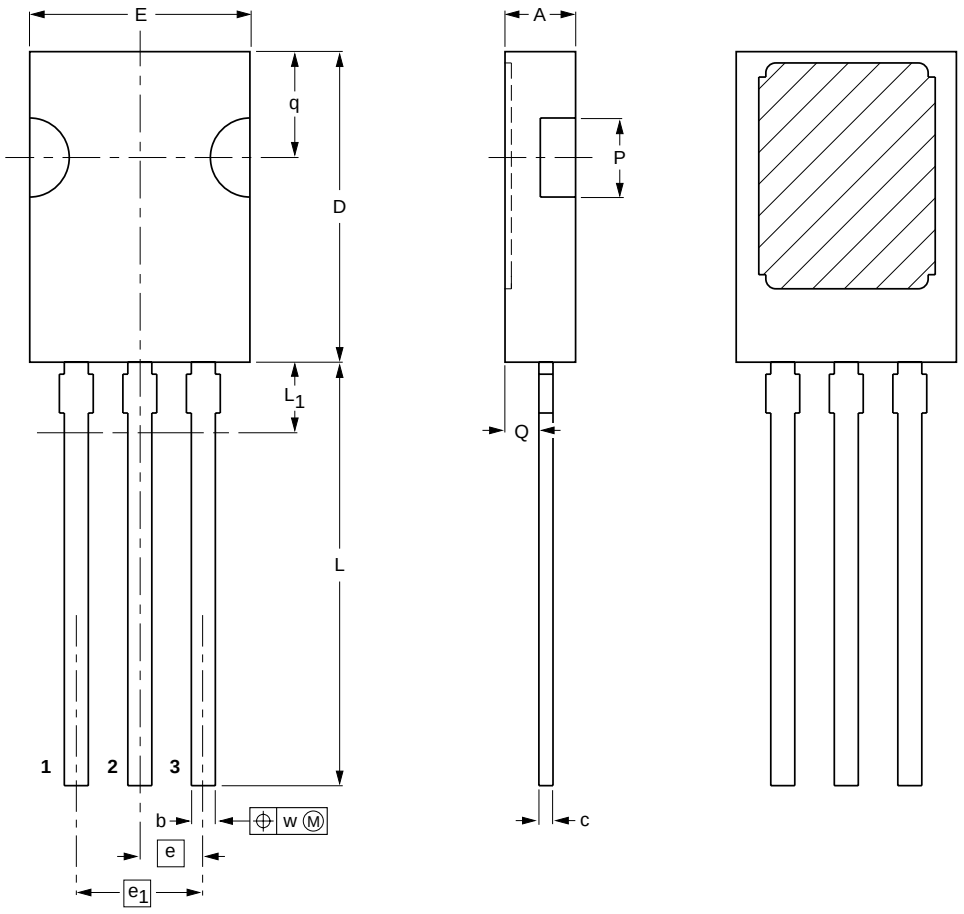


Fig.12 Test circuit resistive load.

PACKAGE OUTLINE

Plastic single-ended package; 3 leads (in-line) **SOT82**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	c	D	E	e	e_1	L	L_1 (1) max.	P	Q	q	w
mm	2.8 2.3	0.88 0.65	0.58 0.47	11.1 10.5	7.8 7.2	2.29	4.58	16.5 15.3	2.54	3.1 2.5	1.5 0.9	3.9 3.5	0.254

Note

1. Terminal dimensions within this zone are uncontrolled to allow for body and terminal irregularities.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT82						97-06-11

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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Argentina: see South America

Australia: 34 Waterloo Road, NORTH RYDE, NSW 2113,
Tel. +61 2 9805 4455, Fax. +61 2 9805 4466

Austria: Computerstr. 6, A-1101 WIEN, P.O. Box 213, Tel. +43 160 1010,
Fax. +43 160 101 1210

Belarus: Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,
220050 MINSK, Tel. +375 172 200 733, Fax. +375 172 200 773

Belgium: see The Netherlands

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Bulgaria: Philips Bulgaria Ltd., Energoproject, 15th floor,
51 James Bourchier Blvd., 1407 SOFIA,
Tel. +359 2 689 211, Fax. +359 2 689 102

Canada: PHILIPS SEMICONDUCTORS/COMPONENTS,
Tel. +1 800 234 7381

China/Hong Kong: 501 Hong Kong Industrial Technology Centre,
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,
Tel. +852 2319 7888, Fax. +852 2319 7700

Colombia: see South America

Czech Republic: see Austria

Denmark: Prags Boulevard 80, PB 1919, DK-2300 COPENHAGEN S,
Tel. +45 32 88 2636, Fax. +45 31 57 0044

Finland: Sinikalliontie 3, FIN-02630 ESPOO,
Tel. +358 9 615800, Fax. +358 9 61580920

France: 4 Rue du Port-aux-Vins, BP317, 92156 SURESNES Cedex,
Tel. +33 1 40 99 6161, Fax. +33 1 40 99 6427

Germany: Hammerbrookstraße 69, D-20097 HAMBURG,
Tel. +49 40 23 53 60, Fax. +49 40 23 536 300

Greece: No. 15, 25th March Street, GR 17778 TAVROS/ATHENS,
Tel. +30 1 4894 339/239, Fax. +30 1 4814 240

Hungary: see Austria

India: Philips INDIA Ltd, Band Box Building, 2nd floor,
254-D, Dr. Annie Besant Road, Worli, MUMBAI 400 025,
Tel. +91 22 493 8541, Fax. +91 22 493 0966

Indonesia: see Singapore

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Tel. +353 1 7640 000, Fax. +353 1 7640 200

Israel: RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053,
TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

Italy: PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3,
20124 MILANO, Tel. +39 2 6752 2531, Fax. +39 2 6752 2557

Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku, TOKYO 108,
Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

Korea: Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,
Tel. +82 2 709 1412, Fax. +82 2 709 1415

Malaysia: No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,
Tel. +60 3 750 5214, Fax. +60 3 757 4880

Mexico: 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,
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Middle East: see Italy

Netherlands: Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,
Tel. +31 40 27 82785, Fax. +31 40 27 88399

New Zealand: 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,
Tel. +64 9 849 4160, Fax. +64 9 849 7811

Norway: Box 1, Manglerud 0612, OSLO,
Tel. +47 22 74 8000, Fax. +47 22 74 8341

Philippines: Philips Semiconductors Philippines Inc.,
106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,
Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

Poland: Ul. Lukiska 10, PL 04-123 WARSZAWA,
Tel. +48 22 612 2831, Fax. +48 22 612 2327

Portugal: see Spain

Romania: see Italy

Russia: Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,
Tel. +7 095 755 6918, Fax. +7 095 755 6919

Singapore: Lorong 1, Toa Payoh, SINGAPORE 1231,
Tel. +65 350 2538, Fax. +65 251 6500

Slovakia: see Austria

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South Africa: S.A. PHILIPS Pty Ltd., 195-215 Main Road Martindale,
2092 JOHANNESBURG, P.O. Box 7430 Johannesburg 2000,
Tel. +27 11 470 5911, Fax. +27 11 470 5494

South America: Rua do Rocio 220, 5th floor, Suite 51,
04552-903 São Paulo, SÃO PAULO - SP, Brazil,
Tel. +55 11 821 2333, Fax. +55 11 829 1849

Spain: Balmes 22, 08007 BARCELONA,
Tel. +34 3 301 6312, Fax. +34 3 301 4107

Sweden: Kottbygatan 7, Akalla, S-16485 STOCKHOLM,
Tel. +46 8 632 2000, Fax. +46 8 632 2745

Switzerland: Allmendstrasse 140, CH-8027 ZÜRICH,
Tel. +41 1 488 2686, Fax. +41 1 481 7730

Taiwan: Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1,
TAIPEI, Taiwan Tel. +886 2 2134 2865, Fax. +886 2 2134 2874

Thailand: PHILIPS ELECTRONICS (THAILAND) Ltd.,
209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,
Tel. +66 2 745 4090, Fax. +66 2 398 0793

Turkey: Talatpasa Cad. No. 5, 80640 GÜLTEPE/ISTANBUL,
Tel. +90 212 279 2770, Fax. +90 212 282 6707

Ukraine: PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,
252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

United Kingdom: Philips Semiconductors Ltd., 276 Bath Road, Hayes,
MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

United States: 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,
Tel. +1 800 234 7381

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