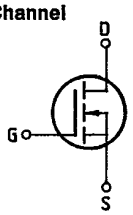
Main ratings

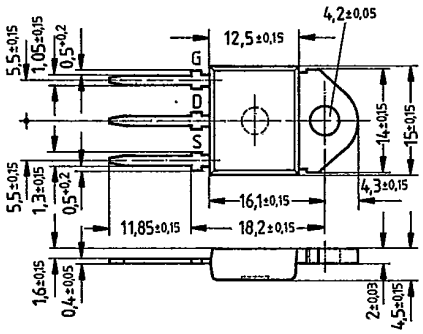
Drain-source voltage V_{DS} = 400 V
Continuous drain current I_D = 11,5 A
Drain-source on-resistance $R_{DS(on)}$ = 0,4 Ω

N-Channel



Description SIPMOS, N-channel, enhancement mode
Case Plastic package 15 in accordance with DIN 41869 or TO 218 AA (TOP 3) in accordance with JEDEC.
The drain terminal is conductively connected to the mounting flange.
Approx. weight 4,5 g

Type	Ordering code
S BUZ 351	C67078-A3103-A2



Dimensions in mm

Maximum ratings

Description	Symbols	Ratings	Units	Conditions
Drain-source voltage	V_{DS}	400	V	
Drain-gate voltage	V_{DGR}	400	V	R_{GS} = 20 kΩ
Continuous drain current	I_D	11,5	A	T_C = 30 °C
Pulsed drain current	I_{Dpuls}	46	A	T_C = 25 °C
Gate-source voltage	V_{GS}	±20	V	
Max. power dissipation	P_D	125	W	T_C = 25 °C
Operating and storage temperature range	T_j T_{stg}	−55 ... +150	°C	
DIN humidity category		E	–	DIN 40040
IEC climatic category		55/150/56		DIN IEC 68-1
Thermal resistance				
Chip – case	$R_{th\,JC}$	≤ 1,0	K/W	
Chip – ambient	$R_{th\,JA}$	≤ 45	K/W	

S Preferred Type

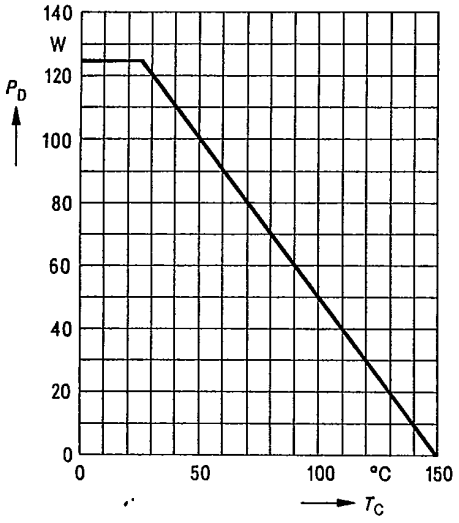
1318 C-11

Electrical characteristics

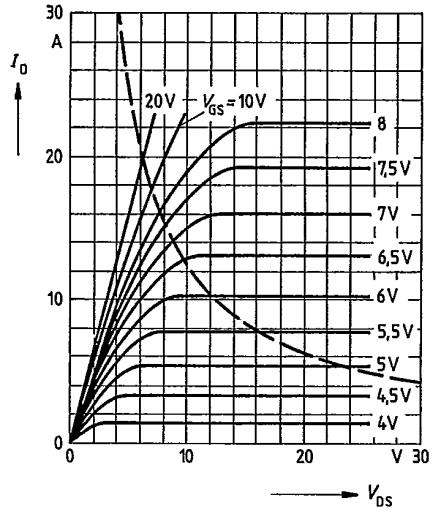
(at T_j = 25 °C unless otherwise specified)

Description	Symbol	Characteristics min.	typ.	max.	Unit	Conditions
Static ratings						
Drain-source breakdown voltage	$V_{(BR)\,DSS}$	400	–	–	V	V_{GS} = 0V I_D = 0,25mA
Gate threshold voltage	$V_{GS\,(th)}$	2,1	3,0	4,0		V_{DS} = V_{GS} I_D = 1mA
Zero gate voltage drain current	I_{DSS}	– –	20 100	250 1000	µA	T_j = 25 °C T_j = 125 °C V_{DS} = 400V V_{GS} = 0V
Gate-source leakage current	I_{GSS}	–	10	100	nA	V_{GS} = 20V V_{DS} = 0V
Drain-source on-resistance	$R_{DS\,(on)}$	–	0,35	0,4	Ω	V_{GS} = 10V I_D = 5,5A
Dynamic ratings						
Forward transconductance	g_{fs}	3,3	4,5	–	S	V_{DS} = 25V I_D = 5,5A
Input capacitance	C_{iss}	–	3,8	4,9	nF	V_{GS} = 0V V_{DS} = 25V f = 1MHz
Output capacitance	C_{oss}	–	300	500	pF	
Reverse transfer capacitance	C_{rss}	–	120	200		
Turn-on time t_{on} ($t_{on} = t_{d\,(on)} + t_r$)	$t_{d\,(on)}$	–	50	75	ns	V_{CC} = 30V I_D = 2,9A V_{GS} = 10V R_{GS} = 50Ω
	t_r	–	80	120		
Turn-off time t_{off} ($t_{off} = t_{d\,(off)} + t_f$)	$t_{d\,(off)}$	–	330	430		
	t_f	–	110	140		
Reverse diode						
Continuous reverse drain current	I_{DR}	–	–	11,5	A	T_C = 25 °C
Pulsed reverse drain current	I_{DRM}	–	–	46		
Diode forward on-voltage	V_{SD}	–	1,3	1,7	V	I_F = 2 × I_{DR} V_{GS} = 0V, T_j = 25 °C
Reverse recovery time	t_{rr}	–	1,0	–	µs	T_j = 25 °C I_F = I_{DR} $d_{iF/dt}$ = 100A/µs V_R = 100V
Reverse recovery charge	Q_{rr}	–	10	–	µC	

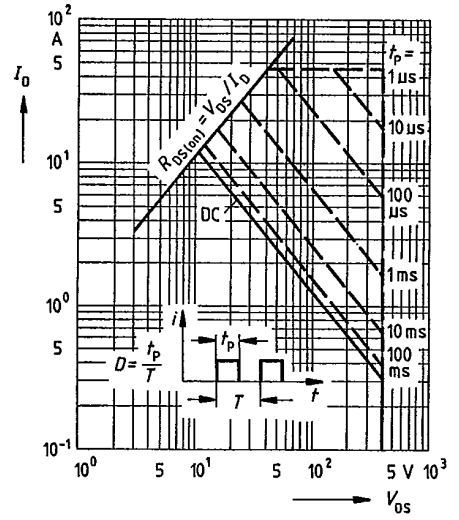
Power dissipation $P_D = f(T_C)$



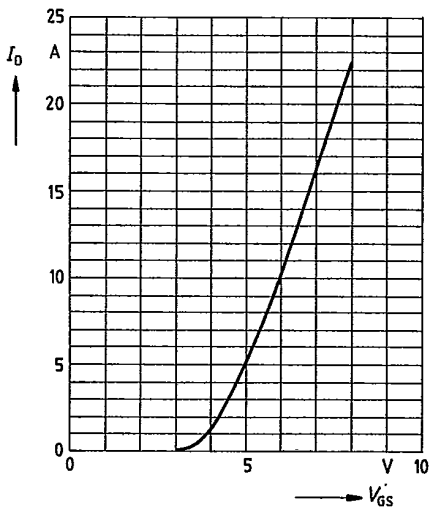
Typical output characteristics $I_D = f(V_{DS})$
parameter: 80 µs pulse test,
$T_j = 25$ °C



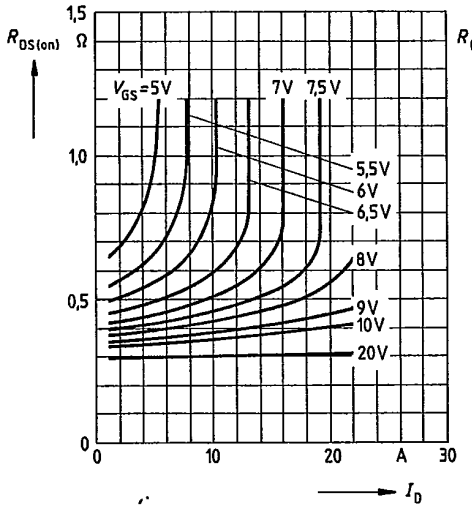
Safe operating area $I_D = f(V_{DS})$
parameter: $D = 0.01$, $T_C = 25$ °C



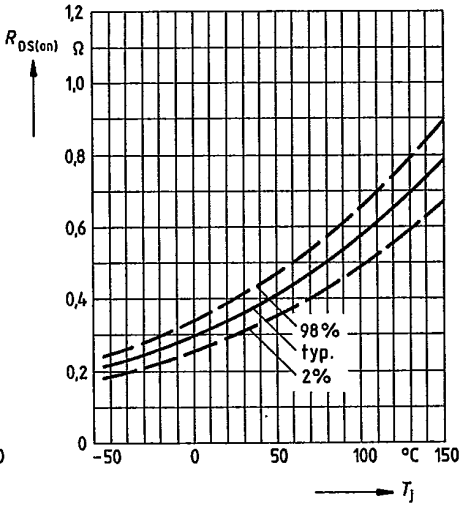
Typical transfer characteristic $I_D = f(V_{GS})$
parameter: 80 µs pulse test,
$V_{DS} = 25$V, $T_j = 25$ °C



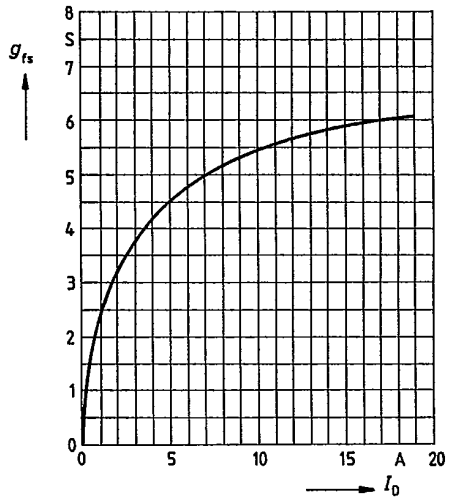
Typical drain-source on-state resistance
$R_{DS(on)} = f(I_D)$
parameter: V_{GS}; $T_j = 25°C$



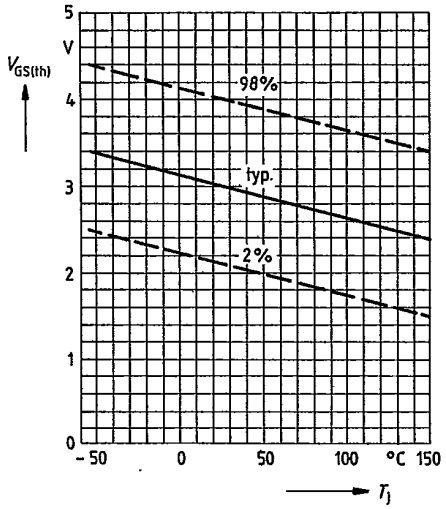
Drain-source on-state resistance
$R_{DS(on)} = f(T_j)$
parameter: I_D = 5,5A, V_{GS} = 10V
(spread)



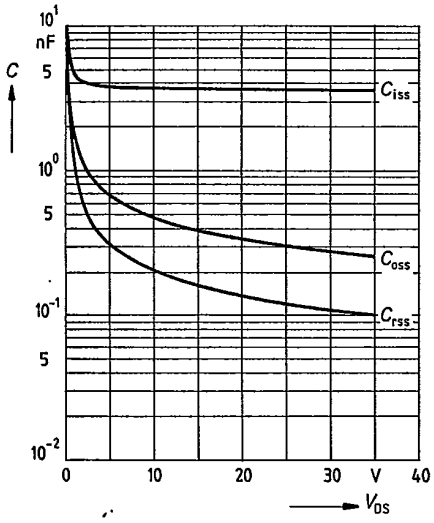
Typical transconductance $g_{fs} = f(I_D)$
parameter: 80 µs pulse test,
V_{DS} = 25V, T_j = 25°C



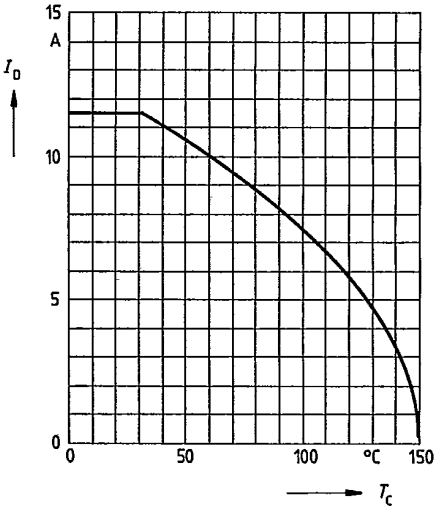
Gate threshold voltage $V_{GS(th)} = f(T_j)$
parameter: $V_{DS} = V_{GS}$, I_D = 1mA
(spread)



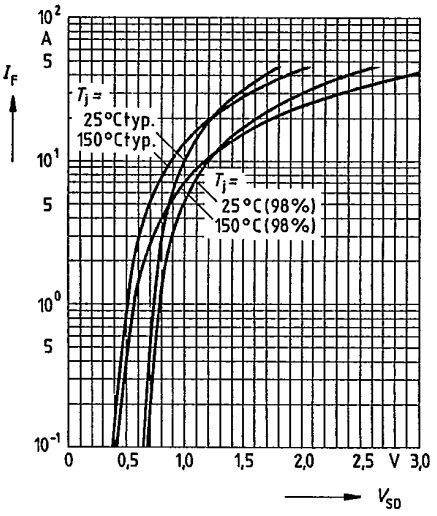
Typical capacitances $C = f(V_{DS})$
parameter: $V_{GS} = 0$, $f = 1$MHz



Continuous drain current $I_D = f(T_C)$
parameter: $V_{GS} \geq 10$V

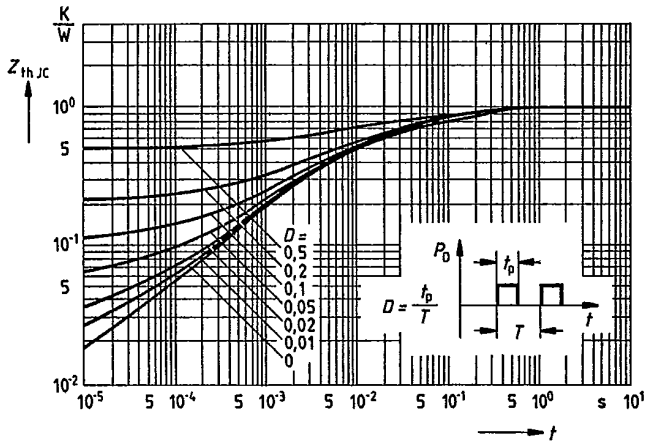


Forward characteristic of reverse diode
$I_F = f(V_{SD})$
parameter: T_j, $t_p = 80$ µs
(spread)

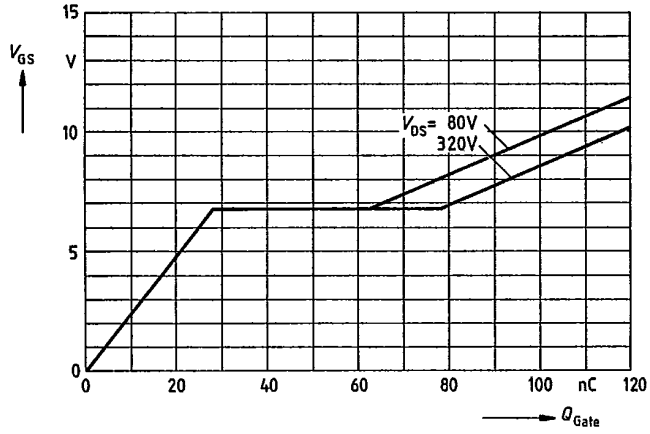


740 1322 D-01

Transient thermal impedance $Z_{thJC} = f(t)$
parameter: $D = t_p / T$



Typical gate-charge $V_{GS} = f(Q_{Gate})$
parameter: $I_{D\,puls} = 17{,}3A$



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