2SK3723

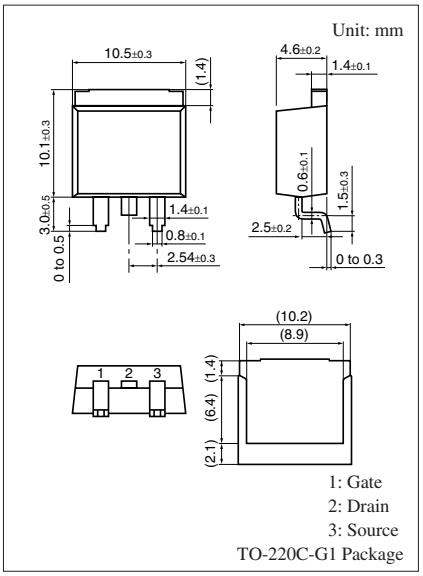
N-channel enhancement mode MOSFET

■ Features

- Low on-resistance, low Q_g
- High avalanche resistance

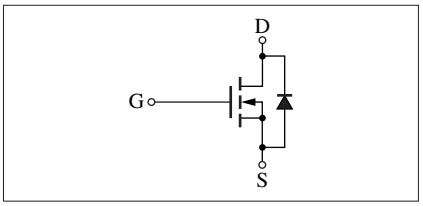
■ Applications

- For PDP
- For high-speed switching



Marking Symbol: K3723

Internal Connection



■ Absolute Maximum Ratings $T_C = 25°C$

Parameter	Symbol	Rating	Unit
Drain-source surrender voltage	V_{DSS}	150	V
Gate-source surrender voltage	V_{GSS}	±30	V
Drain current	I_D	30	A
Peak drain current	I_{DP}	120	A
Power dissipation	P_D	50	W
$T_a = 25°C$		3	
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

■ Electrical Characteristics $T_C = 25°C \pm 3°C$

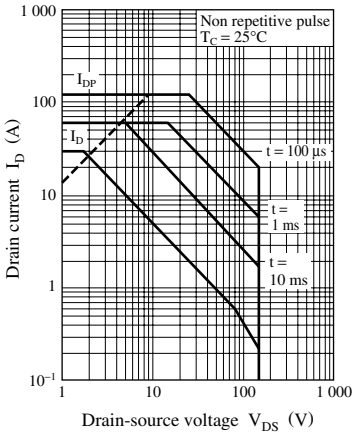
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source surrender voltage	V_{DSS}	I_D = 1 mA, V_{GS} = 0	150			V
Gate threshold voltage	V_{th}	V_{DS} = 25 V, I_D = 1 mA	2		4	V
Drain-source cutoff current	I_{DSS}	V_{DS} = 120 V, V_{GS} = 0			100	μA
Gate-source cutoff current	I_{GSS}	V_{GS} = ±30 V, V_{DS} = 0			±1	μA
Drain-source ON resistance	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 15 A		33	42	mΩ
Forward transfer admittance	$\|Y_{fs}\|$	V_{DS} = 25 V, I_D = 15 A	8	18		S
Short-circuit forward transfer capacitance (Common-source)	C_{iss}	V_{DS} = 25 V, V_{GS} = 0, f = 1 MHz		2900		pF
Short-circuit output capacitance (Common-source)	C_{oss}			618		pF
Reverse transfer capacitance (Common-source)	C_{rss}			91		pF
Turn-on delay time	$t_{d(on)}$	$V_{DD} \approx$ 100 V, I_D = 15 A		32		ns
Rise time	T_r	R_L = 6.7 Ω, V_{GS} = 10 V		46		ns
Turn-off delay time	$t_{d(off)}$			227		ns
Fall time	t_f			66		ns

■ Electrical Characteristics (continued) $T_C = 25°C \pm 3°C$

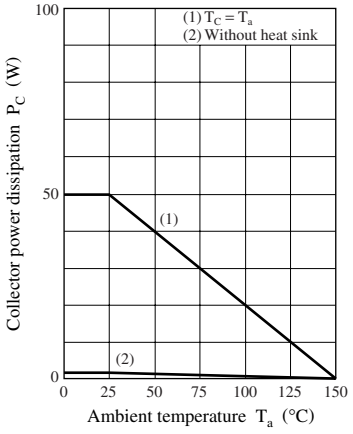
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode foward voltage	V_{DSF}	I_{DR} = 30 A, V_{GS} = 0			−1.5	V
Reverse recovery time	t_{rr}	L = 230 µH, V_{DD} = 100 V		130		ns
Reverse recovery charge	Q_{rr}	I_{DR} = 15 A, di/dt = 100 A/µs		533		nC
Gate charge load	Q_g	V_{DD} = 100 V, I_D = 15 A		55.4		nC
Gate-source charge	Q_{gs}	V_{GS} = 10 V		9.1		nC
Gate-drain charge	Q_{gd}			22.4		nC
Thermal resistance (ch-c)	$R_{th(ch-c)}$				2.5	°C/W
Thermal resistance (ch-a)	$R_{th(ch-a)}$				89.2	°C/W

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

Safe operation area



P_C — T_a



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