

PNP PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDC)
- Built-In Biasing Resistors
- **Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Part Number	R1 (NOM)	R2 (NOM)
DDA124EU	22KΩ	22KΩ
DDA144EU	47KΩ	47KΩ
DDA114YU	10KΩ	47KΩ
DDA123JU	2.2KΩ	47KΩ
DDA114EU	10KΩ	10KΩ

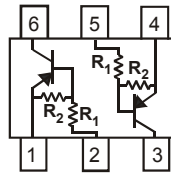
Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208^(e3)
- Weight: 0.006 grams (approximate)

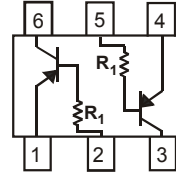
Part Number	R1 Only
DDA113TU	1KΩ
DDA143TU	4.7KΩ
DDA114TU	10KΩ



Top View



R1, R2



R1 Only

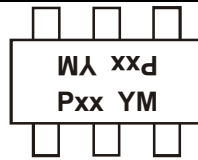
Device Schematic

Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDA124EU-7-F	AEC-Q101	P17	7	8	3,000
DDA124EUQ-7-F	Automotive	P17	7	8	3,000
DDA124EUQ-13-F	Automotive	P17	13	8	10,000
DDA144EU-7-F	AEC-Q101	P20	7	8	3,000
DDA144EUQ-7-F	Automotive	P20	7	8	3,000
DDA114YU-7-F	AEC-Q101	P14	7	8	3,000
DDA114YUQ-7-F	Automotive	P14	7	8	3,000
DDA123JU-7-F	AEC-Q101	P06	7	8	3,000
DDA114EU-7-F	AEC-Q101	P13	7	8	3,000
DDA114EUQ-7-F	Automotive	P13	7	8	3,000
DDA113TU-7-F	AEC-Q101	P01	7	8	3,000
DDA143TU-7-F	AEC-Q101	P07	7	8	3,000
DDA143TUQ-7-F	Automotive	P07	7	8	3,000
DDA143TUQ-13-F	Automotive	P07	13	8	10,000
DDA114TU-7-F	AEC-Q101	P12	7	8	3,000
DDA114TUQ-7-F	Automotive	P12	7	8	3,000
DDA114TUQ-13-F	Automotive	P12	13	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



Pxx = Product Type Marking Code (See Ordering Information)
 YM = Date Code Marking
 Y = Year (ex: T = 2006)
 M = Month (ex: 9 = September)

Date Code Key

Year	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	R	S	T	U	V	W	X	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Supply Voltage (1) to (6) and (4) to (3)		V _{CC}	-50	V
Input Voltage (1) to (2) and (4) to (5)	DDA124EU	V _{IN}	+10 to -40	V
	DDA144EU		+10 to -40	
	DDA114YU		+6 to -40	
	DDA123JU		+5 to -12	
	DDA114EU		+10 to -40	
	DDA113TU		+5V max	
	DDA143TU		+5V max	
Output Current	DDA124EU	I _O	-30	mA
	DDA144EU		-30	
	DDA114YU		-70	
	DDA123JU		-100	
	DDA114EU		-50	
	DDA113TU		-100	
	DDA143TU		-100	
DDA114TU	-100			
Output Current	I _{C(MAX)}	-100	mA	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 6 & 7)	P _D	200	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout.
 7. 150mW per element must not be exceeded.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic (DDA113TU & DDA143TU & DDA114TU only)	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-50	—	—	V	I _C = -50μA
Collector-Emitter Breakdown Voltage	BV _{CEO}	-50	—	—	V	I _C = -1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-5	—	—	V	I _E = -50μA
Collector Cutoff Current	I _{CBO}	—	—	-0.5	μA	V _{CB} = -50V
Emitter Cutoff Current	I _{EBO}	—	—	-0.5	μA	V _{EB} = -4V
Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	—	-0.3	V	I _C /I _B = -2.5mA / -0.25mA DDA143TU I _C /I _B = -1mA / -0.1mA DDA114TU I _C /I _B = -10mA / -1mA DDA113TU
DC Current Transfer Ratio	h _{FE}	100 160	250 —	600 —	—	I _C = -1mA, V _{CE} = -5V I _C = -1mA, V _{CE} = -5V DDA143TUQ
Input Resistor (R ₁) Tolerance	ΔR ₁	-30	—	+30	%	—
Gain-Bandwidth Product (Note 7)	f _T	—	250	—	MHz	V _{CE} = -10V, I _E = 5mA, f = 100MHz

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition	
Input Voltage	V _{I(off)}	DDA124EU	-0.5	-1.1	—	V	V _{CC} = -5V, I _O = -100μA
		DDA144EU	-0.5	-1.1			
DDA114YU		-0.3	—				
DDA123JU		-0.5	—				
DDA114EU		-0.5	-1.1				
Input Voltage	V _{I(on)}	DDA124EU	—	-1.9	-3.0	V	V _O = -0.3, I _O = -5mA V _O = -0.3, I _O = -2mA V _O = -0.3, I _O = -1mA V _O = -0.3, I _O = -5mA V _O = -0.3, I _O = -10mA
		DDA144EU	—	-1.9	-3.0		
		DDA114YU	—	—	-1.4		
		DDA123JU	—	—	-1.1		
		DDA114EU	—	-1.9	-3.0		
Output Voltage	V _{O(on)}	—	-0.1	-0.3	V	I _O /I _I = -10mA / -0.5mA I _O /I _I = -10mA / -0.5mA I _O /I _I = -5mA / -0.25mA I _O /I _I = -5mA / -0.25mA I _O /I _I = -10mA / -0.5mA	
Input Current	I _I	—	—	-0.36 -0.18 -0.88 -3.6 -0.88	mA	V _I = -5V	
Output Current	I _{O(off)}	—	—	-0.5	μA	V _{CC} = -50V, V _I = -0V	
DC Current Gain	G _I	56 60 68 68 80 30	—	—	—	V _O = -5V, I _O = -5mA V _O = -5V, I _O = -5mA V _O = -5V, I _O = -5mA V _O = -5V, I _O = -10mA V _O = -5V, I _O = -10mA V _O = -5V, I _O = -5mA	
Input Resistor (R ₁) Tolerance	ΔR ₁	-30	—	+30	%	—	
Resistance Ratio Tolerance	R ₂ /R ₁	-20	—	+20	%	—	
Gain-Bandwidth Product	f _T	—	250	—	MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz	

Note: 7. Transistor - For Reference Only

Typical Curves – DDA123JU (@T_A = +25°C, unless otherwise specified.)

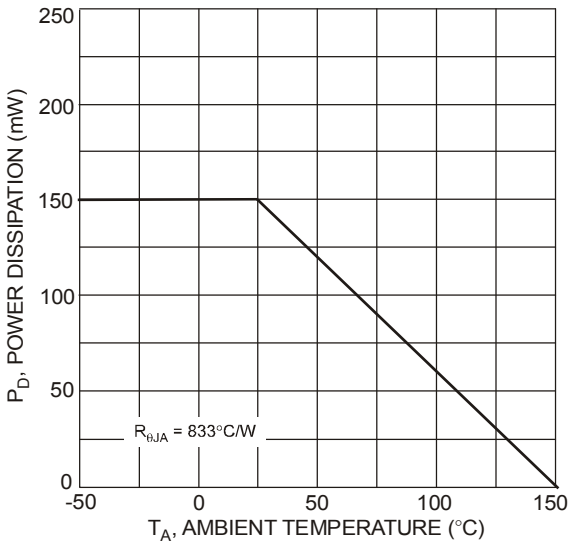


Fig. 1 Power Dissipation vs. Ambient Temperature

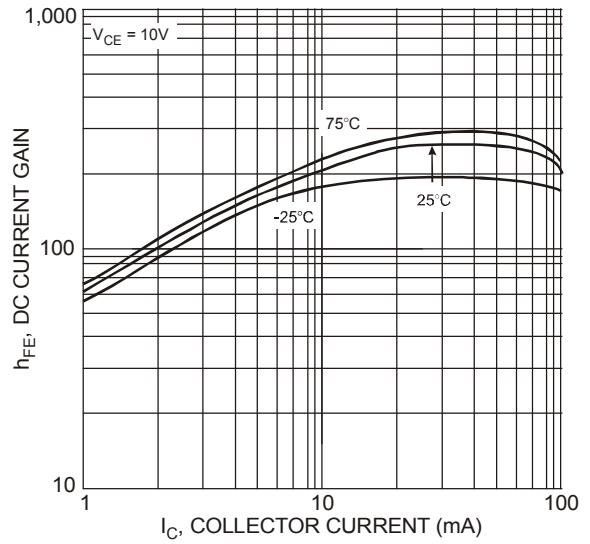


Fig. 2 Typical DC Current Gain vs. Collector Current

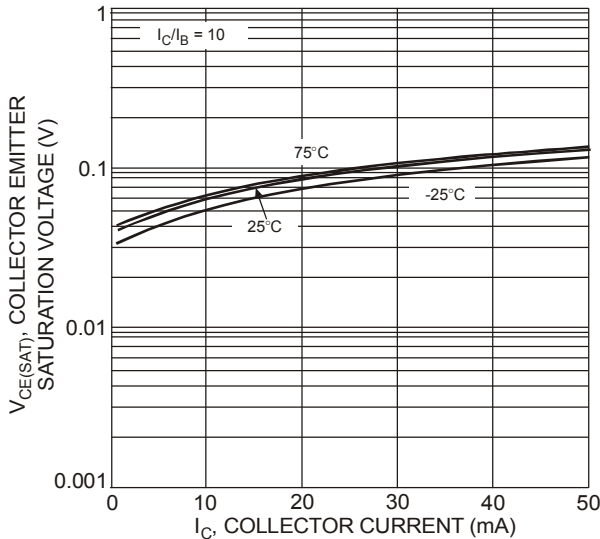


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

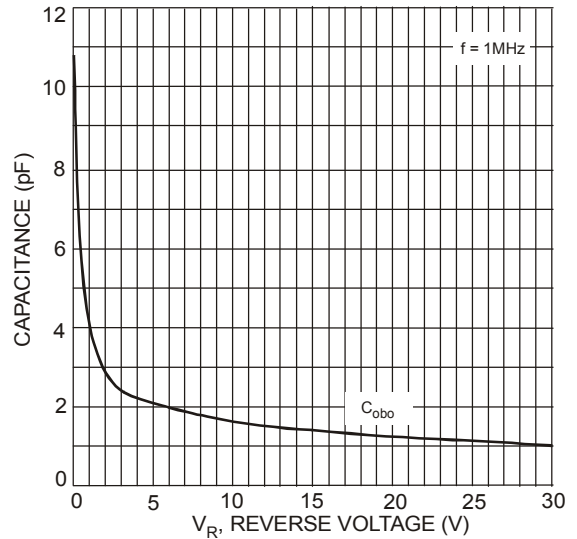


Fig. 4 Typical Capacitance Characteristics

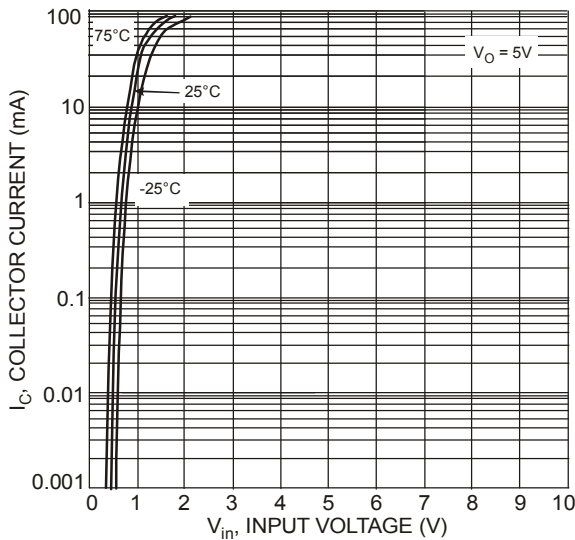


Fig. 5 Collector Current vs. Input Voltage

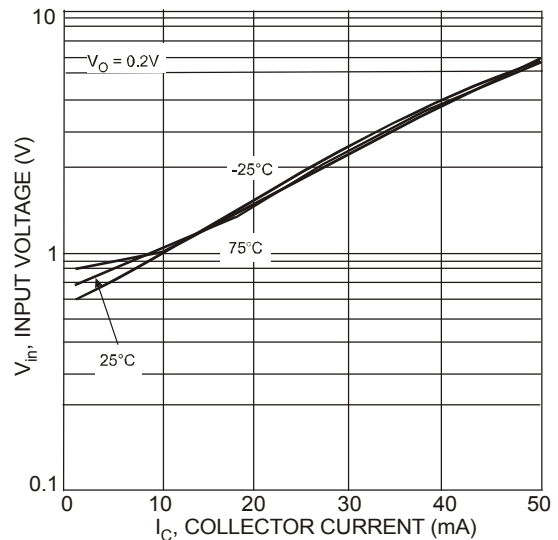


Fig. 6 Input Voltage vs. Collector Current

Typical Curves – DDA114TU (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

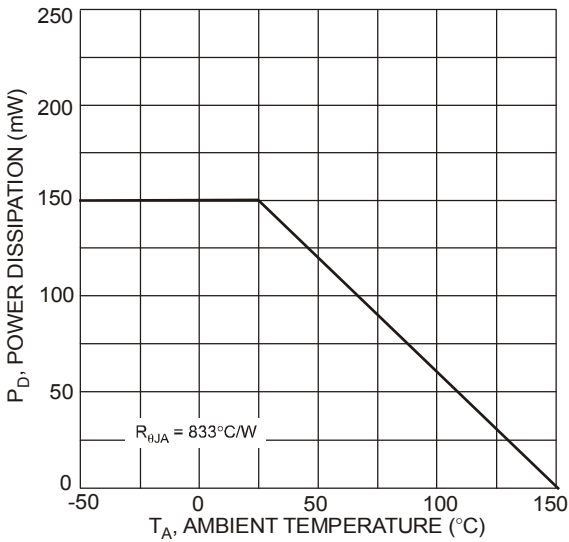


Fig. 1 Power Dissipation vs. Ambient Temperature

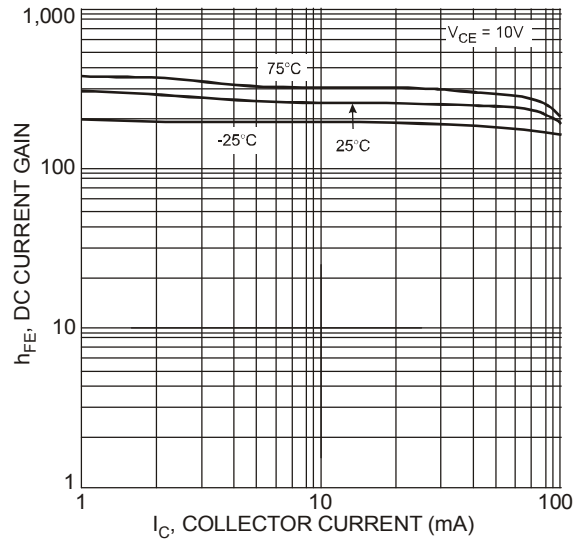


Fig. 2 Typical DC Current Gain vs. Collector Current

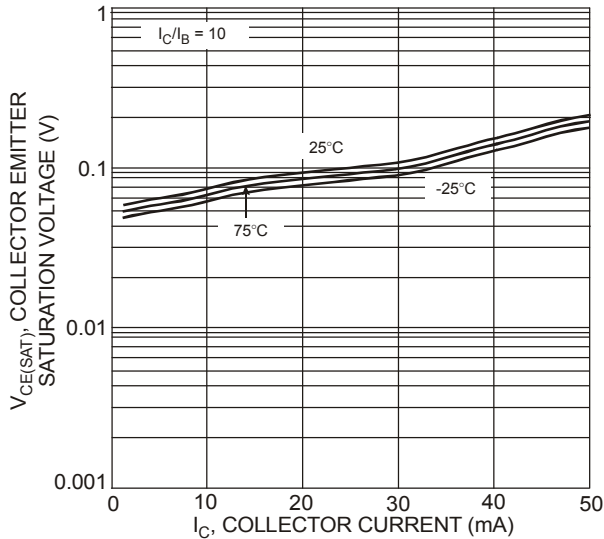


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

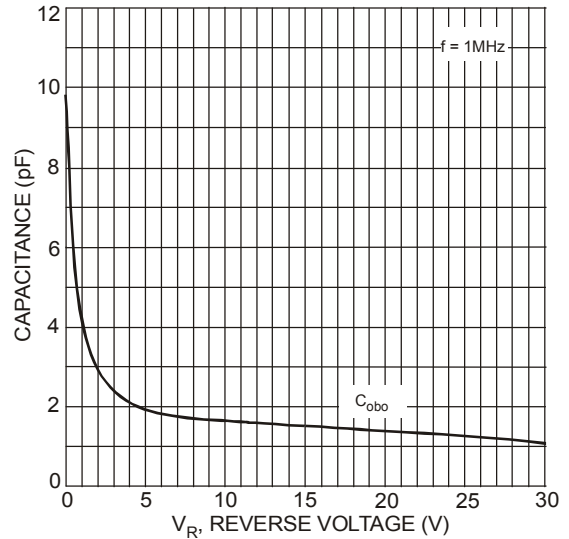


Fig. 4 Typical Capacitance Characteristics

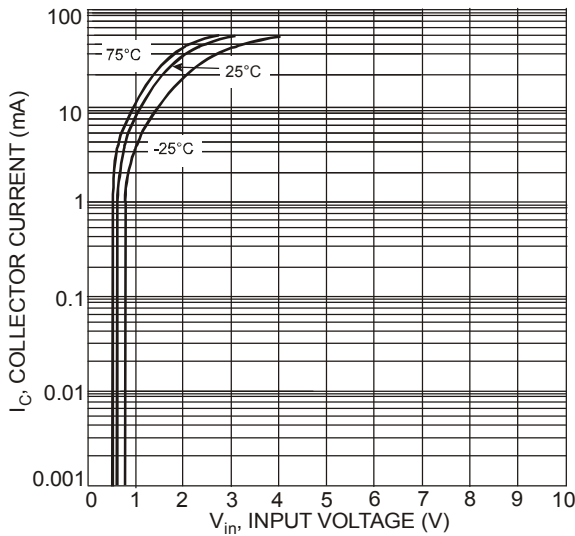


Fig. 5 Collector Current vs. Input Voltage

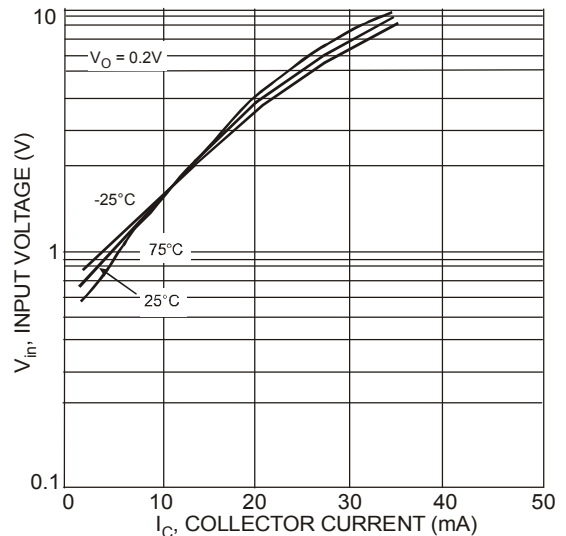
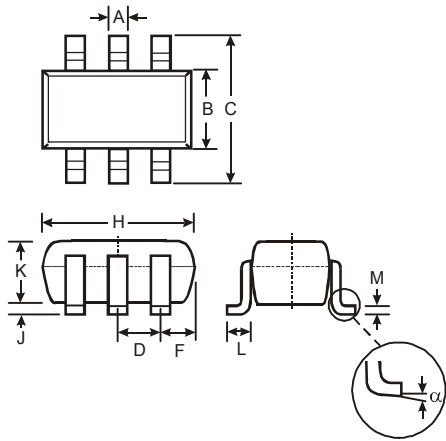


Fig. 6 Input Voltage vs. Collector Current

Package Outline Dimensions

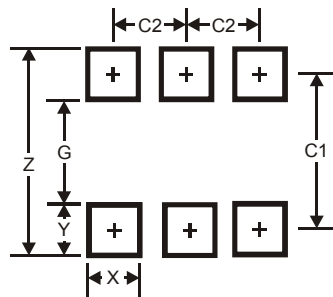
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT363			
Dim	Min	Max	Typ
A	0.10	0.30	0.25
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	0.65 Typ		
F	0.40	0.45	0.425
H	1.80	2.20	2.15
J	0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.22	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

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