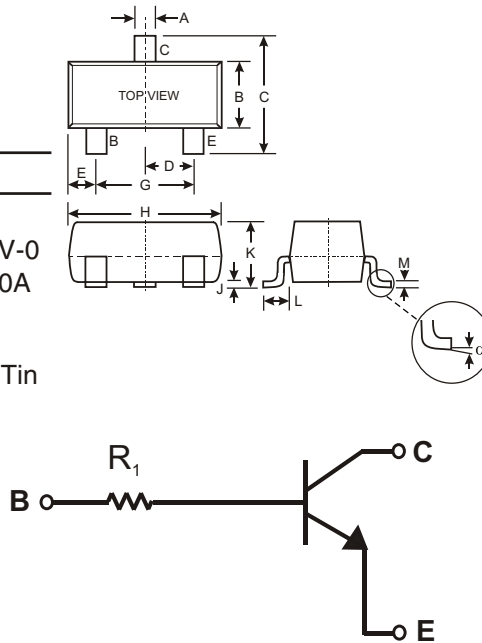


### Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistor, R1 only
- Also Available in Lead Free Version

### Mechanical Data

- Case: SOT-23, Molded Plastic
- Case material - UL Flammability Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020A
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish). Please see Ordering Information, Note 3, on Page 2
- Terminal Connections: See Diagram
- Marking: Date Code and Marking Code (See Diagrams & Page 2)
- Weight: 0.008 grams (approx.)
- Ordering Information (See Page 2)



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
α	0°	8°
All Dimensions in mm		

P/N	R1 (NOM)	MARKING
DDTC113TCA	1KΩ	N01
DDTC123TCA	2.2KΩ	N03
DDTC143TCA	4.7KΩ	N07
DDTC114TCA	10KΩ	N12
DDTC124TCA	22KΩ	N16
DDTC144TCA	47KΩ	N19
DDTC115TCA	100KΩ	N23
DDTC125TCA	200KΩ	N25

SCHMATIC DIAGRAM

### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Collector Current	I <sub>C</sub> (Max)	100	mA
Power Dissipation	P <sub>d</sub>	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R <sub>θJA</sub>	625	°C/W
Operating and Storage and Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.

**Electrical Characteristics** @ T<sub>A</sub> = 25°C unless otherwise specified

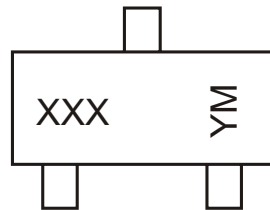
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	50	—	—	V	I <sub>C</sub> = 50μA
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	50	—	—	V	I <sub>C</sub> = 1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	5	—	—	V	I <sub>E</sub> = 50μA
Collector Cutoff Current	I <sub>CBO</sub>	—	—	0.5	μA	V <sub>CB</sub> = 50V
Emitter Cutoff Current	I <sub>EBO</sub>	—	—	0.5	μA	V <sub>EB</sub> = 4V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	—	—	0.3	V	I <sub>C</sub> /I <sub>B</sub> = 10mA/1mA DDTC113TCA I <sub>C</sub> /I <sub>B</sub> = 5mA/0.5mA DDTC123TCA I <sub>C</sub> /I <sub>B</sub> = 2.5mA/.25mA DDTC143TCA I <sub>C</sub> /I <sub>B</sub> = 1mA/.1mA DDTC114TCA I <sub>C</sub> /I <sub>B</sub> = 5mA/0.5mA DDTC124TCA I <sub>C</sub> /I <sub>B</sub> = 2.5mA/.25mA DDTC144TCA I <sub>C</sub> /I <sub>B</sub> = 1mA/0.1mA DDTC115TCA I <sub>C</sub> /I <sub>B</sub> = .5mA/.05mA DDTC125TCA
DC Current Transfer Ratio	h <sub>FE</sub>	100	250	600	—	I <sub>C</sub> = 1mA, V <sub>CE</sub> = 5V
Input Resistor (R <sub>1</sub> ) Tolerance	ΔR <sub>1</sub>	-30	—	+30	%	—
Gain-Bandwidth Product*	f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz

\* Transistor - For Reference Only

**Ordering Information**

Device	Packaging	Shipping
DDTC113TCA-7	SOT-23	3000/Tape & Reel
DDTC123TCA-7	SOT-23	3000/Tape & Reel
DDTC143TCA-7	SOT-23	3000/Tape & Reel
DDTC114TCA-7	SOT-23	3000/Tape & Reel
DDTC124TCA-7	SOT-23	3000/Tape & Reel
DDTC144TCA-7	SOT-23	3000/Tape & Reel
DDTC115TCA-7	SOT-23	3000/Tape & Reel
DDTC125TCA-7	SOT-23	3000/Tape & Reel

- Notes: 2. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.  
 3. For Lead Free version (with Lead Free terminal finish) part number, please add "-F" suffix to part number above.  
 Example: DDTC125TCA-7-F.

**Marking Information**


XXX = Product Type Marking Code  
 See Sheet 1 Diagrams  
 YM = Date Code Marking  
 Y = Year ex: N = 2002  
 M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009
Code	N	P	R	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**TYPICAL CURVES - DDTC114TCA**

**NEW PRODUCT**

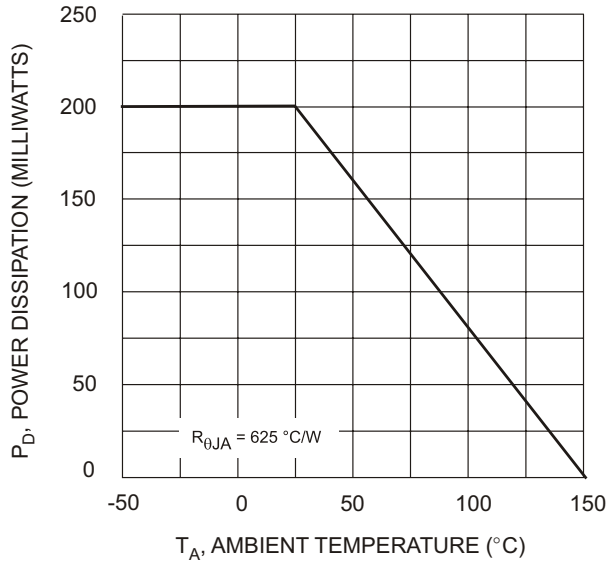


Fig. 1 Derating Curve

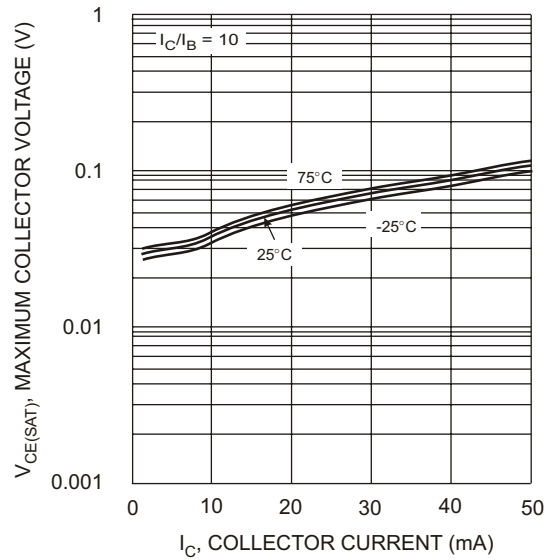


Fig. 2  $V_{CE(SAT)}$  vs.  $I_C$

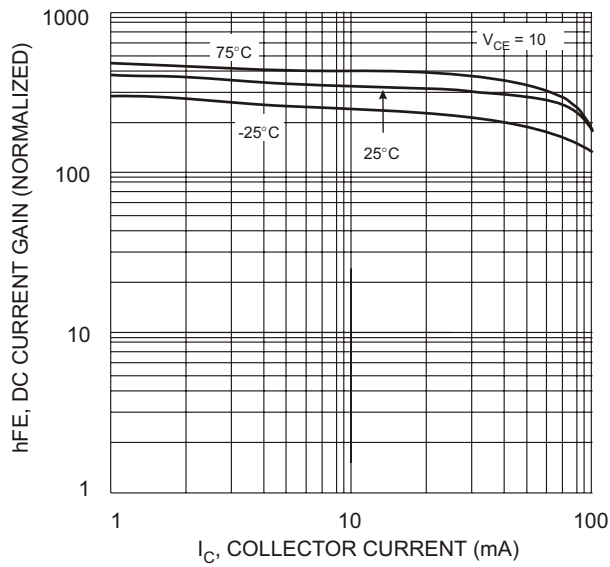


Fig. 3 DC Current Gain

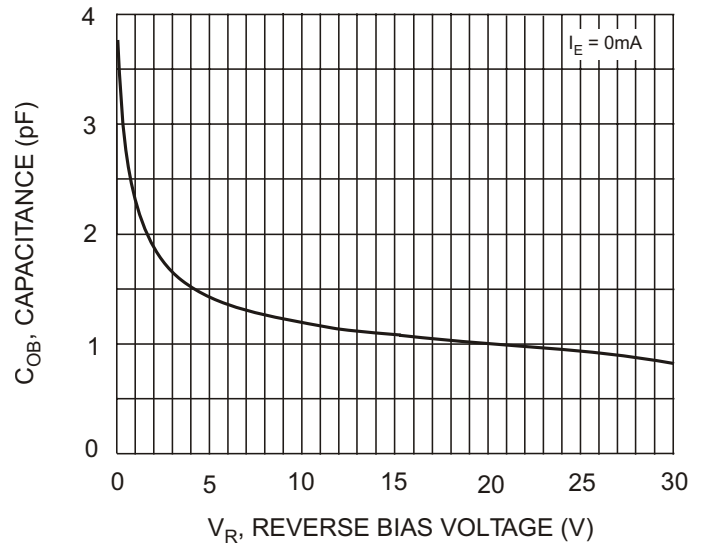


Fig. 4 Output Capacitance

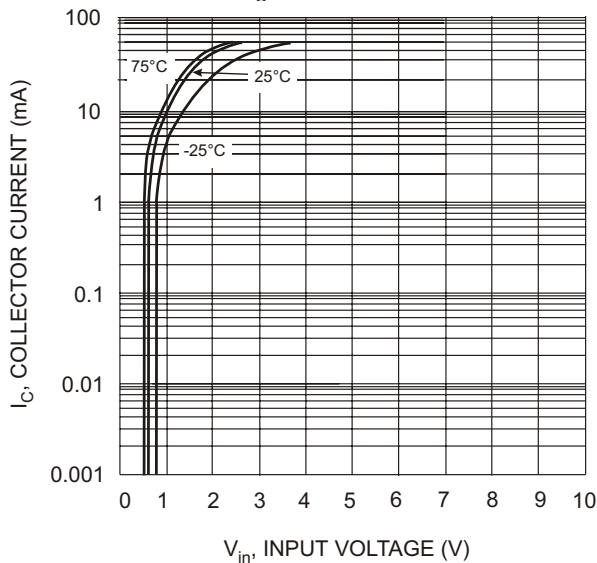


Fig. 5 Collector Current Vs. Input Voltage

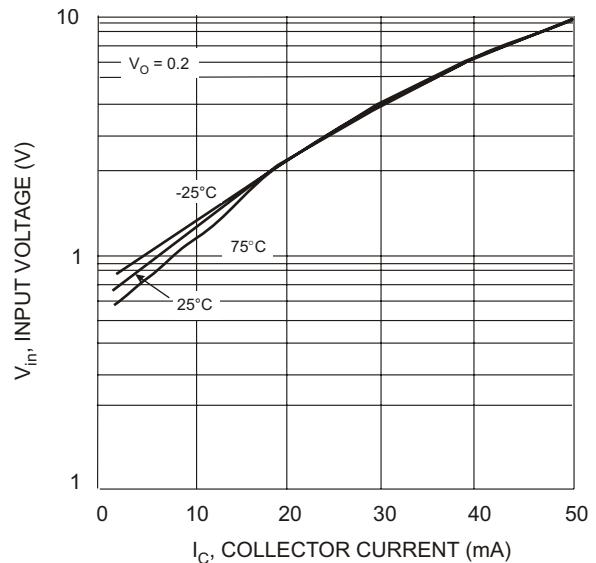


Fig. 6 Input Voltage vs. Collector Current



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