



### Features

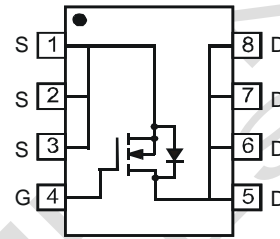
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

### Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (approximate)



Top View



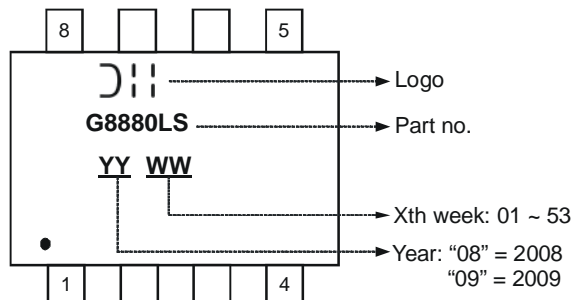
Top View  
Internal Schematic

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMG8880LSS-13	SO-8	2500 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com>.

### Marking Information



### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5)	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	11.6	A
		T <sub>A</sub> = 70°C		8.5	
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	80	A

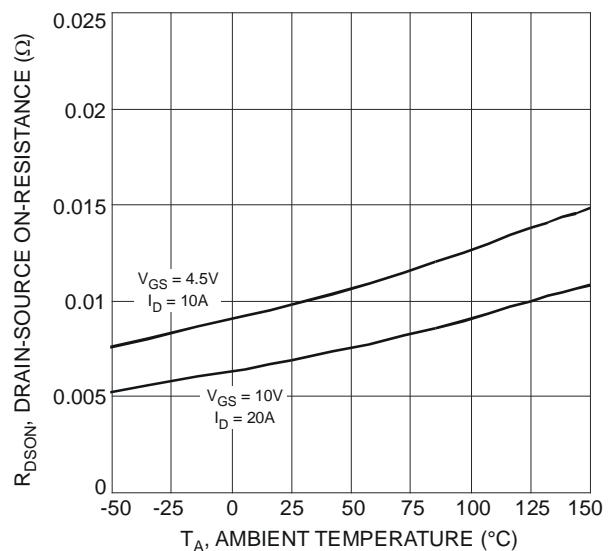
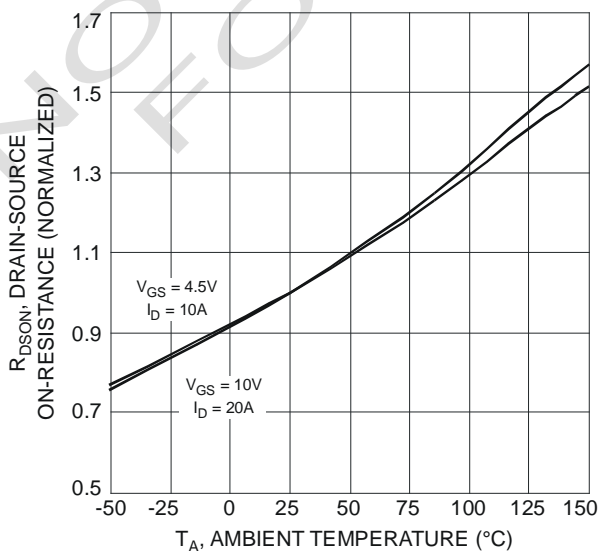
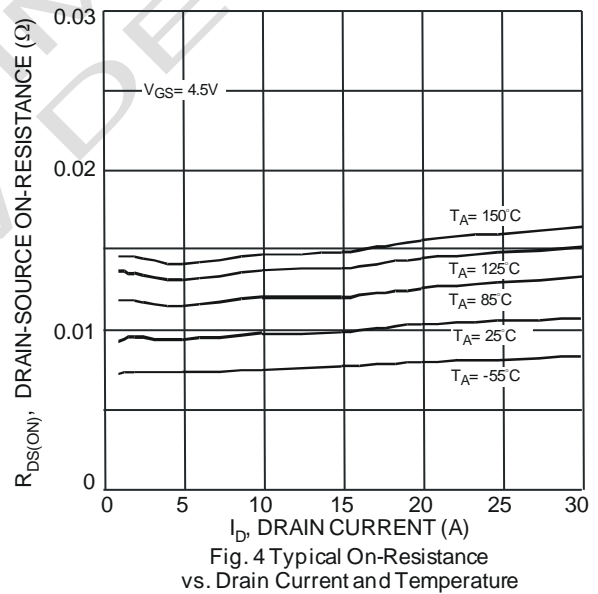
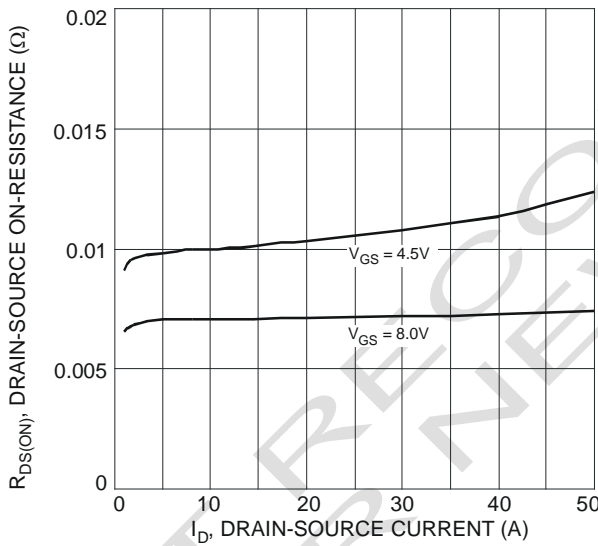
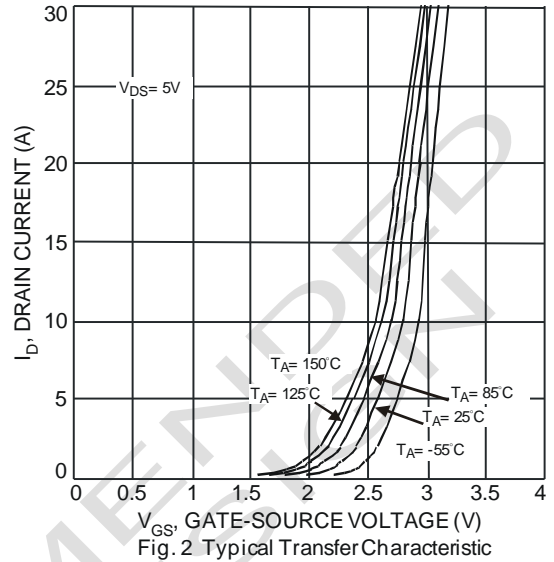
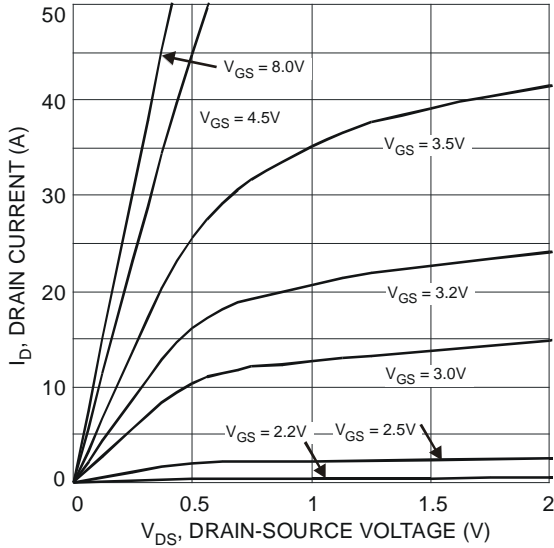
### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	1.43	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = 25°C (Note 5)	R <sub>JA</sub>	87	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	1.0	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	1.5	2.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	-	7.0	10	m•	V <sub>GS</sub> = 10V, I <sub>D</sub> = 11.6A
			9.6	14		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10.7A
Diode Forward Voltage	V <sub>SD</sub>	-	0.7	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.1A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	-	1289	-	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	-	187	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	162	-	pF	
Gate Resistance	R <sub>g</sub>	-	0.97	-	•	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge at 10V	Q <sub>g</sub>	-	27.6	-	nC	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 11.6A, I <sub>G</sub> = 1.0mA
Total Gate Charge at 5V	Q <sub>g</sub>	-	14.4	-	nC	V <sub>GS</sub> = 5V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 11.6A, I <sub>G</sub> = 1.0mA
Gate-Source Charge	Q <sub>gs</sub>	-	3.6	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>	-	4.9	-	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	-	7.04	-	ns	V <sub>DD</sub> = 15V, V <sub>GS</sub> = 10V, R <sub>GS</sub> = 11•, I <sub>D</sub> = 11.6A
Turn-On Rise Time	t <sub>r</sub>	-	17.52	-	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	36.13	-	ns	
Turn-Off Fall Time	t <sub>f</sub>	-	19.67	-	ns	

- Notes:
5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
  6. Repetitive rating, pulse width limited by junction temperature.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to production testing.



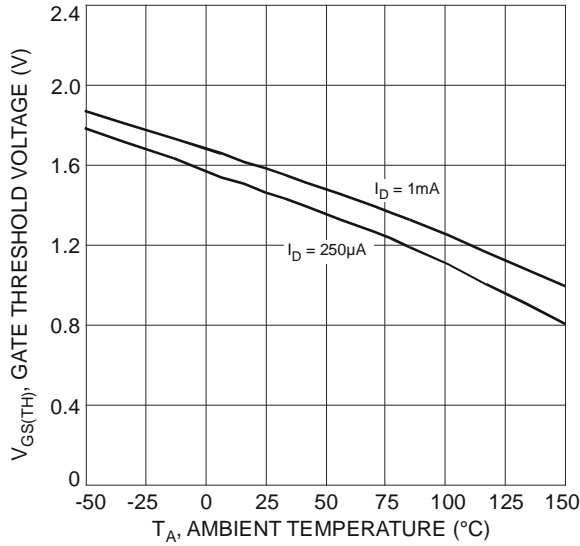


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

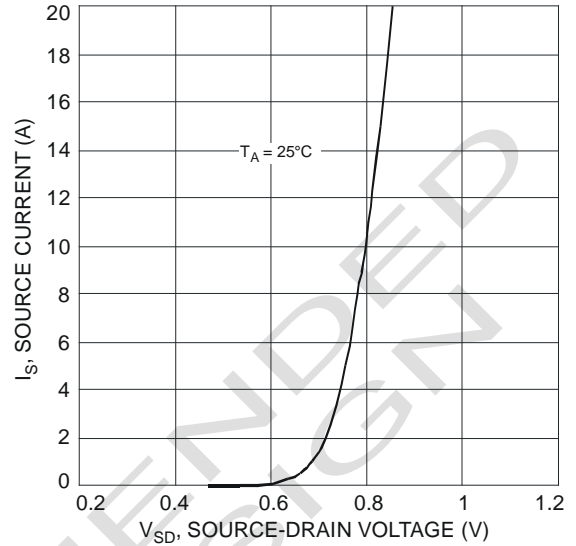


Fig. 8 Diode Forward Voltage vs. Current

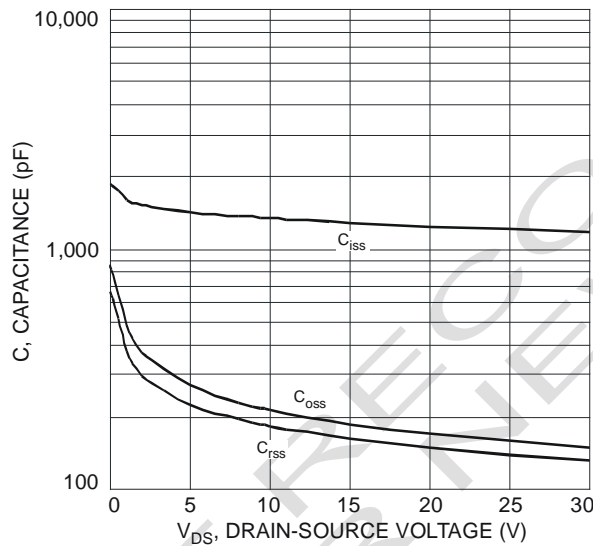


Fig. 9 Typical Total Capacitance

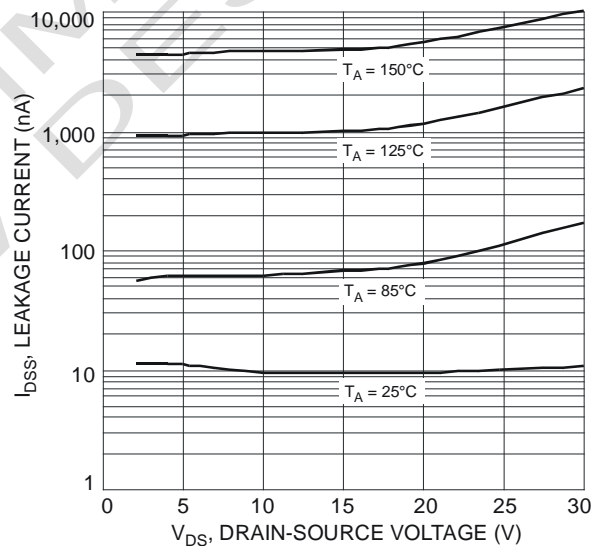


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

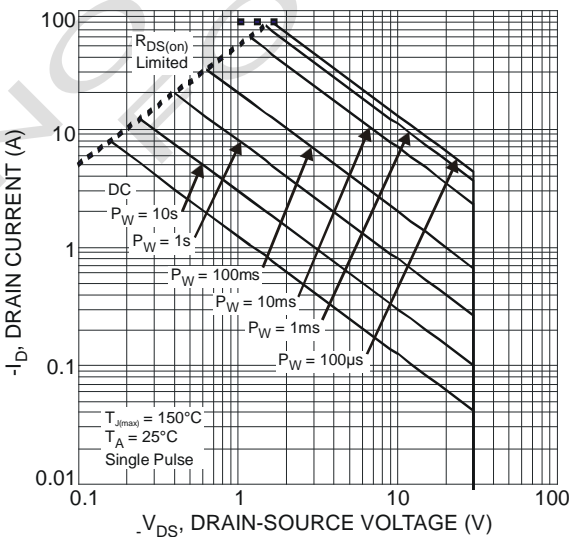


Fig. 11 Safe Operation Area

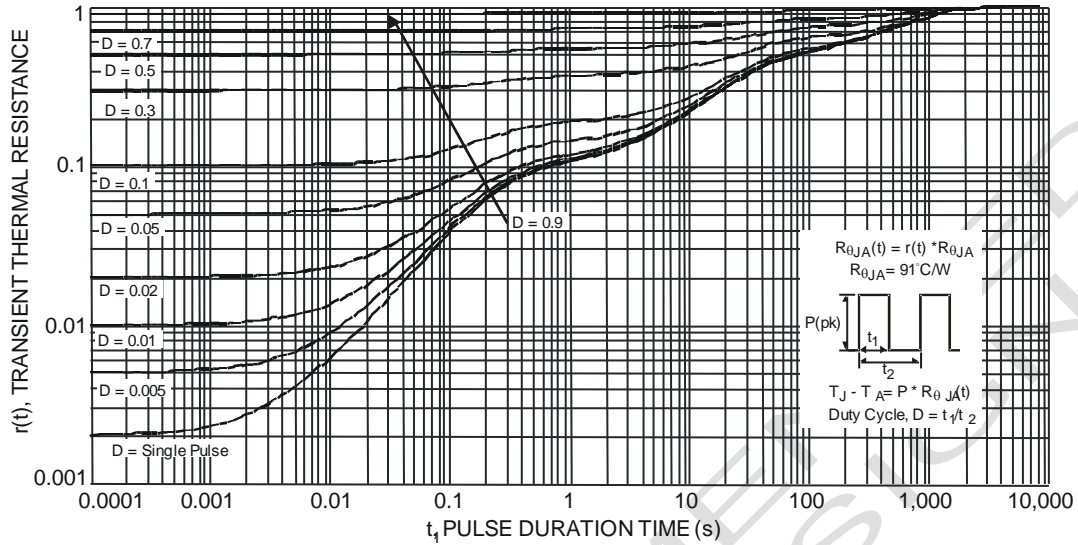
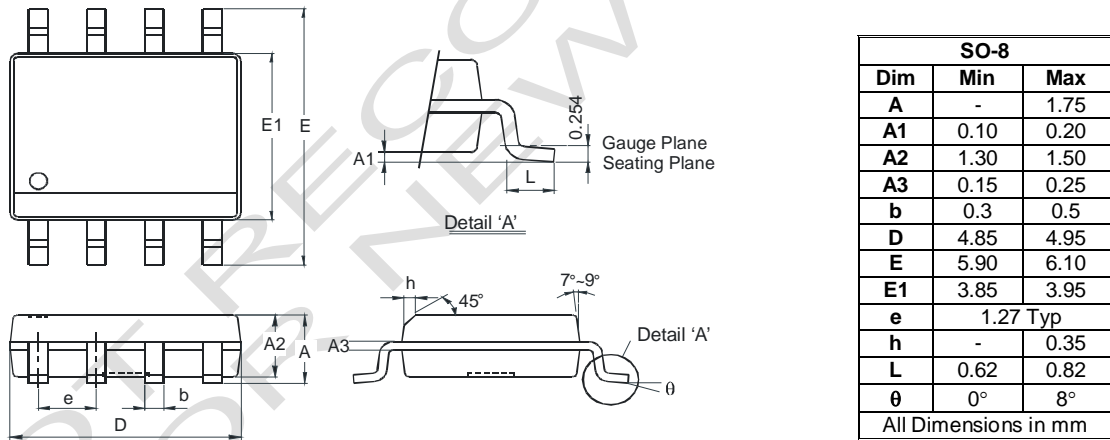
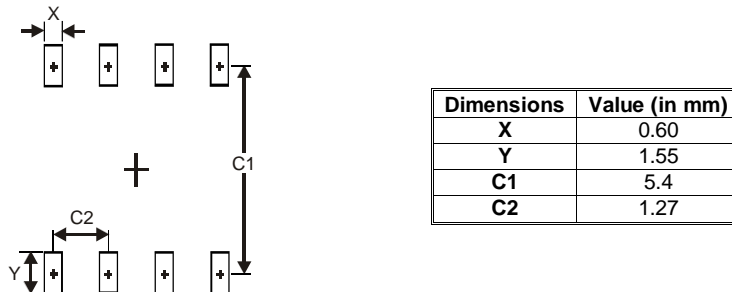


Fig. 12 Transient Thermal Response

Package Outline Dimensions



Suggested Pad Layout



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