HZM6.2ZWA

Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-499A (Z)

Rev.1
Nov. 2001

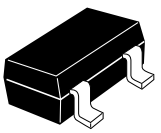
Features

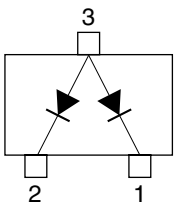
- HZM6.2ZWA has two devices, and can absorb external + and -surge.
- Low capacitance (C = 8.5 pF max) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.2ZWA	62Z	MPAK

Pin Arrangement





(Top View)

1. Cathode
2. Cathode
3. Anode

Hitachi semiconductor

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: Two device total, See Fig.2.

Electrical Characteristics*[1]

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V_Z	5.90	—	6.50	V	I_Z = 5 mA, 40 ms pulse
Reverse current	I_R	—	—	3	μA	V_R = 5.5 V
Capacitance	C	—	8.0	8.5	pF	V_R = 0 V, f = 1 MHz
Dynamic resistance	r_d	—	—	60	Ω	I_Z = 5 mA
ESD-Capability *[2]	—	13	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.
2. Failure criterion ; I_R > 3 μA at V_R = 5.5 V.

Main Characteristic

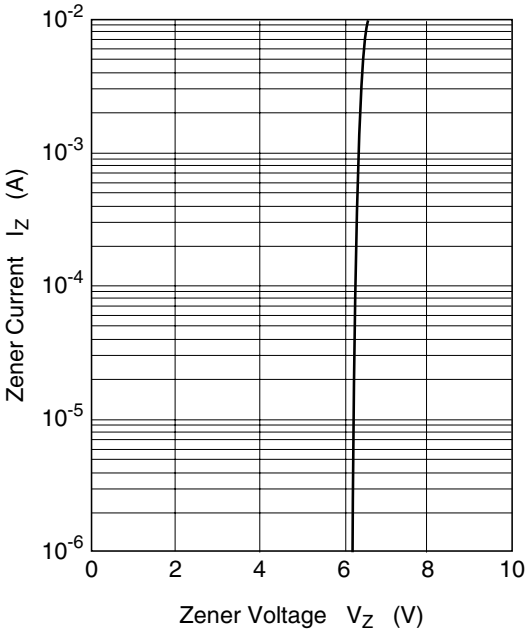


Fig.1 Zener current vs. Zener voltage

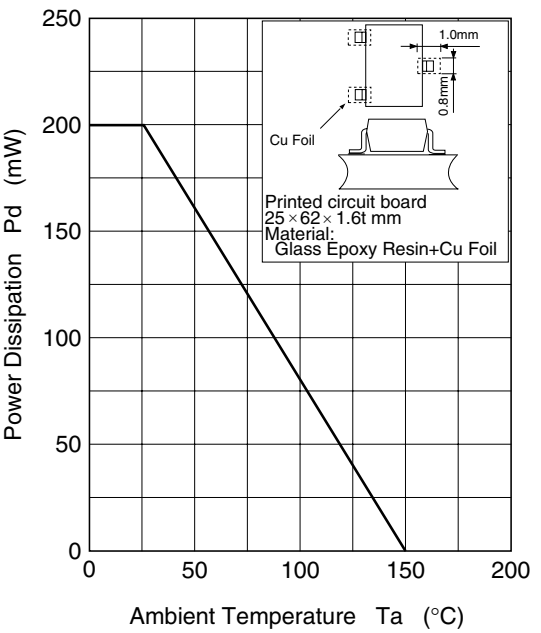


Fig.2 Power Dissipation vs. Ambient Temperature

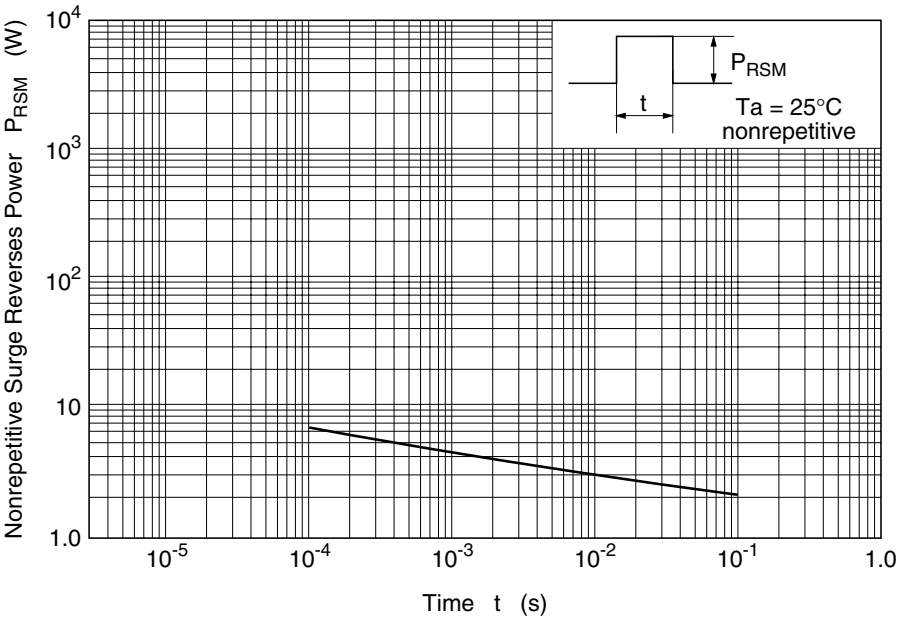


Fig.3 Surge Reverse Power Ratings

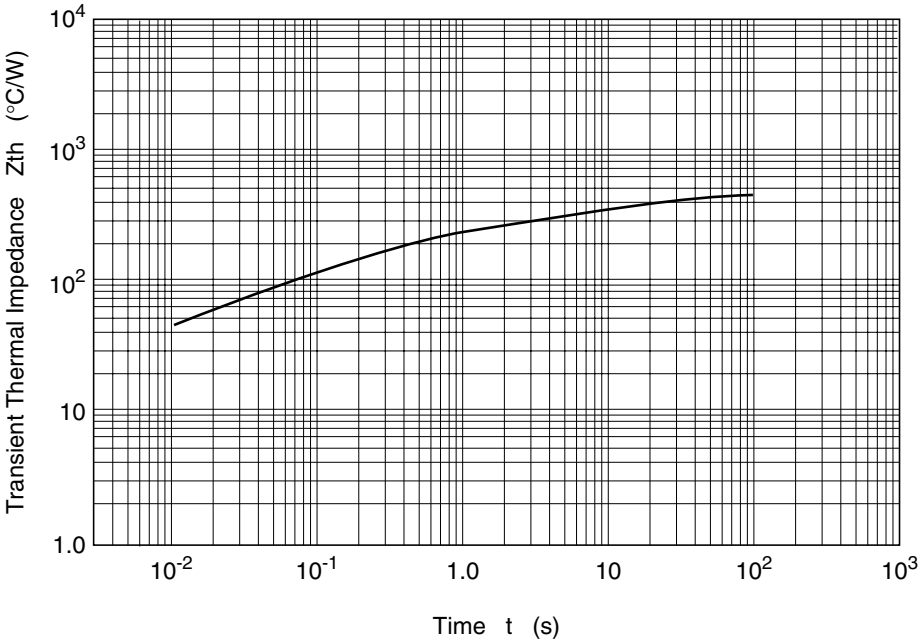
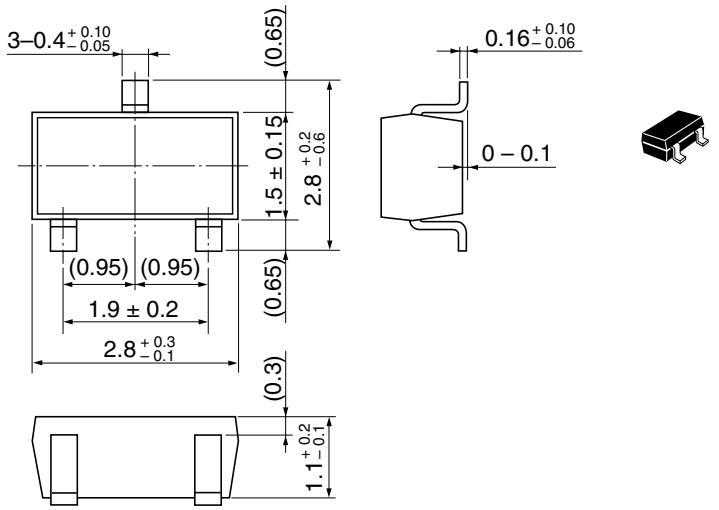


Fig.4 Transient Thermal Impedance

Package Dimensions

As of July, 2001

Unit: mm



Hitachi Code	MPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.011 g

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