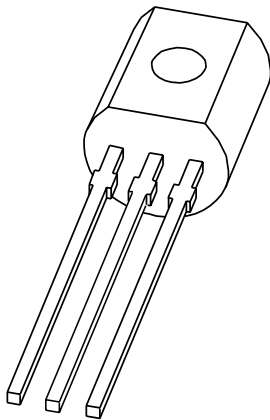


DATA SHEET



BFV421 PNP high-voltage transistor

Product specification
Supersedes data of 1996 Dec 09

1999 Apr 23

PNP high-voltage transistor

BFV421

FEATURES

- High voltage
- High transition frequency
- Low output capacitance.

APPLICATIONS

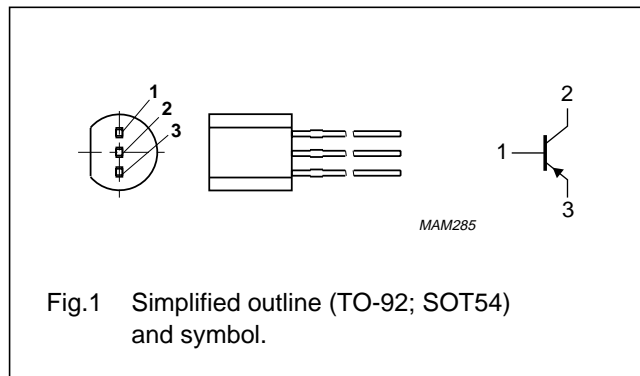
- Primarily intended for video applications (monitors).

DESCRIPTION

PNP transistor in a plastic TO-92; SOT54 package.
NPN complement: BFV420.

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–140	V
V_{CEO}	collector-emitter voltage	open base	–	–100	V
V_{EBO}	emitter-base voltage	open collector	–	–5	V
I_C	collector current (DC)		–	–100	mA
I_{CM}	peak collector current		–	–100	mA
I_{BM}	peak base current		–	–100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ }^\circ\text{C}$	–	830	mW
T_{stg}	storage temperature		–65	+150	$^\circ\text{C}$
T_j	junction temperature		–	150	$^\circ\text{C}$
T_{amb}	operating ambient temperature		–65	+150	$^\circ\text{C}$

PNP high-voltage transistor

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	in free air; note 1	150	K/W

Note

1. Transistor mounted on a printed-circuit board; maximum lead length 4 mm; mounting pad for collector lead minimum 10×10 mm.

CHARACTERISTICS

$T_{amb} = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -100\text{ V}$	–	–100	nA
		$I_E = 0; V_{CB} = -100\text{ V}; T_{amb} = 150\text{ °C}$	–	–10	μA
I_{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -4\text{ V}$	–	–100	nA
h_{FE}	DC current gain	$I_C = -10\text{ mA}; V_{CE} = -10\text{ V}$	150	–	
		$I_C = -50\text{ mA}; V_{CE} = -10\text{ V}$	20	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -30\text{ mA}; I_B = -5\text{ mA}$	–	–200	mV
C_{re}	feedback capacitance	$I_C = i_c = 0; V_{CE} = -25\text{ V}; f = 1\text{ MHz}$	–	2.3	pF
f_T	transition frequency	$I_C = -20\text{ mA}; V_{CE} = -20\text{ V}; f = 100\text{ MHz}$	150	–	MHz

PNP high-voltage transistor

BFV421

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	c	D	d	E	e	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT54		TO-92	SC-43		97-02-28

PNP high-voltage transistor

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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PNP high-voltage transistor

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NOTES

PNP high-voltage transistor

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NOTES

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Printed in The Netherlands

115002/00/02/pp8

Date of release: 1999 Apr 23

Document order number: 9397 750 05404

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