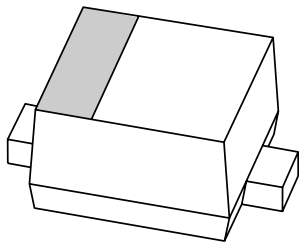


# DATA SHEET



**1PS79SB63**

Schottky barrier diode

Product specification

2002 Apr 08

## Schottky barrier diode

1PS79SB63

## FEATURES

- Very low capacitance
- Low forward voltage
- Ultra small plastic SMD package.

## APPLICATIONS

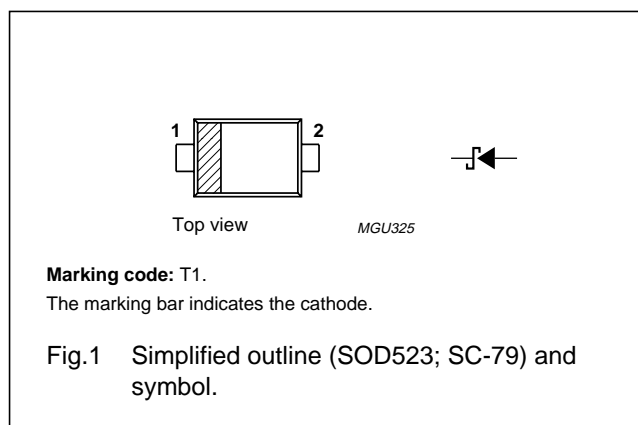
- High frequency detection
- Ultra high-speed switching
- Zero bias detection.

## DESCRIPTION

Planar Schottky barrier diode encapsulated in a SOD523 (SC-79) ultra small plastic SMD package. ESD sensitive device, observe handling precautions.

## PINNING

PIN	DESCRIPTION
1	cathode
2	anode



## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_R$	continuous reverse voltage		–	5	V
$I_F$	continuous forward current		–	20	mA
$I_{FRM}$	repetitive peak forward current	$t_p \leq 1$ ms; $\delta = 0.25$	–	400	mA
$I_{FSM}$	non-repetitive peak forward current	$t = 8.3$ ms half sine wave; JEDEC method	–	550	mA
$T_{stg}$	storage temperature		–65	+150	°C
$T_j$	junction temperature		–	125	°C
$T_{amb}$	operating ambient temperature		–65	+125	°C

## CAUTION

This product is supplied in anti-static packing to prevent damage caused by electrostatic discharge during transport and handling. For further information, refer to Philips specs.: SNW-EQ-608, SNW-FQ-302A and SNW-FQ-302B.

## Schottky barrier diode

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**ELECTRICAL CHARACTERISTICS** $T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$V_F$	forward voltage	see Fig.2 $I_F = 0.1\text{ mA}$	160	200	mV
		$I_F = 1\text{ mA}$	240	300	mV
$I_R$	continuous reverse current	see Fig.3 $V_R = 1\text{ V}$	0.4	1	$\mu\text{A}$
		$V_R = 5\text{ V}$ ; note 1	–	50	$\mu\text{A}$
$C_d$	diode capacitance	$V_R = 0\text{ V}$ ; $f = 1\text{ MHz}$ ; see Fig.4	0.35	0.5	pF
$L_s$	series inductance		0.6	–	nH

**Note**

1. Pulse test: pulse width = 300  $\mu\text{s}$ ;  $\delta = 0.02$ .

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	450	K/W

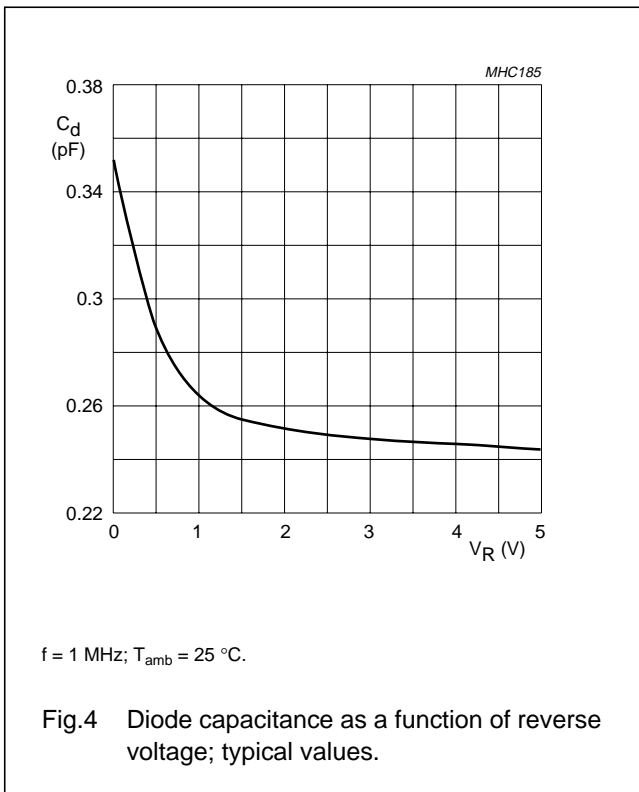
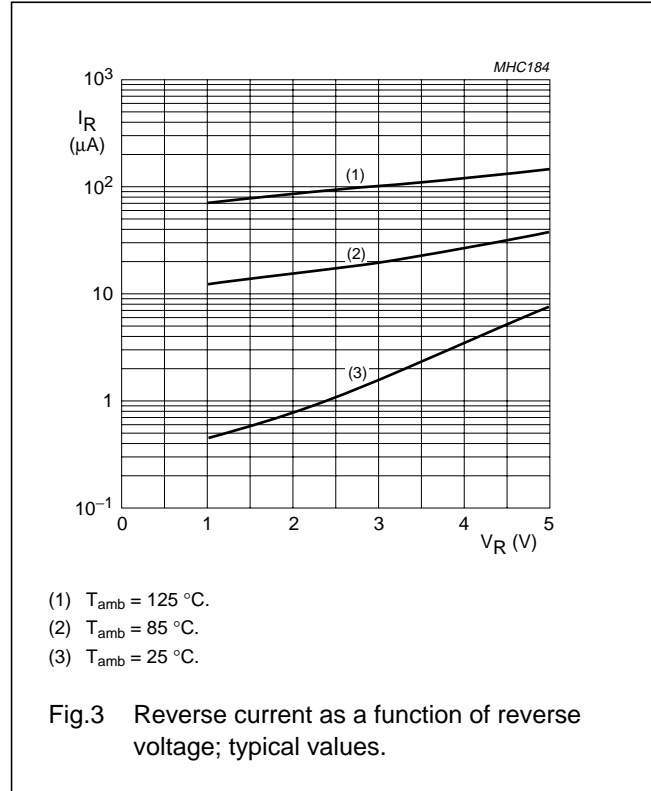
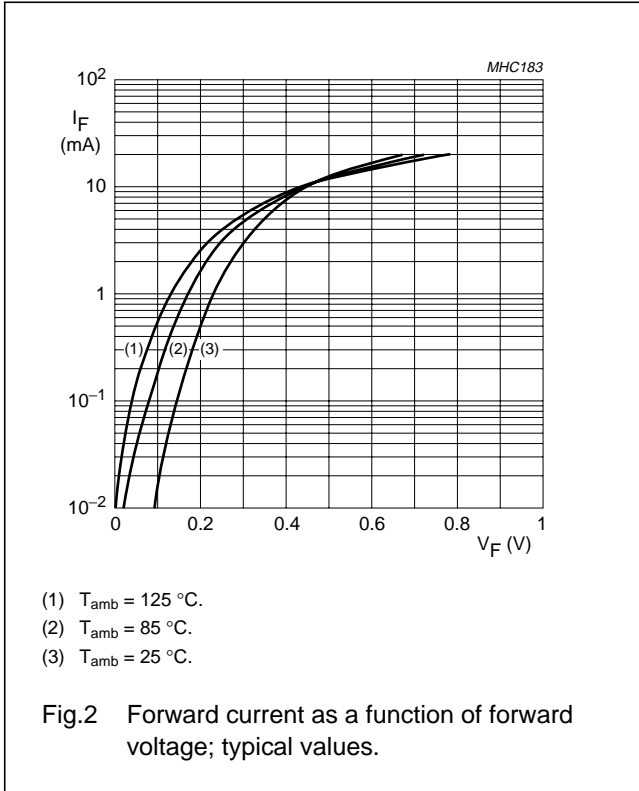
**Note**

1. Refer to SOD523 (SC-79) standard mounting conditions.

Schottky barrier diode

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GRAPHICAL DATA



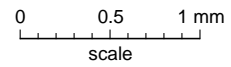
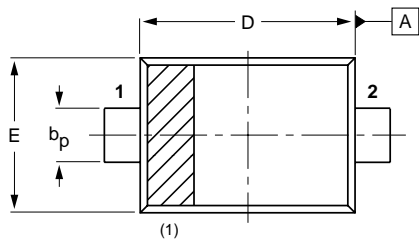
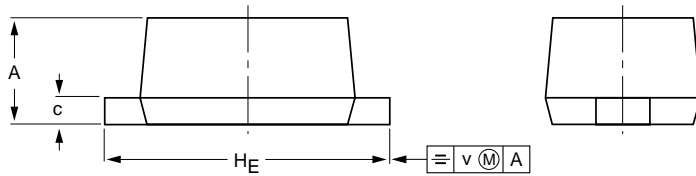
Schottky barrier diode

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PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD523



DIMENSIONS (mm are the original dimensions)

UNIT	A	bp	c	D	E	HE	v
mm	0.7 0.5	0.35 0.25	0.2 0.1	1.3 1.1	0.9 0.7	1.7 1.5	0.15

Note

1. The marking bar indicates the cathode.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOD523			SC-79			98-11-25

## Schottky barrier diode

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## DATA SHEET STATUS

DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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Schottky barrier diode

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**NOTES**

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