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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

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HSB278S

Silicon Schottky Barrier Diode for High Speed Switching

RENESAS

ADE-208-1383 (Z)

Rev.0
Mar. 2001

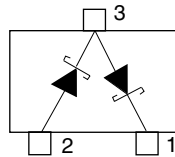
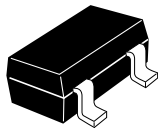
Features

- Low forward voltage, Low capacitance.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSB278S	S2	CMPAK

Pin Arrangement



- 1 Cathode 2
- 2 Anode 1
- 3 Cathode 1
Anode 2

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Reverse voltage	V_R	30	V
Non-Repetitive peak forward surge current	$I_{FSM}^{*1 *2}$	200	mA
Peak forward current	I_{FM}^{*2}	150	mA
Average rectified current	I_O^{*2}	30	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. 10 msec sine wave 1 pulse
2. Per one device.

Electrical Characteristics

(Ta = 25°C) *¹

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.30	V	$I_F = 1 \text{ mA}$
	V_{F2}	—	—	0.95		$I_F = 30 \text{ mA}$
Reverse current	I_R	—	—	700	nA	$V_R = 10 \text{ V}$
Capacitance	C	—	—	1.50	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$
ESD-Capability * ¹	—	100	—	—	V	C = 200 pF, $R_L = 0 \Omega$, Both forward and reverse direction 1 pulse.

Notes: 1. Per one device.
2. Failure criterion ; $I_R > 1.4 \mu\text{A}$ at $V_R = 10 \text{ V}$

Main Characteristic

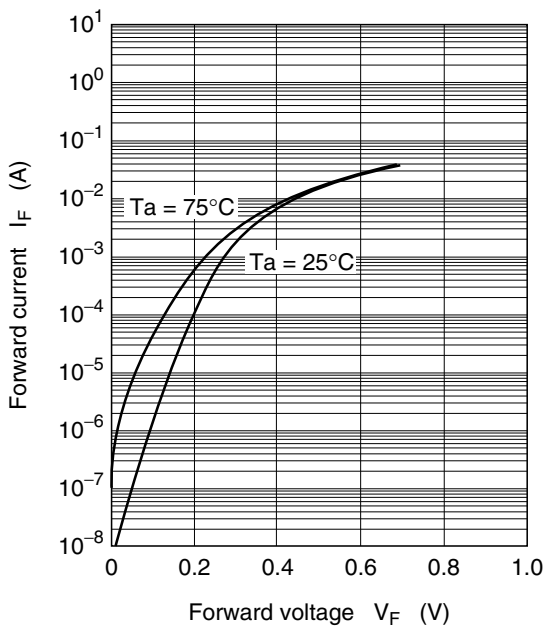


Fig.1 Forward current Vs. Forward voltage

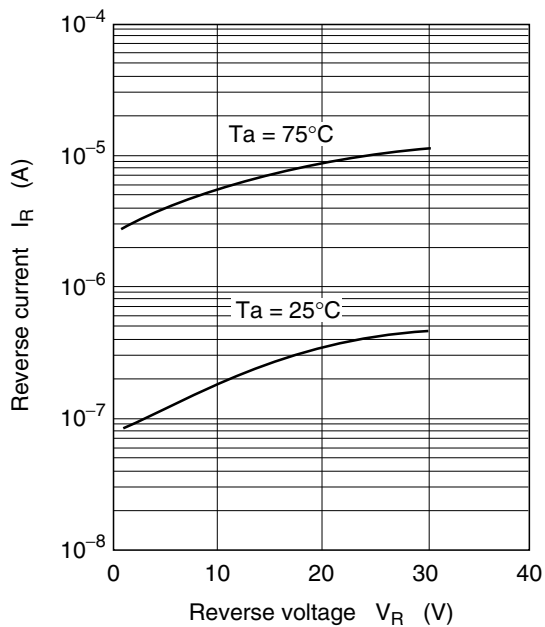


Fig.2 Reverse current Vs. Reverse voltage

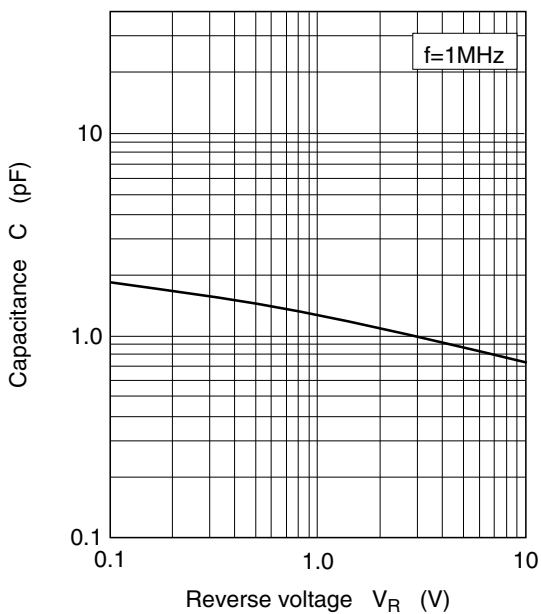
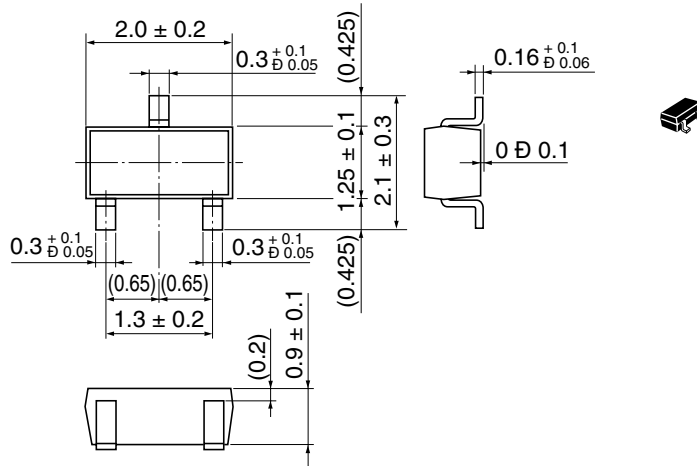


Fig.3 Capacitance Vs. Reverse voltage

Package Dimensions

As of January, 2001

Unit: mm



Hitachi Code	CMPAK
JEDEC	N
EIAJ	Conforms
Mass (reference value)	0.006 g

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