HVD326C

Variable Capacitance Diode for UHF/VHF tuner

REJ03G0380-0100Z
Rev.1.00
Jun 28, 2004

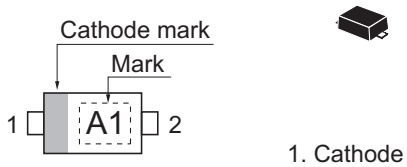
Features

- Low voltage type (tuning voltage 1 to 10 V), it is suitable for ET without DC/DC converter.
- Low series resistance. (rs = 0.6 Ω max) and good C-V linearity.
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVD326C	A1	SFP

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	10	nA	V_R = 10 V
	I_{R2}	—	—	100		V_R = 10 V, Ta = 60°C
Capacitance	C_1	13.0	—	16.0	pF	V_R = 1 V, f = 1 MHz
	C_{10}	2.0	—	2.3		V_R = 10 V, f = 1 MHz
Capacitance ratio	n	6.0	—	—	—	C_1 / C_{10}
Series resistance	r_S	—	—	0.6	Ω	V_R = 5 V, f = 470 MHz
Matching error	ΔC/C *3	—	—	2.0	%	V_R = 1 to 10 V, f = 1 MHz

Notes: 1. Please do not use the soldering iron due to avoid high stress to the SFP package.

2. The material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

3. C.C system (Continuous Connected taping system) enable to make any 10 pcs of ΔC/C continuous in a reel, expect extention to another group.
   Calculate Matching Error,

$$\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100\ (\%)$$

Main Characteristic

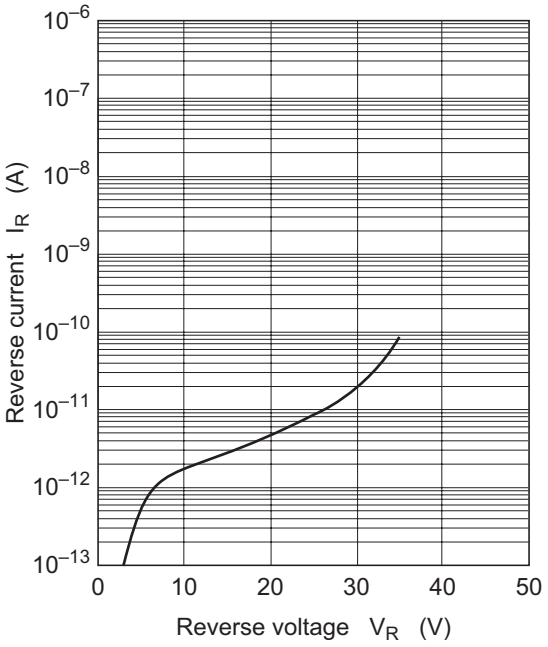


Fig.1 Reverse current vs. Reverse voltage

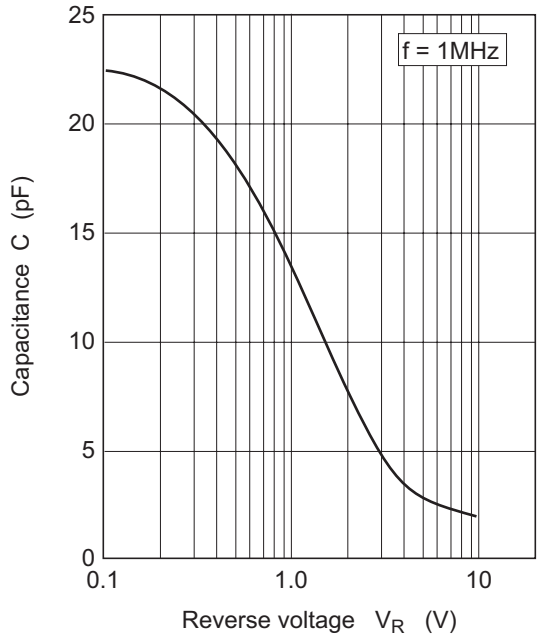


Fig.2 Capacitance vs. Reverse voltage

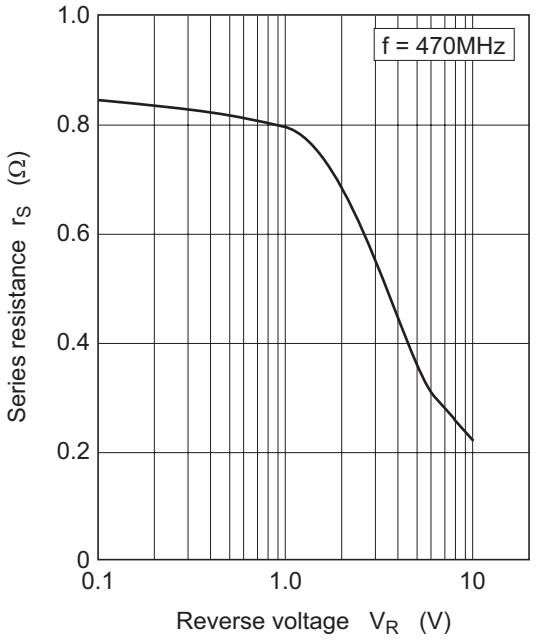


Fig.3 Series resistance vs. Reverse voltage

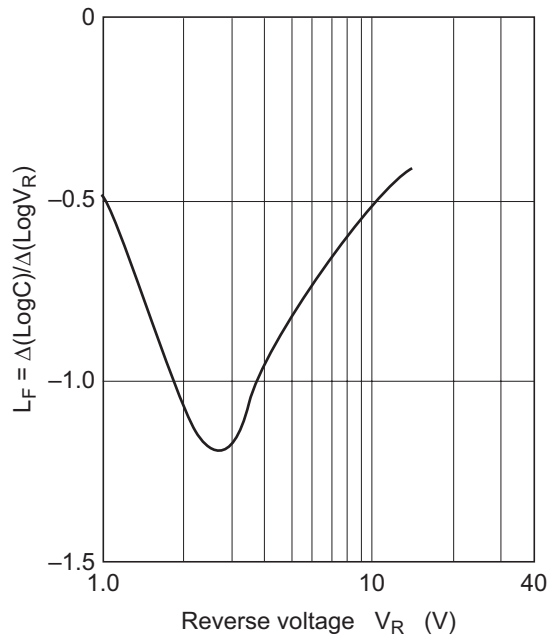
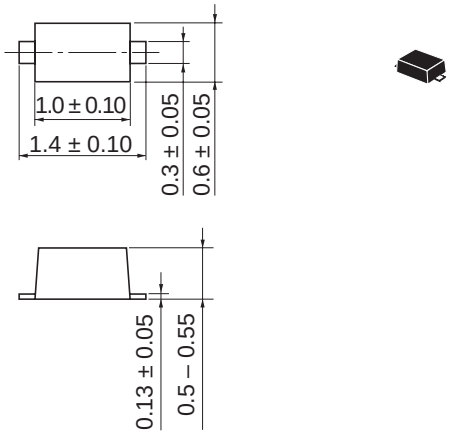


Fig.4 Linearity factor vs. Reverse voltage

Package Dimensions

As of January, 2003

Unit: mm



Package Code	SFP
JEDEC	—
JEITA	—
Mass (reference value)	0.0010 g

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