



MILITARY DATA SHEET

MN54F280-X REV 1A0

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9-Bit Parity Generator /Checker

General Description

The F280 is a high-speed parity generator/checker that accepts nine bits of input data and detects whether an even or odd number of these inputs is HIGH. If an even number of inputs is HIGH, the Sum Even output is HIGH. If an odd number is HIGH, the Sum Even output is LOW. The sum Odd output is the complement of the Sum Even output.

Industry Part Number

54F280

NS Part Numbers

54F280DMQB
 54F280FMQB
 54F280LMQB

Prime Die

M280

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Features

- Guaranteed 4000V minimum ESD protection

(Absolute Maximum Ratings)

(Note 1)

Storage Temperature	-65 C to +150 C
Ambient Temperature under Bias	-55 C to +125 C
Junction Temperature under Bias	-55 C to +175 C
Vcc Pin Potential to Ground Pin	-0.5V to +7.0V
Input Voltage (Note 2)	-0.5V to +7.0V
Input Current (Note 2)	-30mA to +5.0mA
Voltage Applied to Output in HIGH State (with Vcc=0V)	
Standard Output	-0.5V to Vcc
TRI-STATE Output	-0.5V to +5.5V
Current Applied to Output in LOW State (Max)	twice the rated Iol(mA)
ESD Last Passing Voltage (Min)	4000V

Note 1: Absolute maximum ratings are those values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature	
Commercial	0 C to +70 C
Military	-55 C to +125 C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)
DC: VCC 4.5V to 5.5V, Temp range: -55C to 125C

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
I _{IH}	Input High Current	VCC=5.5V, V _M =2.7V, V _{INH} =5.5V	1, 3	INPUTS		20	uA	1, 2, 3
I _{BVI}	Input High Current	VCC=5.5V, V _M =7.0V, V _{INH} =5.5V	1, 3	INPUTS		100	uA	1, 2, 3
I _{IL}	Input LOW Current	VCC=5.5V, V _M =0.5V, V _{INH} =5.5V	1, 3	INPUTS		-0.6	mA	1, 2, 3
V _{OL}	Output LOW Voltage	VCC=4.5V, V _{IL} =0.8V, I _{OL} =20mA, V _{INH} =5.5V	1, 3	OUTPUTS		0.5	V	1, 2, 3
V _{OH}	Output HIGH Voltage	VCC=4.5V, V _{IH} =2.0V, I _{OH} =-1.0mA, V _{IL} =0.8V	1, 3	OUTPUTS	2.5		V	1, 2, 3
I _{OS}	Short Circuit Current	VCC=5.5V, V _{INH} =5.5V, V _{INL} =0.0V, V _M =0.0V	1, 3	OUTPUTS	-60	-150	mA	1, 2, 3
V _{CD}	Input Clamp Diode Voltage	VCC=4.5V, I _M =-18mA, V _{INH} =5.5V	1, 3	INPUTS		-1.2	V	1, 2, 3
I _{CC}	Supply Current	VCC=5.5V, V _{INL} =0.0V	1, 3	VCC		38	mA	1, 2, 3
I _{CEX}	Output HIGH Leakage Current	VCC=5.5V, V _{INL} =0.0V, V _{INH} =5.5V, V _M =5.5V	1, 3	OUTPUTS		250	uA	1, 2, 3

AC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)
AC: C_L=50pf, R_L=500 OHMS, T_R=2.5ns, T_F=2.5ns SEE AC FIGS

tp _{LH} (1)	Propagation Delay	VCC=5.0V @25C, VCC=4.5V & 5.5V @-55/125C	2, 4	In to Eo	5.0	15.0	ns	9
			2, 4	In to Eo	5.0	20.0	ns	10, 11
tp _{HL} (1)	Propagation Delay	VCC=5.0V @25C, VCC=4.5V & 5.5V @-55/125C	2, 4	In to Eo	6.5	16.0	ns	9
			2, 4	In to Eo	6.5	21.0	ns	10, 11
tp _{LH} (2)	Propagation Delay	VCC=5.0V @25C, VCC=4.5V & 5.5V @-55/125C	2, 4	In to Ee	6.5	15.0	ns	9
			2, 4	In to Ee	6.5	20.0	ns	10, 11
tp _{HL} (2)	Propagation Delay	VCC=5.0V @25C, VCC=4.5V & 5.5V @-55/125C	2, 4	In to Ee	6.5	16.0	ns	9
			2, 4	In to Ee	6.5	21.0	ns	10, 11

Note 1: Screen tested 100% on each device at +25C, +125C & -55C temperature, subgroups A1, 2, 3, 7 & 8.

Note 2: Screen tested 100% on each device at +25C temperature only, subgroup A9.

(Continued)

- Note 3: Sample tested (Method 5005, Table 1) on each MFG. lot at +25C, +125C & -55C temperature, subgroups A1, 2, 3, 7 & 8.
- Note 4: Sample tested (Method 5005, Table 1) on each MFG. lot at +25C subgroup A9, and periodically at +125C & -55C temperature, subgroups 10 & 11.

National Semiconductor was acquired by Texas Instruments.

http://www.ti.com/corp/docs/investor_relations/pr_09_23_2011_national_semiconductor.html

This file is the datasheet for the following electronic components:

54F280LMQB - <http://www.ti.com/product/54f280lmbq?HQS=TI-null-null-dscatalog-df-pf-null-wwe>

JM38510/34901B2A - <http://www.ti.com/product/jm38510/34901b2a?HQS=TI-null-null-dscatalog-df-pf-null-wwe>

54F280J-MLS - <http://www.ti.com/product/54f280j-mls?HQS=TI-null-null-dscatalog-df-pf-null-wwe>

JM38510/34901BCA - <http://www.ti.com/product/jm38510/34901bca?HQS=TI-null-null-dscatalog-df-pf-null-wwe>

JM38510/34901BDA - <http://www.ti.com/product/jm38510/34901bda?HQS=TI-null-null-dscatalog-df-pf-null-wwe>

54F280FMQB - <http://www.ti.com/product/54f280fmqb?HQS=TI-null-null-dscatalog-df-pf-null-wwe>

54F280DMQB - <http://www.ti.com/product/54f280dmqb?HQS=TI-null-null-dscatalog-df-pf-null-wwe>



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