HD14093B

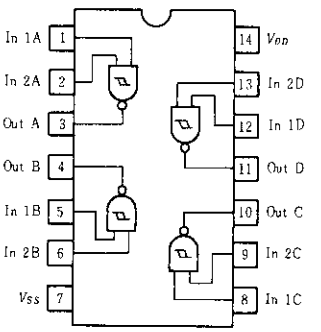
Quadruple 2-input NAND Schmitt Trigger

The HD14093B Schmitt trigger finds primary use where low power dissipation and/or high noise immunity is desired. The HD14093B may be used in place of the HD14011B quad 2-input NAND gate for enhanced noise immunity or to "square up" slowly changing waveforms.

■ FEATURES

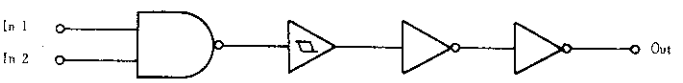
- Quiescent Current = 0.5nA/pkg typ. @5V
- Supply Voltage Range = 3 to 18V
- Capable of Driving One Low-power Schottky TTL Load Over the Rated Temperature Range
- Double Diode Protection on All Inputs
- Pin-for-Pin Compatible with CD4093 and MC14093B
- Can be Use to Replace HD14011B

■ PIN ARRANGEMENT

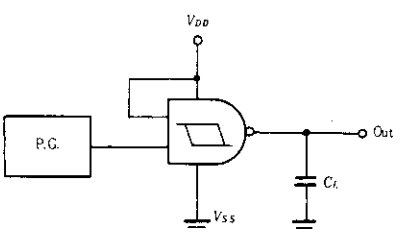


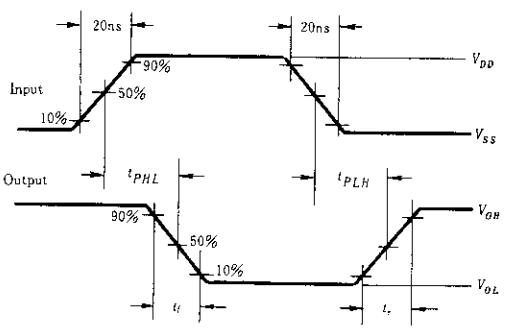
(Top View)

■ LOGIC DIAGRAM (1/4)



■ SWITCHING TIME TEST CIRCUIT





■ ELECTRICAL CHARACTERISTICS

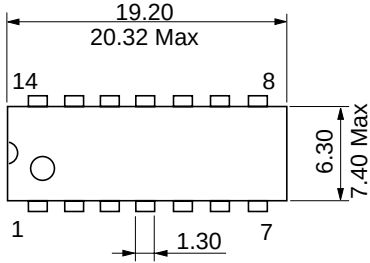
Characteristic	Symbol	V_{DD}(V)	Test Conditions	−40°C min	−40°C max	25°C min	25°C typ	25°C max	85°C min	85°C max	Unit
Output Voltage	V_{OL}	5.0	$V_{in}=V_{DD}$ or 0	—	0.05	—	* 0	0.05	—	0.05	V
		10		—	0.05	—	0	0.05	—	0.05	
		15		—	0.05	—	0	0.05	—	0.05	
	V_{OH}	5.0	V_{in}=0 or V_{DD}	4.95	—	4.95	5.0	—	4.95	—	V
		10		9.95	—	9.95	10	—	9.95	—	
		15		14.95	—	14.95	15	—	14.95	—	
Input Voltage	V_{IL}	5.0	V_{out}=9.0 or 1.0V	—	1.5	—	2.25	1.5	—	1.5	V
		10	V_{out}=9.0 or 1.0V	—	3.0	—	4.50	3.0	—	3.0	
		15	V_{out}=13.5 or 1.5V	—	4.0	—	6.75	4.0	—	4.0	
	V_{IH}	5.0	V_{out}=0.5 or 4.5V	3.5	—	3.5	2.75	—	3.5	—	V
		10	V_{out}=1.0 or 9.0V	7.0	—	7.0	5.50	—	7.0	—	
		15	V_{out}=1.5 or 13.5V	11.0	—	11.0	8.25	—	11.0	—	
Output Drive Current	I_{OH}	5.0	V_{OH}=2.5V	−2.5	—	−2.1	−4.2	—	−1.7	—	mA
		5.0	V_{OH}=4.6V	−0.52	—	−0.44	−0.88	—	−0.36	—	
		10	V_{OH}=9.5V	−1.3	—	−1.1	−2.25	—	−0.9	—	
		15	V_{OH}=13.5V	−3.6	—	−3.0	−8.8	—	−2.4	—	
	I_{OL}	5.0	V_{OL}=0.4V	0.52	—	0.44	0.88	—	0.36	—	mA
		10	V_{OL}=0.5V	1.3	—	1.1	2.25	—	0.9	—	
		15	V_{OL}=1.5V	3.6	—	3.0	8.8	—	2.4	—	
Input Current	I_{in}	15		—	±0.3	—	±0.0001	±0.3	—	±1.0	μA
Input Capacitance	C_{in}	—	V_{in}=0	—	—	—	5.0	7.5	—	—	pF
Quiescent Current	I_{DD}	5.0	Zero Signal, per Package	—	1.0	—	0.0005	1.0	—	7.5	μA
		10		—	2.0	—	0.0010	2.0	—	15	
		15		—	4.0	—	0.0015	4.0	—	30	
Total Supply Current*	I_T	5.0	Dynamic $+I_{DD}$, per Gate, C_L=50pF f=1kHz	—	—	—	1.2	—	—	—	μA
		10		—	—	—	2.4	—	—	—	
		15		—	—	—	3.6	—	—	—	
Hysteresis Voltage	V_H	5.0		0.20	0.42	0.17	0.26	0.39	0.13	0.39	V
		10		0.29	0.65	0.25	0.38	0.60	0.20	0.60	
		15		0.39	1.00	0.33	0.5	0.90	0.27	0.90	
Threshold Voltage	V_T^+	5.0		1.90	4.15	1.80	2.70	4.05	1.70	4.05	V
		10		3.05	6.75	2.95	4.43	6.65	2.85	6.65	
		15		4.12	9.15	4.02	6.03	9.05	3.92	9.05	
	V_T	5.0		1.63	3.76	1.63	2.44	3.66	1.53	3.66	
		10		2.70	6.18	2.70	4.05	6.08	2.60	6.08	
		15		3.59	8.40	3.69	5.53	8.38	3.70	8.30	

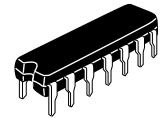
* To calculate total supply current at frequency other than 1kHz.
@V_{DD}=5.0V I_T=(1.2μA/kHz)f+I_{DD}, @V_{DD}=10V I_T=(2.4μA/kHz)f+I_{DD}=15V I_T=(3.6μA/kHz)f+I_{DD}

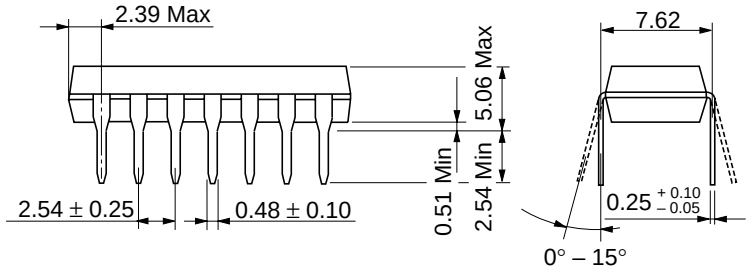
■ SWITCHING CHARACTERISTICS (C_L=50pF, Ta=25°C)

Characteristic	Symbol	V_{DD}(V)	min	typ	max	Unit
Output Rise Time	t_r	5.0	—	100	200	ns
		10	—	50	100	
		15	—	40	80	
Output Fall Time	t_f	5.0	—	100	200	ns
		10	—	50	100	
		15	—	40	80	
Propagation Delay Time	t_{PLH}, t_{PHL}	5.0	—	125	250	ns
		10	—	50	100	
		15	—	40	80	

Unit: mm

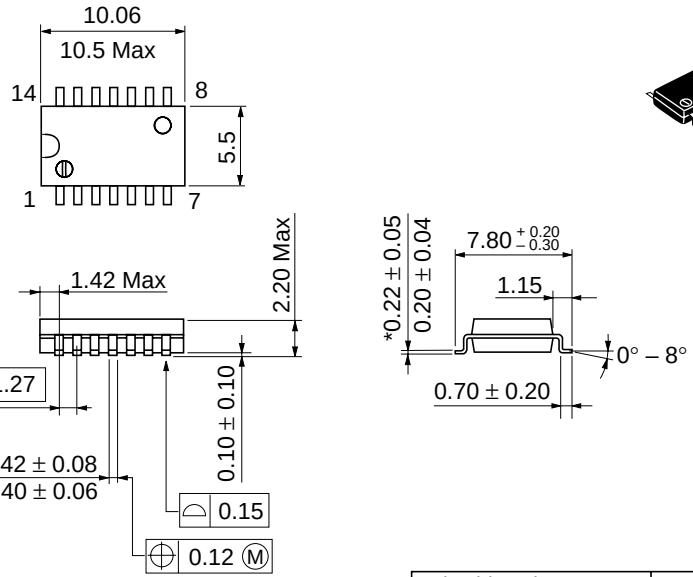


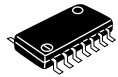




Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

Unit: mm

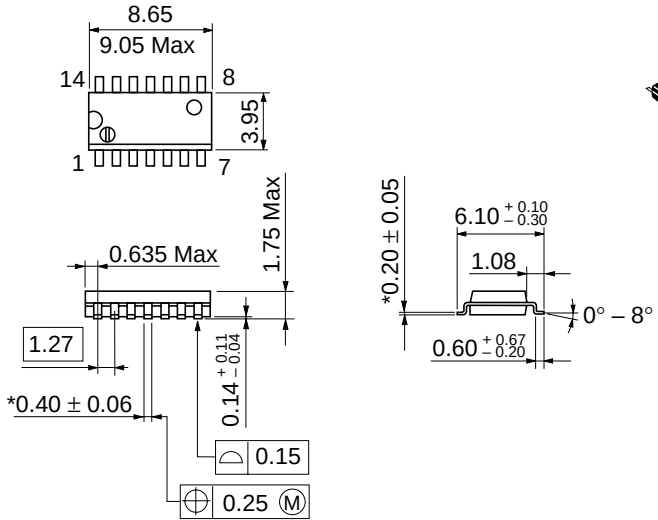




Hitachi Code	FP-14DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.23 g

*Dimension including the plating thickness
Base material dimension

Unit: mm



Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

*Pd plating

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