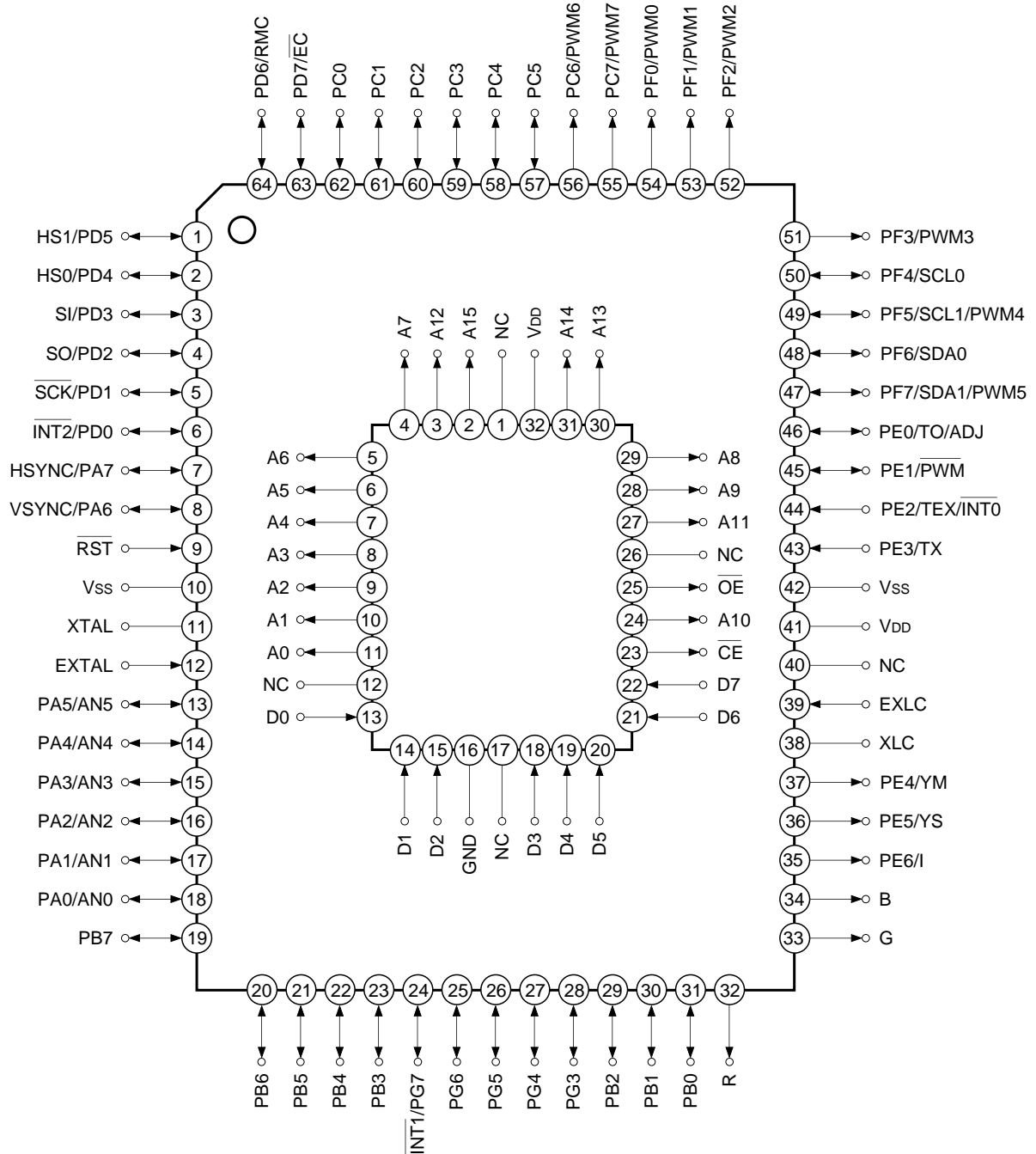


CXP86400

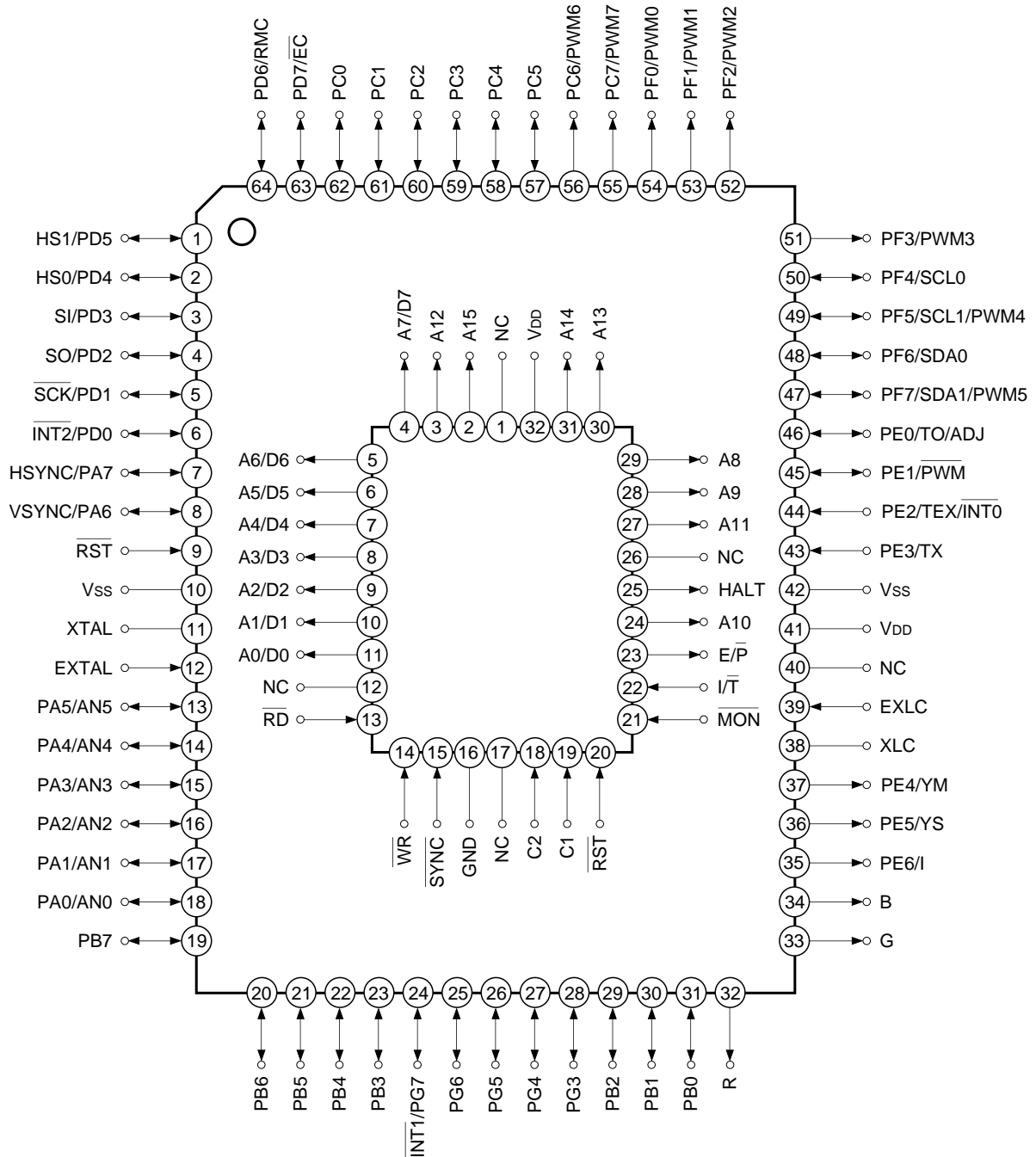
Pin Assignment in Piggyback Mode (Top View)



- Note)** 1. NC (Pin 40) is left open.
 2. Vss (Pins 10 and 42) are both connected to GND.

CXP86400

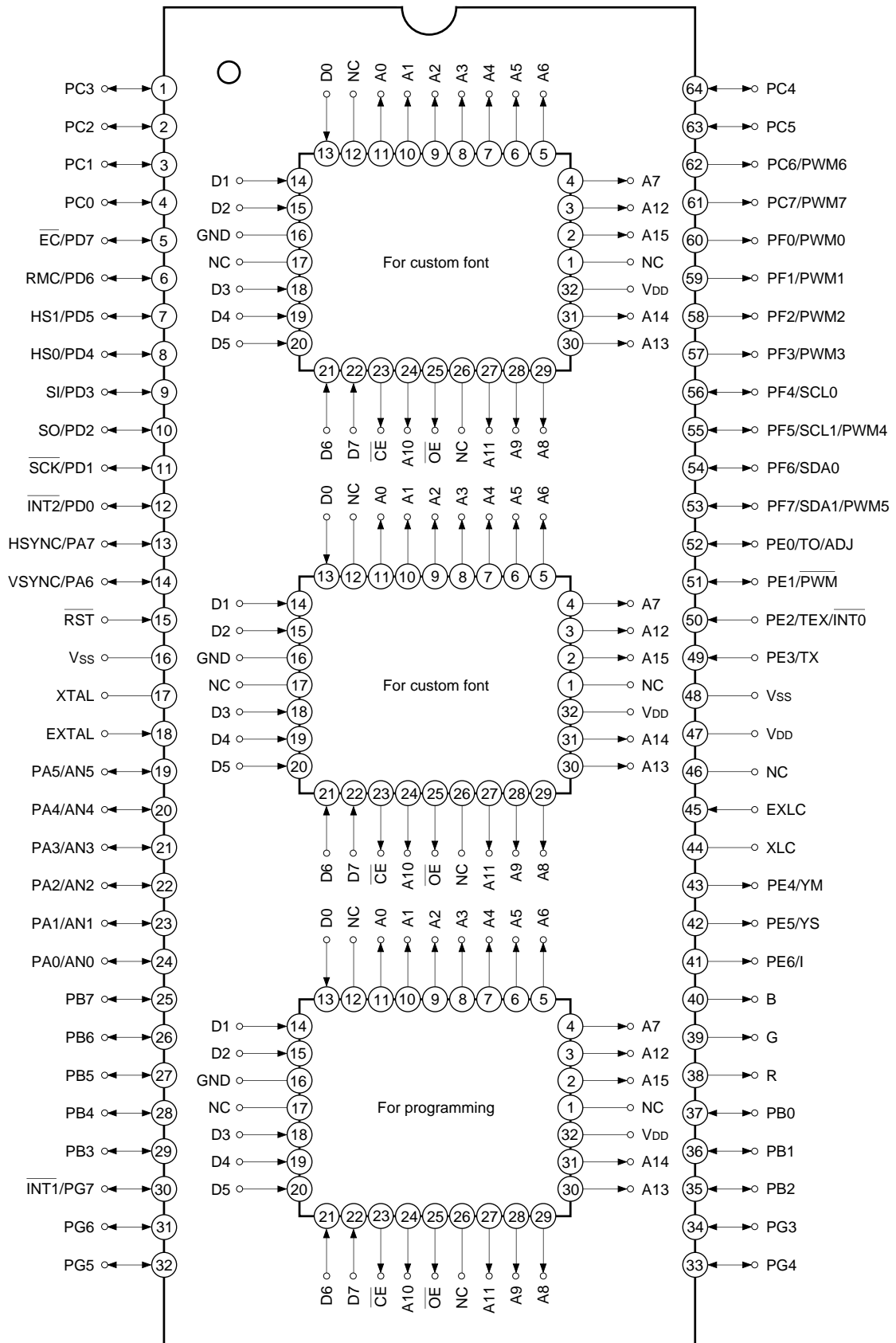
Pin Assignment in Evaluator Mode (Top View)



- Note)**
1. NC (Pin 40) is left open.
 2. Vss (Pins 10 and 42) are both connected to GND.

CXP86490

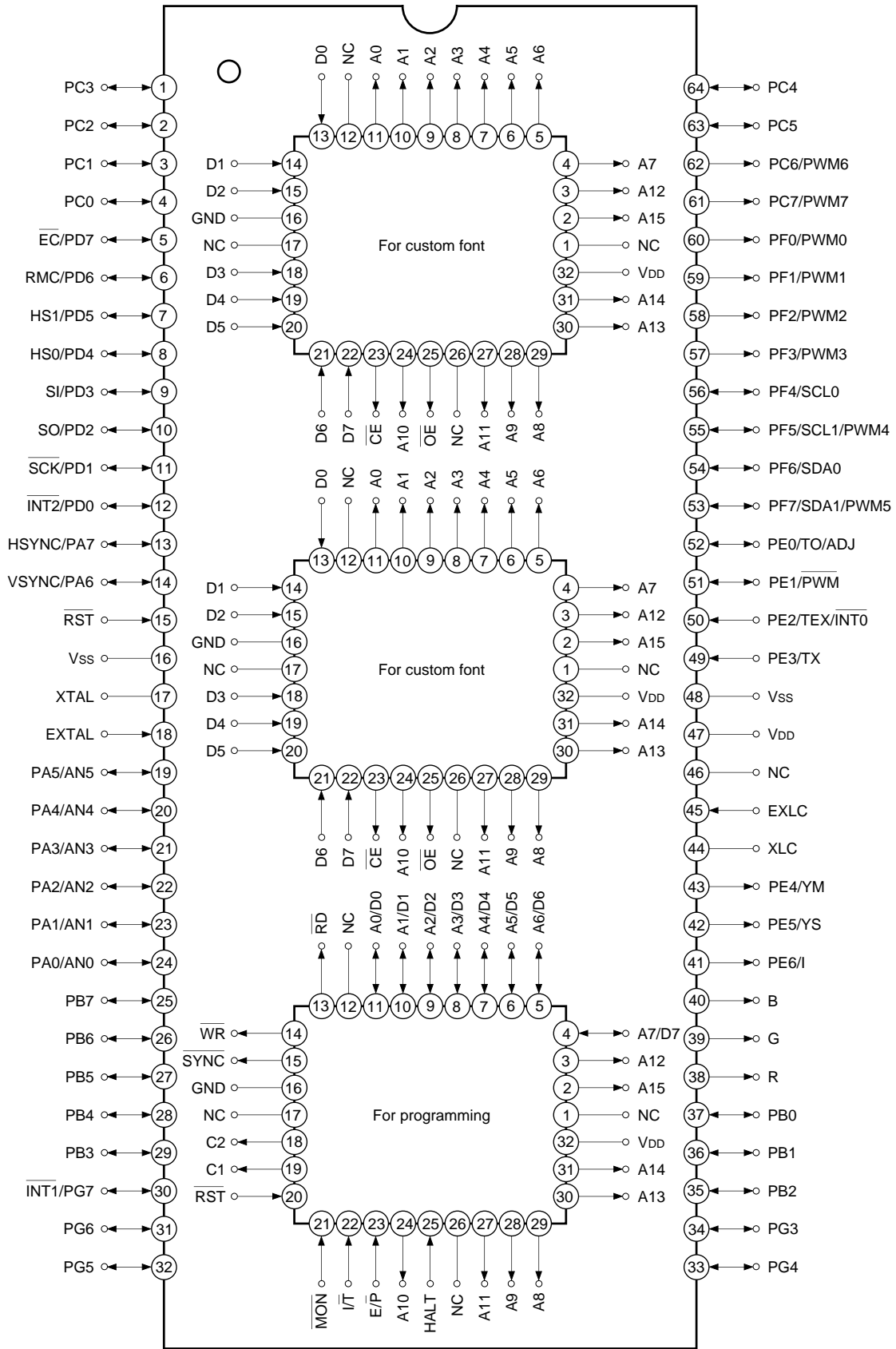
Pin Assignment in Piggyback Mode (Top View)



- Note)** 1. NC (Pin 46) is left open.
 2. Vss (Pins 16 and 48) are both connected to GND.

CXP86490

Pin Assignment in Evaluator Mode (Top View)

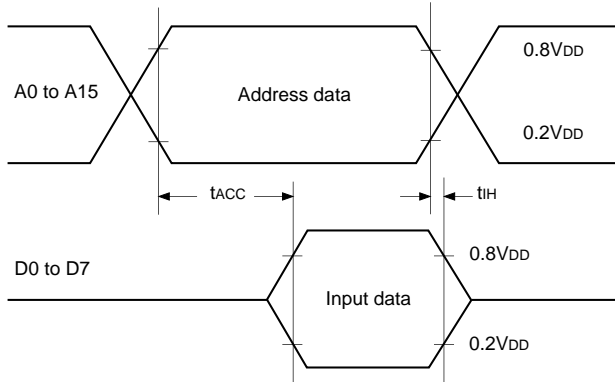


Note 1. NC (Pin 46) is left open.
 2. Vss (Pins 16 and 48) are both connected to GND.

EPROM Read Timing

($T_a = -20$ to $+75^\circ\text{C}$, $V_{DD} = 4.5$ to 5.5V , $V_{SS} = 0\text{V}$)

Item	Symbol	Pins	Min.	Max.	Unit
Address → data input delay time	t_{ACC}	A0 to A15 D0 to D7		75	ns
Address → data hold time	t_{IH}	A0 to A15 D0 to D7	0		ns



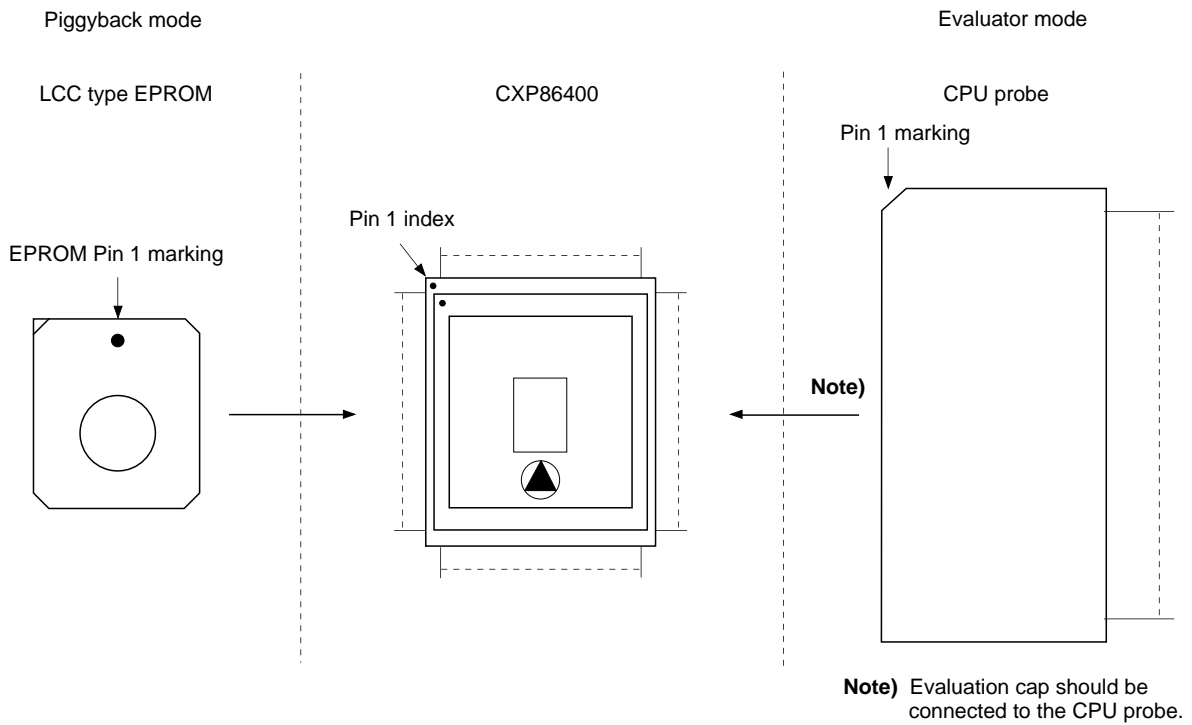
Product List

Optional item	Products									
	Mask									
	CXP86212	CXP86216	CXP86324	CXP86332	CXP86440	CXP86448	CXP86460	CXP86540	CXP86548	CXP86560
Package	64-pin plastic SDIP/QFP/LQFP									
Oscillation clock	16MHz									
ROM capacity	12K bytes	16K bytes	24K bytes	32K bytes	40K bytes	48K bytes	60K bytes	40K bytes	48K bytes	60K bytes
Reset pin pull-up resistor	Existent/Non existent									
Font data	User data									

Optional item	Products	
	Piggyback/evaluator	
	CXP86400-U01Q	CXP86490-U01S
Package	64-pin ceramic PQFP	64-pin ceramic PSDIP
Oscillation clock	16MHz	
ROM capacity	EPROM 60K bytes	
Reset pin pull-up resistor	Existent	
Font data	Fixed	EPROM 12K bytes

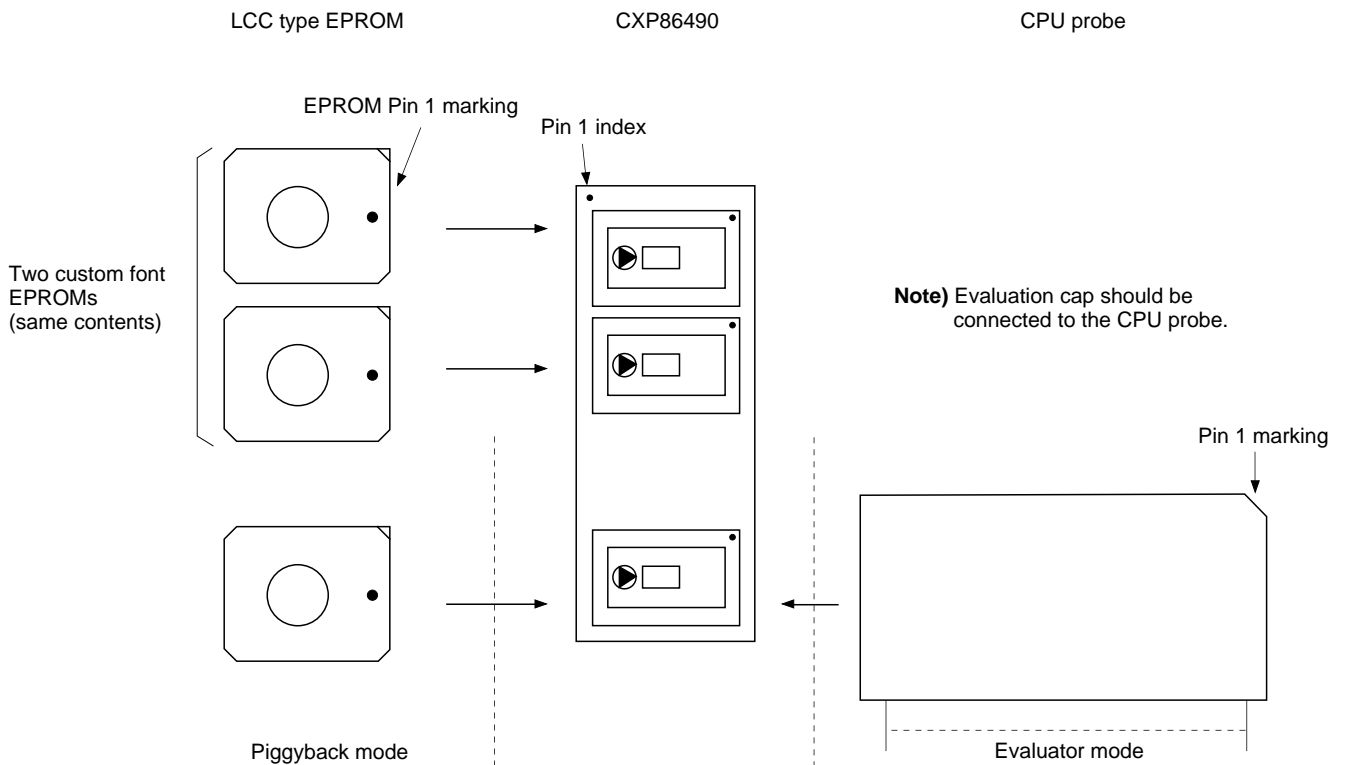
CXP86400

Piggyback mode and evaluator mode can be switched as shown below.



CXP86490

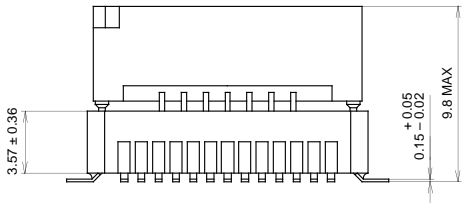
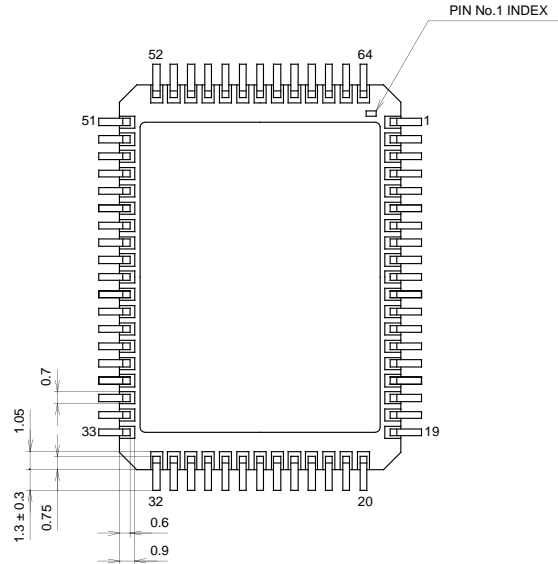
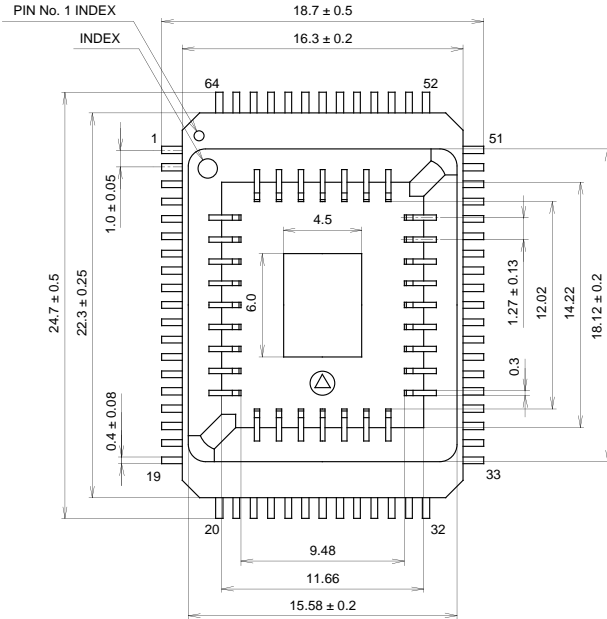
Piggyback mode and evaluator mode can be switched as shown below.



Package Outline Unit: mm

CXP86400

64PIN PQFP (CERAMIC)



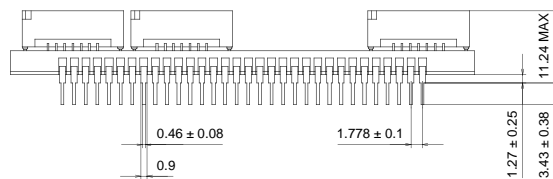
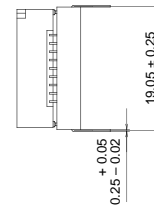
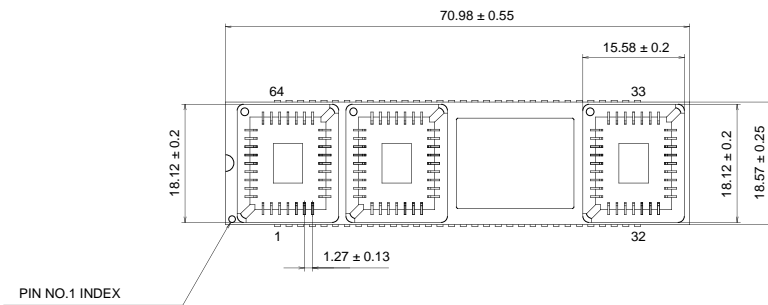
PACKAGE STRUCTURE

PACKAGE MATERIAL	CERAMIC
LEAD TREATMENT	GOLD PLATING
LEAD MATERIAL	42 ALLOY
PACKAGE WEIGHT	5.2g

SONY CODE	PQFP-64C-L02
EIAJ CODE	AQFP064-C-0000-B
JEDEC CODE	—

CXP86490

64PIN PSDIP (CERAMIC)



PACKAGE STRUCTURE

PACKAGE MATERIAL	CERAMIC
LEAD TREATMENT	GOLD PLATING
LEAD MATERIAL	42 ALLOY
PACKAGE WEIGHT	22.8g

SONY CODE	PSDIP-64C-02
EIAJ CODE	ADIP064-C-0750-B
JEDEC CODE	—



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