

# AN6342N

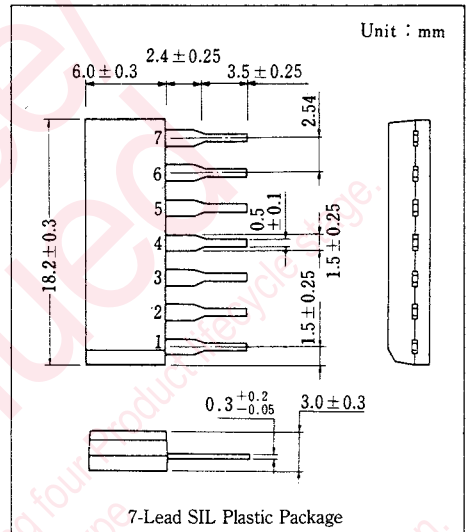
## VTR Reference Frequency Divider

### Outline

The AN6342N is an integrated circuit designed for VTR's reference frequency dividing.

### Features

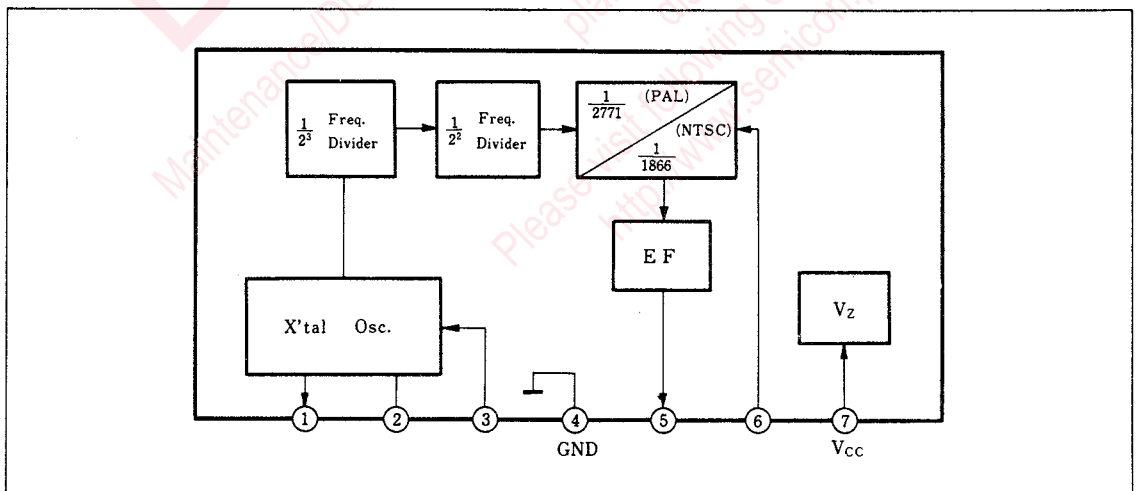
- The functions consist of:  
3.58MHz, 4.43MHz crystal oscillation  
Frequency divider circuit
- Divider ratio changeable either for NTSC system or PAL system
- Divider output frequency  
NTSC system 59.9Hz(3.58MHz)  
PAL system 50.0Hz(4.43MHz)
- Supply voltage either 9V or 12V



### Pin

Pin No.	Pin Name
1	Osc. Output
2	X'tal Osc.
3	X'tal Osc.
4	GND
5	Divide Output
6	NTSC/PAL Select
7	Vcc

### Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

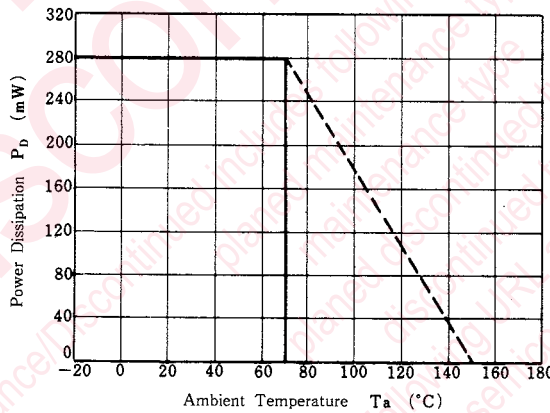
Item	Symbol	Rating	Unit
Supply voltage	V <sub>cc'</sub>	12.5	V
Supply current	I <sub>cc</sub>	22.5	mA
Power dissipation	P <sub>D</sub>	280	°C
Operating ambient temperature	T <sub>opr</sub>	-20~+70	°C
Storage temperature	T <sub>stg</sub>	-40~+150	°C

■ Electrical Characteristics (Ta=25°C)

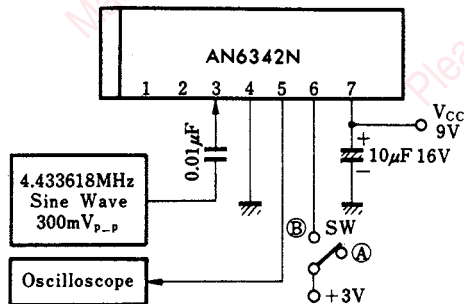
Item	Symbol	Test Circuit	Condition	min.	typ.	max.	Unit
Total circuit current	I <sub>tot</sub>		V <sub>7-4</sub> =9V	13		21	mA
Sensitivity(Divider Input)	S <sub>(1)</sub>	1	V <sub>cc</sub> =9V	400			mV <sub>P-P</sub>
Sensitivity(N/P Switch)	S <sub>(2)</sub>	1		3			V
High-level output voltage (Divider)	V <sub>OH</sub>	1	V <sub>cc</sub> =9V	4.5			V
Low-level output voltage (Divider)	V <sub>OL</sub>	1				0.5	V
Output voltage(X'tal Osc)	V <sub>O</sub>	2	V <sub>cc</sub> =9V, 3.58MHz	2.1		2.8	V <sub>P-P</sub>

Note) Operating supply voltage V<sub>cc(opr.)</sub>=8.8~12.5V

P<sub>D</sub>-T<sub>a</sub>

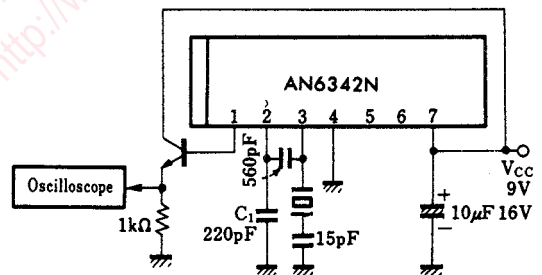


Test Circuit 1 (S<sub>(1)</sub>, S<sub>(2)</sub>, V<sub>OH</sub>, V<sub>OL</sub>)



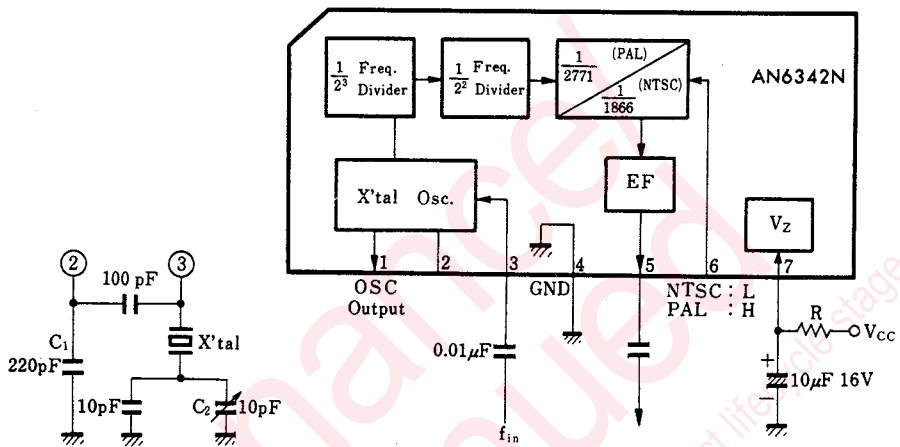
SW: ⊕ when measuring N/P select input sensitivity S<sub>e</sub>

Test Circuit 2 (V<sub>O</sub>)



X'tal: 3.579545MHz used

■ Application Circuit



	f (MHz)	f. Divider Ratio	f. Divider Output (Hz)
NTSC	3.579545	59712	59.9468
PAL	4.433618	88672	50.0002

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