

# DATA SHEET

Part No.	AN16996A
Package Code No.	QFN044-P-0606C

Maintenance/Discontinued includes following lifecycle stage.  
planned maintenance type  
maintenance type  
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# AN16996A

## VIF/SIF IC for Car TV Receivers

### ■ Overview

The AN16996A is a VIF/SIF signal-processing IC for use in car TV receivers. It adopts a split carrier system that completely separates video and audio processing and includes soft muting, SD, and other circuits required for car TV use. It provides superb performance in addition to its extensive functionality.

### ■ Features

- Split carrier audio detection circuit
- Field detection and bandwidth detection soft muting and SD circuits
- Pseudo-synchronous detection strongly resistant to field strength fluctuations is used in the VIF circuit
- Video output pin for diversity detection (no noise inverter required)
- Supports FM audio reception (function that turns off the VIF system)

### ■ Applications

- Car TV

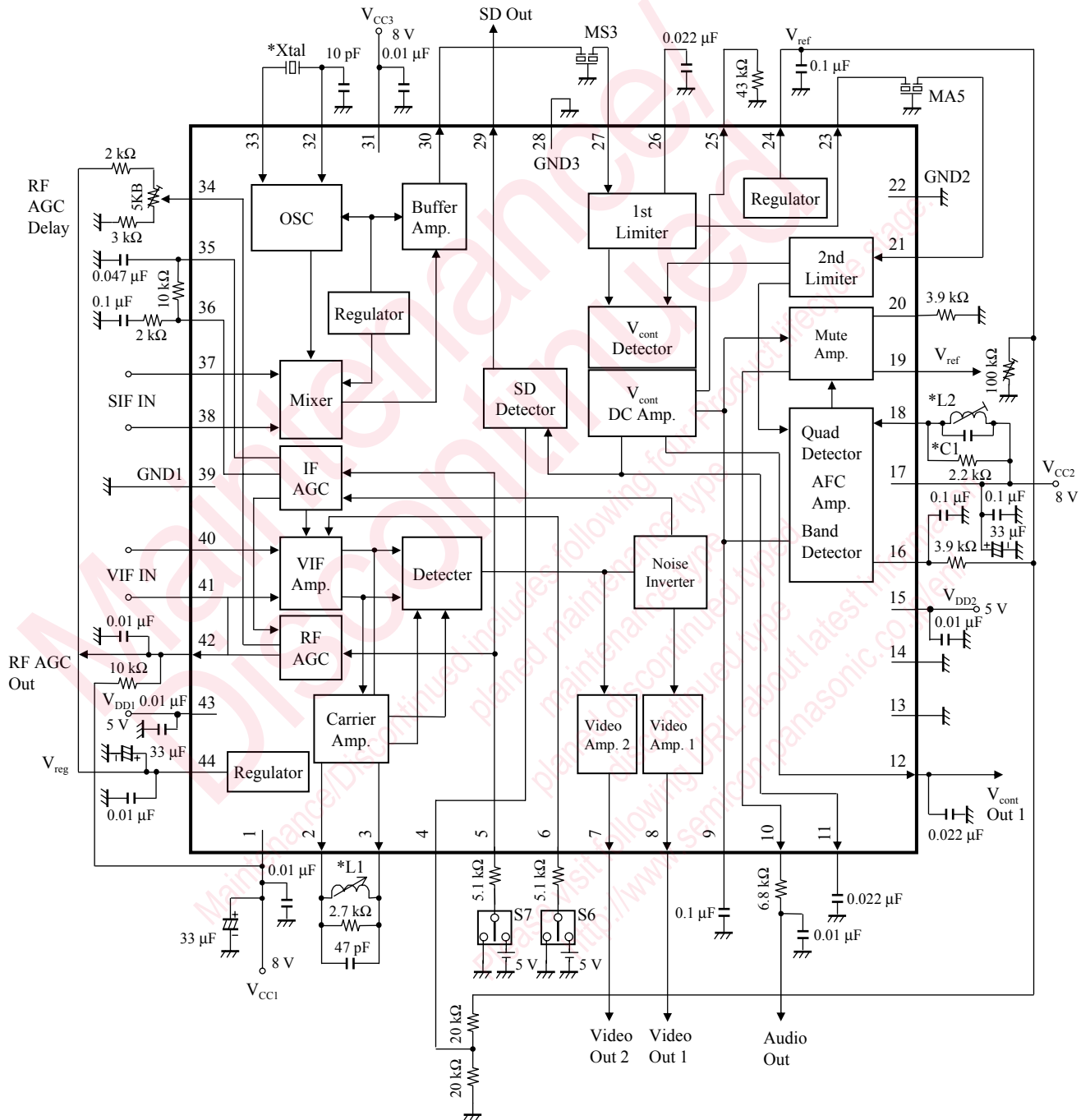
### ■ Package

- 44-pin plastic quad flat non-leaded package (QFN type)

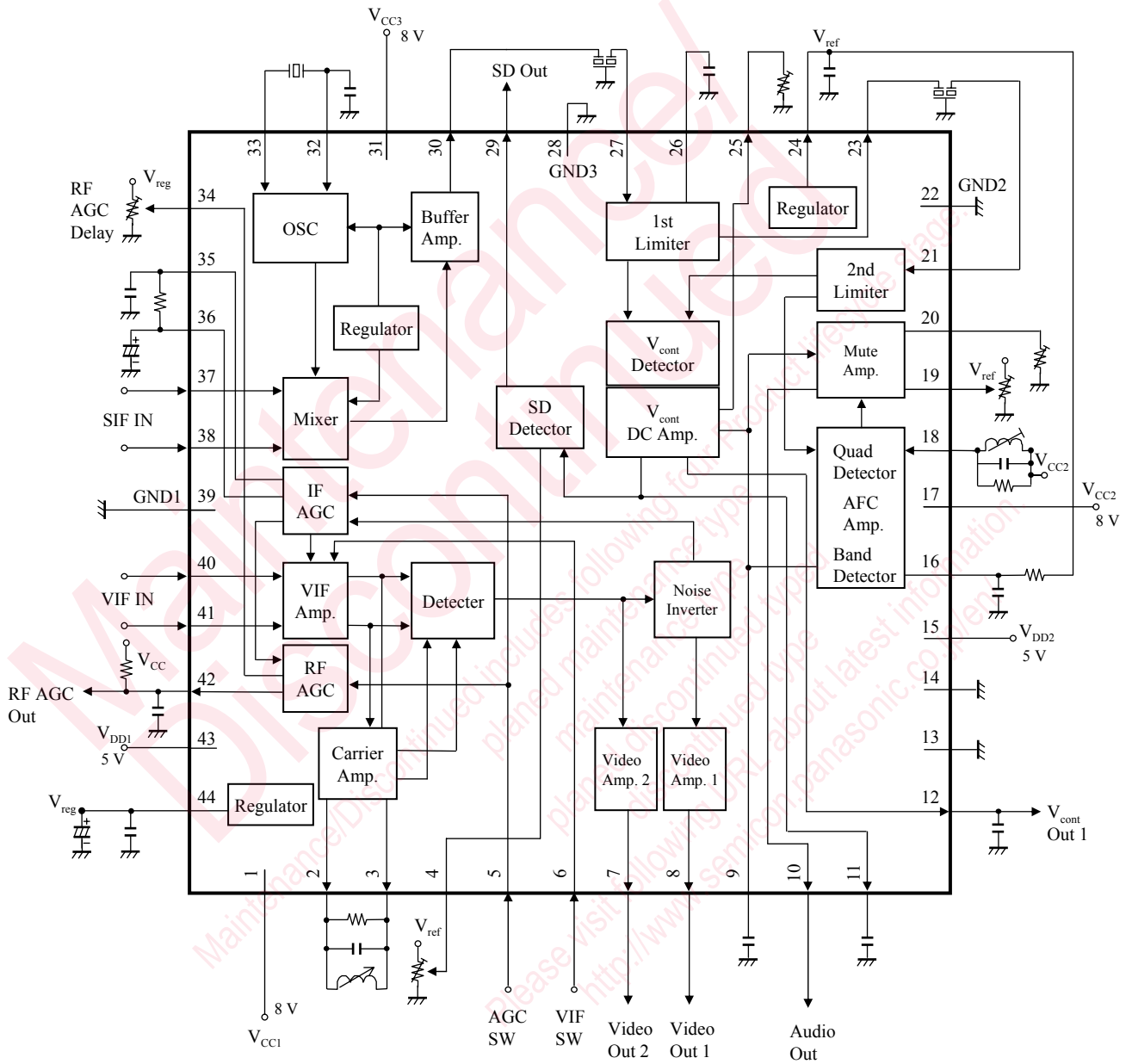
### ■ Type

- Silicon monolithic bipolar IC

Application Circuit Example



■ Block Diagram



### ■ Pin Descriptions

Pin No.	Pin name	Type	Description
1	V <sub>CC1</sub>	Power supply	V <sub>CC1</sub>
2	LC1	Out	VIF detection coil 1
3	LC2	Out	VIF detection coil 2
4	SSC	Out	SD detection adjustment
5	AGCSW	In	AGC switch
6	VIFSW	In	VIF switch
7	VIDEOOUT2	Out	Video output 2
8	VIDEOOUT1	Out	Video output 1
9	SMTC	Out	Muting voltage
10	AUDIOOUT	Out	Audio output
11	VASC	Out	ASC S-meter voltage
12	VCONOUT	Out	S-meter voltage
13	DMON	In	Test pin
14	RMON	In	Test pin
15	V <sub>DD2</sub>	Power supply	V <sub>DD2</sub>
16	AFCOUT	Out	AFC output
17	V <sub>CC2</sub>	Power supply	V <sub>CC2</sub>
18	QUADIN	In	SIF detection coil
19	SMA1	Out	Soft muting start point adjustment
20	SMA2	Out	Soft muting slope adjustment
21	2NDIFIN	In	Second limiter input
22	GND2	Ground	Ground 2
23	SIFLIMOUT	Out	First limiter output
24	VREF	Out	SIF reference voltage
25	VCONADJ	Out	S-meter voltage adjustment
26	IFAB	Out	First limiter bypass
27	IFIN	In	First limiter input
28	GND3	Ground	Ground 3
29	SDOUT	Out	SD output
30	MIXOUT	Out	Mixer output
31	V <sub>CC3</sub>	Power supply	V <sub>CC3</sub>
32	OSC1	In	OSC 1
33	OSC2	In	OSC 2
34	AGCDLY	Out	RF AGC delay adjustment
35	IFAGC1	Out	IF AGC output

## ■ Pin Descriptions (continued)

Pin No.	Pin name	Type	Description
36	IFAGC2	In	IF AGC input
37	SIFIN1	In	SIF input 1
38	SIFIN2	In	SIF input 2
39	GND1	Ground	Ground 1
40	VIFIN1	In	VIF input 1
41	VIFIN2	In	VIF input 2
42	RFAGCOUT	Out	RF AGC output
43	V <sub>DD1</sub>	Power supply	V <sub>DD1</sub>
44	VREG	Out	VIF reference voltage

### ■ Absolute Maximum Ratings

A No.	Parameter	Symbol	Rating	Unit	Notes
1	Supply voltage	$V_{CC1}$	9.0	V	*1
		$V_{CC2}$	9.0		
		$V_{CC3}$	9.0		
		$V_{DD1}$	6.2		
		$V_{DD2}$	6.2		
2	Supply current	$I_{CC}$	—	A	—
3	Power dissipation	$P_D$	229	mW	*2
4	Operating ambient temperature	$T_{opr}$	-30 to +85	°C	*3
5	Storage temperature	$T_{stg}$	-55 to +150	°C	*3

Notes) \*1 : The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

\*2 : The power dissipation shown is the value at  $T_a = 85^\circ\text{C}$  for the independent (unmounted) IC package with out a heat sink.

\*3 : Except for the power dissipation, operating ambient temperature, and storage temperature, all ratings are for  $T_a = 25^\circ\text{C}$ .

### ■ Operating supply voltage range

Parameter	Symbol	Rangs	Unit	Notes
Supply voltage range	$V_{CC1}$	7.2 to 8.8	V	*
	$V_{CC2}$	7.2 to 8.8		
	$V_{CC3}$	7.2 to 8.8		
	$V_{DD1}$	4.6 to 5.5		
	$V_{DD2}$	4.6 to 5.5		

Note) \* : The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

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