



### Software Drivers for the M59BW102 Flash Memory

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#### INTRODUCTION

This application note provides library source code in C for the M59BW102 Flash Memory. The M59BW102 Flash Memory includes a Synchronous Burst bus interface, allowing very high data throughput from a small, low-cost device. The C code drivers are fully compatible with either the Synchronous or the Asynchronous modes of operation.

Listings of the source code can be found at the end of this document. The source code is also available in file form from the internet site <http://www.st.com> or from your STMicroelectronics distributor. The c1202\_16.c and c1202\_16.h files contain libraries for accessing the M59BW102.

Also included in this application note is an overview of the programming model for the M59BW102. This will familiarize the reader with the operation of the memory devices and provide a basis for understanding and modifying the accompanying source code.

The source code is written to be as platform independent as possible and requires minimal changes by the user in order to compile and run. The application note explains how the user should modify the source code for their individual target hardware. All of the source code is backed up by comments explaining how it is used and why it has been written as it has.

This application note does not replace the M59BW102 Data Sheet. It refers to the Data Sheet throughout and it is necessary to have a copy in order to follow some of the explanations.

The software and accompanying documentation has been fully tested on a target platform. It is small in size and can be applied to any target hardware.

#### THE M59BW102 PROGRAMMING MODEL

The M59BW102 is a 1 Mbit (64Kb x16) Flash Memory which can be electrically erased and programmed through special coded command sequences on most standard microprocessor buses.

The M59BW102 is a single voltage device. It differs from first generation devices which require a 12V supply to program or erase. The M59BW102 is therefore easier to use since the hardware does not need to cater for special bus signal levels.

The voltages needed to erase the device are generated by charge pumps inside the device.

Included in the device is a Program/Erase Controller. With first generation Flash Memory devices the software had to manually program all of the bytes to 00h before erasing to FFh using special programming sequences. The Program/Erase Controller in the M59BW102 allows a simpler programming model to be used, by taking care of all the necessary steps required to erase and program the memory. This has led to improved reliability so that in excess of 100,000 program/erase cycles are guaranteed for the device.

### Bus Operations and Commands

Most of the functionality of the M59BW102 is available via the two standard bus operations: read and write. Read operations retrieve data or status information from the device. Write operations are interpreted by the device as commands, which modify the data stored or the behavior of the device. Only certain special sequences of write operations are recognized as commands by the M59BW102. The various commands recognized by the M59BW102 are listed in the Commands Table of the datasheet and can be grouped as follows:

1. Read/Reset
2. Auto Select
3. Erase
4. Program

The Read/Reset command returns the M59BW102 to its reset state where it behaves as a ROM. In this state, a read operation outputs onto the data bus the data stored at the specified address of the device.

The Auto Select command places the device in the Auto Select mode, which allows the user to read the Electronic Signature of the device. These (the Manufacturer and Device Codes) are accessed by reading different addresses whilst in the Auto Select mode.

The Chip Erase command is used to set all the bits to '1' in every memory location in the whole chip. All data previously stored in the memory will be lost. The Chip Erase command typically takes 1.5s to complete on the M59BW102. This is longer than the other commands because the whole device is erased at once.

The Program command is used to modify the data stored at the specified address of the device. Note that programming cannot change bits from '0' to '1'. It may therefore be necessary to erase the device before programming it. Programming modifies a single word at a time. Programming larger amounts of data must be done one word at a time, by issuing a Program command, waiting for the command to complete, then issuing the next Program command, and so on. Each Program command requires 4 write operations to issue.

### The Status Register

While the M59BW102 is programming or erasing, a read from the device will output the Status Register of the Program/Erase Controller. This provides valuable information about the current Program or Erase command. The Status Register bits are described in the Status Register Bits Table of the M59BW102 Data Sheet. Their main use is to determine when programming or erasing is complete and whether it is successful or not.

Completion of the Program or Erase operation can be determined either from the polling bit (Status Register bit DQ7) or from the toggle bit (Status Register bit DQ6), by following the Data Polling Flow Chart Figure or the Data Toggle Flow Chart Figure in the datasheet. The library routines described in this application note use the Data Toggle Flow Chart. However, a function based on the Data Polling Flow Chart is also provided as an illustration.

Programming or erasing errors are indicated by the error bit (Status Register bit DQ5) becoming '1' before the command has completed. If a failure occurs, the command will not complete and read operations will continue to output the Status Register bits until a Read/Reset command is issued to the device.

### A Detailed Example

The Commands Table of the M59BW102 Data Sheet describes the sequences of write operations that will be recognized by the Program/Erase Controller as valid commands. For example programming 65h to the address 03E2h requires the user to write the following sequence (in C):

```
*(unsigned int*)(0x0555) = 0x00AA;  
*(unsigned int*)(0x02AA) = 0x0055;  
*(unsigned int*)(0x0555) = 0x00A0;  
*(unsigned int*)(0x03E2) = 0x9465;
```

This example assumes that address 0000h of the M59BW102 is mapped to address 0000h in the micro-processor address space. In practice it is likely that the Flash will have a base offset which needs to be added to the address.

While the device is programming the specified address, read operations will access the Status Register bits. Status Register bit DQ5 will be '1' if an error has occurred. Bit DQ6 will toggle while programming is on-going. Bit DQ7 will be the complement of the data being programmed.

There are only two possible outcomes to this programming command: success or failure. Success will be indicated by the toggle bit DQ6 no longer toggling but being constant at its programmed value (of '1' in our example) and the polling bit DQ7 also being at its programmed value (of '0' in our example). Failure will be indicated by the error bit DQ5 becoming '1' while the toggle bit DQ6 still toggles and the polling bit DQ7 remains the complement ('1' in our example) of the data being programmed. Note that failure of the device itself is extremely unlikely. If the command fails it will normally be because the user is attempting to change a '0' to a '1' by programming. It is only possible to change a '0' to a '1' by erasing.

### WRITING C CODE FOR THE M59BW102

The low-level functions (drivers) described in this application note have been provided to simplify the process of developing application code in C for the STMicroelectronics Flash Memories. This enables users to concentrate on writing the high level functions required for their particular applications. These high level functions can access the Flash Memories by calling the low level drivers, hence keeping details of special command sequences away from the users' high level code: this will result in source code both simpler and easier to maintain.

Code developed using the drivers provided can be decomposed into three layers:

1. the hardware specific bus operations
2. the low-level drivers
3. the high level functions written by the user

The implementation in C of the hardware specific read and write bus operations is required by the low-level drivers in order to communicate with the M59BW102. This implementation is hardware platform dependent as it is affected by which microprocessor the C code runs on and by where in the microprocessor's address space the memory device is located. The user will have to write the C functions appropriate to his hardware platform (see `FlashRead()` and `FlashWrite()` in the next section).

The low-level drivers take care of issuing the correct sequences of write operations for each command and of interpreting the information received from the device during programming or erasing. These drivers encode all the specific details of how to issue commands and how to interpret the Status Register bits.

The high level functions written by the user will access the memory device by calling the low-level functions. By keeping the specific details of how to access the M59BW102 away from the high level functions, the user is left with code which is simple and easier to maintain. It also makes the user's high level functions easier to apply to other STMicroelectronics Flash Memories.

When developing an application, the user is advised to proceed as follows:

- first write a simple program to test the low level drivers provided and verify that these operate as expected on the user's target hardware and software environments.

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- then the user should write the high level code for his application, which will access the Flash Memories by calling the low level drivers provided.
- finally test the complete application source code thoroughly.

### C LIBRARY FUNCTIONS PROVIDED

The software library provided with this application note provides the user with source code for the following functions:

**FlashReadReset()** is used to reset the device into the Read mode. Note that there should be no need to call this function under normal operation as all of the other software library functions leave the device in this mode.

**FlashAutoSelect()** is used to identify the Manufacturer Code and the Device Code of the device.

**FlashChipErase()** is used to erase the entire chip.

**FlashProgram()** is used to program data arrays into the Flash. Only previously erased words can be programmed reliably.

The functions provided in the software library rely on the user implementing the hardware specific bus operations as well as a suitable timing function. This is to be done by writing three functions as follows:

- **FlashRead()** must be written to read a value from the Flash.
- **FlashWrite()** must be written to write a value to the Flash.
- **FlashPause()** must be written to provide a timer with microsecond resolution. This is used to wait while the Flash recovers from some conditions.

An example of these functions is provided in the source code.

In many instances these functions can be written as macros and therefore will not incur the function call time overhead. The two functions which perform the basic I/O to the device have been provided for users who have awkward systems. For example where the addressing system is peculiar or the data bus has D0..D7 of the device on D8..D15 of the microprocessor. They allow any user to quickly adapt the code to virtually any target system.

Throughout the functions assumptions have been made on the data types. These are:

A **char** is 8 bits (1 byte). This is not the case in all microcontrollers. Where it is not it will be necessary to mask the unused bits of the word (particularly in the user's **FlashRead()** function).

An **int** is 16 bits (2 bytes). Again, like the **char**, if this is not the case it will be necessary to use a variable type which is 16 bits or longer and mask bits above 16 bits.

A **long** is 32 bits (4 bytes). It is necessary to have arithmetic greater than 16 bits in order to address the entire device.

Two approaches to the addressing are available: the desired address in the Flash can be specified by a 32 bit linear pointer or a 32 bit offset into the device could be provided by the user. The **FlashRead()** functions in each case would be declared as:

```
unsigned char FlashRead( unsigned char *Addr);  
unsigned char FlashRead( unsigned long ulOff);
```

The pointer option has the advantage that it runs faster. The 32 bit offset needs to be changed to an address for each access and this involves 32 bit arithmetic. Using a 32 bit offset is, however, more portable since the resulting software can easily be changed to run on microprocessors with segmented memory spaces (such as the 8086). For maximum portability all the functions in this application note use a 32 bit unsigned long offset, rather than a pointer.

### **PORTING THE DRIVERS TO THE TARGET SYSTEM**

Before using the software in the Target System the user needs to write `FlashRead()`, `FlashWrite()` and `FlashPause()` functions appropriate to the Target Hardware. The example `FlashRead()` and `FlashWrite()` functions provided in the source code should give the user a good idea of what is required and can be used in many instances without much modification.

To test the source code in the Target System start by simply reading from the M59BW102. If it is erased then only FFh data should be read. Next read the Manufacturer and Device codes and check they are correct. If these functions work then it is likely that all of the functions will work but they should all be tested thoroughly.

The programmer needs to take extra care when the device is accessed during an interrupt service routine. Two situations exist which must be considered:

1. When the device is in Read mode interrupts can freely read from the device.
2. Interrupts which do not access the device may be used during the Program, Autoselect and Chip Erase functions.

The programmer should also take care when a Reset is applied during Program or Erase operations. The Flash will be left in an indeterminate state and data could be lost.

### **LIMITATIONS OF THE SOFTWARE**

Care should be taken in some of the `while()` loops. No time-outs have been implemented. Software execution may stop in one of the loops due to a hardware error. A `/* TimeOut! */` comment has been put at these places and the user can add a timer to them to prevent the software failing.

The software only caters for one device in the system. To add software for more devices a mechanism for selecting the devices will be required.

When an error occurs the software simply returns the error message. It is left to the user to decide what to do. Either the command can be tried again or, if necessary the device may need to be replaced.

### **CONCLUSION**

The M59BW102 single voltage Flash Memory is an ideal product for embedded and other computer systems, able to be easily interfaced to microprocessors and driven with simple software drivers written in the C language.

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/\***\*\*\*** c1202\_16.h Header File for c1202\_16.c **\*\*\*\*\***\*/

Filename: c1202\_16.h  
Description: Header file for c1202\_16.c. Consult the C file for details

Version: V1.00 For use with C file V1.00

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**\*\*\*\*\***

Commands for the various functions

**\*\*\*\*\***/

```
#define FLASH_READ_MANUFACTURER (-2)
#define FLASH_READ_DEVICE_CODE (-1)
```

**\*\*\*\*\***

Error Conditions and return values.

See end of C file for explanations and help

**\*\*\*\*\***/

```
#define FLASH_SUCCESS (-1)
#define FLASH_POLL_FAIL (-2)
#define FLASH_TOO_MANY_BLOCKS (-3)
#define FLASH_MPU_TOO_SLOW (-4)
#define FLASH_BLOCK_INVALID (-5)
#define FLASH_PROGRAM_FAIL (-6)
#define FLASH_OFFSET_OUT_OF_RANGE (-7)
#define FLASH_WRONG_TYPE (-8)
#define FLASH_BLOCK_FAILED_ERASE (-9)
#define FLASH_FUNCTION_NOT_SUPPORTED (-12)
#define FLASH_ERASE_FAIL (-14)
#define FLASH_TOGGLE_FAIL (-15)
```

**\*\*\*\*\***

Function Prototypes

**\*\*\*\*\***/

```
extern unsigned int FlashWrite( unsigned long ulOff, unsigned int uVal );
extern unsigned int FlashRead( unsigned long ulOff );
extern void FlashReadReset( void );
extern int FlashAutoSelect( int iFunc );
extern int FlashErase( void );
extern int FlashProgram( unsigned long ulOff, size_t NumBytes, void *Array );
extern int FlashDataToggle( void );
static int FlashDataPoll( unsigned long ulOff, unsigned int uVal );
extern char *FlashErrorStr( int iErrNum );
```

/\* **1202\_16.c Flash Memory \*\*\*\*\***

Filename: 1202\_16.c  
Description: Library routines for the M59BW102 Flash Memory.  
  
Version: 1.00 Initial release, fully tested on target platform.  
Date: 14/09/2000  
Author: Tim Webster, Oxford Technical Solutions (www.ots.ndirect.co.uk)

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**\*\*\*\*\***

Version History.

Ver.	Date	Comments
0.00	14/09/2000	Initial creation
1.00	15/09/2000	First release after testing

**\*\*\*\*\***

This source file provides library C code for using the M59BW102 device. The following devices are supported in the code:

M59BW102

The following functions are available in this library:

FlashReadReset()	to reset the flash for normal memory access
FlashAutoSelect()	to get information about the device
FlashErase()	to Erase the flash
FlashProgram()	to program a word or an array
FlashErrorStr()	to return the error string of an error

For further information consult the Data Sheet and the Application Note. The Application Note gives information about how to modify this code for a specific application.

The hardware specific functions which need to be modified by the user are:

FlashWrite()	for writing a word to the flash
FlashRead()	for reading a word from the flash

A list of the error conditions is at the end of the code.

There are no timeouts implemented in the loops in the code. At each point where an infinite loop is implemented a comment `/# TimeOut! #/` has been placed. It is up to the user to implement these to avoid the code hanging instead of timing out.

The source code assumes that the compiler implements the numerical types as

unsigned char	8 bits
---------------	--------

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unsigned int      16 bits  
unsigned long     32 bits

Additional changes to the code will be necessary if these are not correct.

```
*****/
#include <stdlib.h>
#include "1202_16.h" /* Header file with global prototypes */
#define USE_M59BW102

/*****
Constants
*****/
#define MANUFACTURER_ST (0x0020)
#define BASE_ADDR ((volatile unsigned int*)0x0000)
/* BASE_ADDR is the base address of the flash, see the functions FlashRead
and FlashWrite(). Some applications which require a more complicated
FlashRead() or FlashWrite() may not use BASE_ADDR */
#define ANY_ADDR (0x0000L)
/* Any address offset within the Flash Memory will do */

/* Bulk device not block */
#ifdef USE_M59BW102
#define EXPECTED_DEVICE (0x00C1) /* Device code for the M59BW102 */
#define FLASH_SIZE (0x10000) /* 64K */
#endif

/*****
Static Prototypes

The following function is only needed in this module.
*****/
static unsigned int FlashWrite( unsigned long ulOff, unsigned int uVal );

/*****
Function:      unsigned int FlashWrite( unsigned long ulOff, unsigned int uVal )
Arguments:     ulOff is word offset in the flash to write to.
               uVal is the value to be written
Returns:       uVal
Description:   This function is used to write a word to the flash. On many
               microprocessor systems a macro can be used instead, increasing the speed of
               the flash routines. For example:

#define FlashWrite( ulOff, uVal ) ( BASE_ADDR[ulOff] = (unsigned int) uVal )

A function is used here instead to allow the user to expand it
if necessary. The function is made to return uVal so that it is
compatible with the macro.

Pseudo Code:
Step 1: Write uVal to the word offset in the flash
Step 2: return uVal
*****/
static unsigned int FlashWrite( unsigned long ulOff, unsigned int uVal )
{
/* Step1, 2: Write uVal to the word offset in the flash and return it */
```

```

    return BASE_ADDR[uOff] = uVal;
}

```

```

/*****

```

Function: unsigned int FlashRead( unsigned long uOff )

Arguments: uOff is the word offset into the flash to read from.

Returns: The unsigned int at the word offset.

Description: This function is used to read a word from the flash. On many microprocessor systems a macro can be used instead, increasing the speed of the flash routines. For example:

```

#define FlashRead( uOff ) ( BASE_ADDR[uOff] )

```

A function is used here instead to allow the user to expand it if necessary.

Pseudo Code:

Step 1: Return the value at word offset uOff

```

*****/

```

```

unsigned int FlashRead( unsigned long uOff )

```

```

{
    /* Step 1 Return the value at word offset uOff */
    return BASE_ADDR[uOff];
}

```

```

/*****

```

Function: void FlashReadReset( void )

Arguments: none

Return Value: none

Description: This function places the flash in the Read Array mode described in the Data Sheet. In this mode the flash can be read as normal memory.

All of the other functions leave the flash in the Read Array mode so this is not strictly necessary. It is provided for completeness.

Pseudo Code:

Step 1: write command sequence

```

*****/

```

```

void FlashReadReset( void )

```

```

{
    /* Step 1: write command sequence */
    FlashWrite( 0x0555L, 0x00AA ); /* 1st Cycle */
    FlashWrite( 0x02AAL, 0x0055 ); /* 2nd Cycle */
    FlashWrite( ANY_ADDR, 0x00F0 ); /* 3rd Cycle */
}

```

```

/*****

```

Function: int FlashAutoSelect( int iFunc )

Arguments: iFunc should be set to one of the Read Signature values. The header file defines the values for reading the Signature.

Return Value: When iFunc is FLASH\_READ\_MANUFACTURER (-2) the function returns the manufacturer's code. The Manufacturer code for ST is 0020h.

When iFunc is FLASH\_READ\_DEVICE\_CODE (-1) the function returns the Device Code. The device codes for the parts are:  
M59BW102 00C1h

When iFunc is invalid the function returns FLASH\_FUNCTION\_NOT\_SUPPORTED (-12)

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Description: This function can be used to read the electronic signature of the device or the manufacturer code.

Pseudo Code:

Step 1: Send the Auto Select Instruction to the device  
instruction

Step 2: Read the required function from the device

Step 3: Return the device to Read Array mode

\*\*\*\*\*/

```
int FlashAutoSelect( int iFunc )
```

```
{
```

```
    int iRetVal; /* Holds the return value */
```

```
    /* Step 1: Send the Read Electronic Signature instruction */
```

```
    FlashWrite( 0x5555L, 0x00AA ); /* 1st Cycle */
```

```
    FlashWrite( 0x2AAAL, 0x0055 ); /* 2nd Cycle */
```

```
    FlashWrite( 0x5555L, 0x0090 ); /* 3rd Cycle */
```

```
    /* Step 2: Read the required function */
```

```
    if( iFunc == FLASH_READ_MANUFACTURER )
```

```
        iRetVal = FlashRead( 0x0000L ); /* A0 = A1 = 0 */
```

```
    else if( iFunc == FLASH_READ_DEVICE_CODE )
```

```
        iRetVal = FlashRead( 0x0001L ); /* A0 = 1, A1 = 0 */
```

```
    else
```

```
        iRetVal = FLASH_FUNCTION_NOT_SUPPORTED;
```

```
    /* Step 3: Return to Read Array mode */
```

```
    FlashReadReset();
```

```
    return iRetVal;
```

```
}
```

\*\*\*\*\*

```
Function:    int FlashErase(void)
```

```
Arguments:   Void
```

```
Return Value: The function returns the following conditions:
```

```
    FLASH_SUCCESS          (-1)
```

```
    FLASH_WRONG_TYPE      (-8)
```

```
    FLASH_ERASE_FAIL      (-14)
```

Description: This function erases the entire flash memory. During the Erase Cycle the Data Toggle Flow Chart of the Data Sheet is followed. The polling bit, DQ7, is not used.

Pseudo Code:

Step 1: Check for correct flash type

Step 2: Write Flash Erase command

Step 3: Follow Data Toggle Flow Chart until Program/Erase  
Controller has completed

Step 4: Return to Read mode (if an error occurs)

\*\*\*\*\*/

```
int FlashErase( void )
```

```
{
```

```
    int iRetVal = FLASH_SUCCESS; /* Holds return value: optimistic initially! */
```

```
    /* Step 1: Check for correct flash type */
```

```
    if( !(FlashAutoSelect( FLASH_READ_MANUFACTURER ) == MANUFACTURER_ST)
```

```
        || !(FlashAutoSelect( FLASH_READ_DEVICE_CODE ) == EXPECTED_DEVICE ) )
```

```

return FLASH_WRONG_TYPE;

/* Step 2: Write Flash Erase command */
FlashWrite( 0x0555L, 0xAA );
FlashWrite( 0x02AAL, 0x55 );
FlashWrite( 0x0555L, 0x80 );
FlashWrite( 0x0555L, 0xAA );
FlashWrite( 0x02AAL, 0x55 );
FlashWrite( 0x0555L, 0x10 );

/* Step 3: Follow Data Toggle Flow Chart until Program/Erase Controller
  completes */
if( FlashDataToggle() != FLASH_SUCCESS )
{
    iRetVal = FLASH_ERASE_FAIL;
    /* Step 4: Return to Read mode */
    FlashReadReset();
}

return iRetVal;
}

/*****
Function:      int FlashProgram( unsigned long ulOff, size_t NumBytes,
                          void *Array )
Arguments:    ulOff is the byte offset into the flash to be programmed
              NumBytes holds the number of bytes in the array.
              Array is a pointer to the array to be programmed.
Return Value: The function returns the following conditions:
              FLASH_SUCCESS          (-1)
              FLASH_PROGRAM_FAIL    (-6)
              FLASH_OFFSET_OUT_OF_RANGE (-7)
              FLASH_WRONG_TYPE      (-8)

On success the function returns FLASH_SUCCESS (-1).
The function returns FLASH_PROGRAM_FAIL (-6) if a programming failure occurs.
If the address range to be programmed exceeds the address range of the Flash
device the function returns FLASH_OFFSET_OUT_OF_RANGE (-7) and nothing is
programmed.
If the wrong type of flash is detected then FLASH_WRONG_TYPE (-8) is returned
and nothing is programmed.
Description: This function is used to program an array into the flash. It does
not erase the flash first.

Pseudo Code:
Step 1: Check for correct flash type
Step 2: Check the offset range is valid
Step 3: While more words are to be programmed
Step 4: Check for changes from '0' to '1'
Step 5: Program the next word
Step 6: Follow Data Toggle Flow Chart until Program/Erase
        Controller has completed
Step 7: Return to Read Mode
Step 8: Update pointers
*****/
int FlashProgram( unsigned long ulOff, size_t NumBytes, void *Array )
{
    unsigned int *uArrayPointer; /* Use an unsigned char to access the array */
    unsigned long ulLastOff;     /* Holds the last offset to be programmed */

```

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```
/* Step 1: Check for correct flash type */
if( !(FlashAutoSelect( FLASH_READ_MANUFACTURER ) == MANUFACTURER_ST)
|| !(FlashAutoSelect( FLASH_READ_DEVICE_CODE ) == EXPECTED_DEVICE ) )
    return FLASH_WRONG_TYPE;

/* Step 2: Check the offset and range are valid */
ulLastOff = ulOff+NumBytes-1;
if( ulLastOff >= FLASH_SIZE )
    return FLASH_OFFSET_OUT_OF_RANGE;

/* Step 3: While there is more to be programmed */
uArrayPointer = (unsigned int *)Array;
while( ulOff <= ulLastOff )
{
    /* Step 4: Check for changes from '0' to '1' */
    if( ~FlashRead( ulOff ) & *uArrayPointer )
        /* Indicate failure as it is not possible to change a '0' to a '1'
        using a Program command. This must be done using an Erase command */
        return FLASH_PROGRAM_FAIL;

    /* Step 5: Program the next word */
    FlashWrite( 0x0555L, 0xAA ); /* 1st cycle */
    FlashWrite( 0x02AAL, 0x55 ); /* 2nd cycle */
    FlashWrite( 0x0555L, 0xA0 ); /* Program command */
    FlashWrite( ulOff, *uArrayPointer ); /* Program value */

    /* Step 6: Follow Data Toggle Flow Chart until Program/Erase Controller
    has completed */
    /* See Data Toggle Flow Chart of the Data Sheet */
    if( FlashDataToggle() == FLASH_TOGGLE_FAIL )
    {
        /* Step 7: Return to Read Mode (if an error occurred) */
        FlashReadReset();
        return FLASH_PROGRAM_FAIL;
    }

    /* Step 8: Update pointers */
    ulOff++;
    uArrayPointer++;
}

return FLASH_SUCCESS;
}
```

```
/******
Function:      static int FlashDataToggle( void )
Arguments:    none
Return Value: The function returns FLASH_SUCCESS if the Program/Erase Controller
              is successful or FLASH_TOGGLE_FAIL if there is a problem.
Description:  The function is used to monitor the Program/Erase Controller during
              erase or program operations. It returns when the Program/Erase Controller has
              completed. In theM59BW102 Data Sheet the Data Toggle Flow Chart shows the
              operation of the function.
*****
```

### Pseudo Code:

- Step 1: Read DQ6 (into a byte)
- Step 2: Read DQ5 and DQ6 (into another byte)
- Step 3: If DQ6 did not toggle between the two reads then

```

        return FLASH_SUCCESS
Step 4: Else if DQ5 is zero then operation is not yet complete,
        goto 1
Step 5: Else (DQ5 != 0), read DQ6 again
Step 6: If DQ6 did not toggle between the last two reads then
        return FLASH_SUCCESS
Step 7: Else return FLASH_TOGGLE_FAIL
*****/
static int FlashDataToggle( void )
{
    unsigned int u1, u2; /* hold values read from any address offset within
                          the Flash Memory */

    while( 1 ) /* TimeOut!: If, for some reason, the hardware fails then this
                loop may not exit. Use a timer function to implement a timeout
                from the loop. */
    {
        /* Step 1: Read DQ6 (into a word) */
        u1 = FlashRead( ANY_ADDR ); /* Read DQ6 from the Flash (any address) */

        /* Step 2: Read DQ5 and DQ6 (into another word) */
        u2 = FlashRead( ANY_ADDR ); /* Read DQ5 and DQ6 from the Flash (any
                                     address) */

        /* Step 3: If DQ6 did not toggle between the two reads then return
                   FLASH_SUCCESS */
        if( (u1&0x0040) == (u2&0x0040) ) /* DQ6 == NO Toggle */
            return FLASH_SUCCESS;

        /* Step 4: Else if DQ5 is zero then operation is not yet complete */
        if( (u2&0x0020) == 0x0000 )
            continue;

        /* Step 5: Else (DQ5 == 1), read DQ6 again */
        u1 = FlashRead( ANY_ADDR ); /* Read DQ6 from the Flash (any address) */

        /* Step 6: If DQ6 did not toggle between the last two reads then
                   return FLASH_SUCCESS */
        if( (u2&0x0040) == (u1&0x0040) ) /* DQ6 == NO Toggle */
            return FLASH_SUCCESS;

        /* Step 7: Else return FLASH_TOGGLE_FAIL */
        else /* DQ6 == Toggle here means fail */
            return FLASH_TOGGLE_FAIL;
    } /* end of while loop */
}

```

```
#ifndef ILLUSTRATION_ONLY
```

```

/*****
Function:    static int FlashDataPoll( unsigned long ulOff, unsigned int uVal )
Arguments:  ulOff should hold a valid offset to be polled. For programming
            this will be the offset of the word being programmed. For erasing this can be
            any offset in the block(s) being erased. uVal should hold the value being
            programmed. A value of FFh should be used when erasing.
Return Value: The function returns FLASH_SUCCESS if the Program/Erase Controller
              is successful or FLASH_POLL_FAIL if there is a problem.
Description: The function is used to monitor the Program/Erase Controller during
              erase or program operations. It returns when the Program/Erase Controller has

```

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---

completed. In the M59BW102 Data Sheet the Data Polling Flow Chart shows the operation of the function.

Note: This library does not use the Data Polling Flow Chart to assess the correct operation of Program/Erase Controller, but uses the Data Toggle Flow Chart instead. The FlashDataPoll() function is only provided here as an illustration of the Data Polling Flow Chart in the Data Sheet. The code uses the function FlashDataToggle() instead.

Pseudo Code:

```
Step 1: Read DQ5 and DQ7 (into a word)
Step 2: If DQ7 is the same as uVal(bit 7) then
        return FLASH_SUCCESS
Step 3: Else if DQ5 is zero then operation is not yet complete,
        goto 1
Step 4: Else (DQ5 != 0), Read DQ7
Step 5: If DQ7 is now the same as uVal(bit 7) then
        return FLASH_SUCCESS
Step 6: Else return FLASH_POLL_FAIL
```

```
*****/
static int FlashDataPoll( unsigned long ulOff, unsigned int uVal )
{
    unsigned int u;          /* holds value read from valid address */

    while(1 ) /* TimeOut!: If, for some reason, the hardware fails then this
                loop may not exit. Use a timer function to implement a timeout
                from the loop. */
    {
        /* Step 1: Read DQ5 and DQ7 (into a word) */
        u = FlashRead( ulOff );          /* Read DQ5, DQ7 at valid addr */

        /* Step 2: If DQ7 is the same as Value(bit 7) then return FLASH_SUCCESS */
        if( (u&0x0080) == (uVal&0x0080) ) /* DQ7 == DATA */
            return FLASH_SUCCESS;

        /* Step 3: Else if DQ5 is zero then operation is not yet complete */
        if( (u&0x0020) == 0x0000 )
            continue;

        /* Step 4: Else (DQ5 == 1) */
        u = FlashRead( ulOff );          /* Read DQ7 at valid addr */

        /* Step 5: If DQ7 is now the same as uVal(bit 7) then
                return FLASH_SUCCESS */
        if( (u&0x0080) == (uVal&0x0080) ) /* DQ7 == DATA */
            return FLASH_SUCCESS;

        /* Step 6: Else return FLASH_POLL_FAIL */
        else /* DQ7 != DATA here means fail */
            return FLASH_POLL_FAIL;
    } /* end of while loop */
}
#endif /* !ILLUSTRATION_ONLY */
```

```
*****
Function:    char *FlashErrorStr( int iErrNum );
Arguments:  iErrNum is the error number returned from another Flash Routine
Return Value: A pointer to a string with the error message
Description: This function is used to generate a text string describing the
                error from the flash. Call with the return value from another
```

flash routine.

Pseudo Code:

Step 1: Check the error message range.

Step 2: Return the correct string.

```

*****
char *FlashErrorStr( int iErrNum )
{
    static char *str[] = { "Flash Success",
                          "Flash Poll Failure",
                          "Flash Too Many Blocks",
                          "MPU is too slow to erase all the blocks",
                          "Flash Block selected is invalid",
                          "Flash Program Failure",
                          "Flash Address Offset Out Of Range",
                          "Flash is of Wrong Type",
                          "Flash Block Failed Erase",
                          "Flash is Unprotected",
                          "Flash is Protected",
                          "Flash function not supported",
                          "Flash Vpp Invalid",
                          "Flash Erase Fail",
                          "Flash Toggle Flow Chart Failure"};

    /* Step 1: Check the error message range */
    iErrNum = -iErrNum - 1; /* All errors are negative: make +ve & adjust */

    if( iErrNum < 0 || iErrNum >= sizeof(str)/sizeof(str[0])) /* Check range */
        return "Unknown Error\n";

    /* Step 2: Return the correct string */
    else
        return str[iErrNum];
}

```

```

/*****
List of Errors and Return values, Explanations and Help.
*****/

```

Return Name: Flash\_SUCCESS

Return Value: -1

Description: This value indicates that the flash command has executed correctly.

```

*****

```

Error Name: Flash\_POLL\_FAIL

Notes: The Data Polling Flow Chart, which applies to M29 series Flash only, is not used in this library. The function FlashDataPoll() is only provided as an illustration of the Data Polling Flow Chart. This error condition should not occur when using this library.

Return Value: -2

Description: The Program/Erase Controller algorithm has not managed to complete the command operation successfully. This may be because the device is damaged

Solution: Try the command again. If it fails a second time then it is likely that the device will need to be replaced.

```

*****

```

Error Name: Flash\_TOO\_MANY\_BLOCKS

Notes: Applies to M29 series Flash only.

Return Value: -3

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Description: The user has chosen to erase more blocks than the device has.  
This may be because the array of blocks to erase contains the same block more than once.

Solutions: Check that the program is trying to erase valid blocks. The device will only have NUM\_BLOCKS blocks (defined at the top of the file). Also check that the same block has not been added twice or more to the array.

\*\*\*\*\*

Error Name: Flash\_MPU\_TOO\_SLOW

Notes: Applies to M29 series Flash only.

Return Value: -4

Description: The MPU has not managed to write all of the selected blocks to the device before the timeout period expired. See BLOCK ERASE COMMAND section of the Data Sheet for details.

Solutions: If this occurs occasionally then it may be because an interrupt is occurring between writing the blocks to be erased. Search for "DSI!" in the code and disable interrupts during the time critical sections.

If this error condition always occurs then it may be time for a faster microprocessor, a better optimising C compiler or, worse still, learn assembly. The immediate solution is to only erase one block at a time.

Disable the test (by #define'ing out the code) and always call the function with one block at a time.

\*\*\*\*\*

Error Name: Flash\_BLOCK\_INVALID

Notes: Does not apply to M59BW102

Return Value: -5

Description: A request for an invalid block has been made. Valid blocks number from 0 to NUM\_BLOCKS-1.

Solution: Check that the block is in the valid range.

\*\*\*\*\*

Error Name: Flash\_PROGRAM\_FAIL

Return Value: -6

Description: The programmed value has not been programmed correctly.

Solutions: Make sure that the block containing the value was erased before programming. Try erasing the block and re-programming the value. If it fails again then the device may need to be changed.

\*\*\*\*\*

Error Name: Flash\_OFFSET\_OUT\_OF\_RANGE

Return Value: -7

Description: The address offset given is out of the range of the device.

Solution: Check that the address offset is in the valid range.

\*\*\*\*\*

Error Name: Flash\_WRONG\_TYPE

Return Value: -8

Description: The source code has been used to access the wrong type of flash.

Solutions: Use a different flash chip with the target hardware or contact STMicroelectronics for a different source code library.

\*\*\*\*\*

Error Name: Flash\_BLOCK\_FAILED\_ERASE

Notes: Does not apply to M59BW102

Return Value: -9

Description: The previous erase to this block has not managed to successfully erase the block.

Solution: Sadly the flash needs replacing.

\*\*\*\*\*

Return Name: Flash\_UNPROTECTED  
Notes: Applies to some M29 series Flash only. This condition should not occur when using this library.  
Return Value: -10  
Description: The user has requested to unprotect a flash that is already unprotected or the user has requested to re-protect a flash that has no protected blocks. This is just a warning to the user that their operation did not make any changes and was not necessary.

\*\*\*\*\*

Return Name: Flash\_PROTECTED  
Notes: This condition should not occur when using this library.  
Return Value: -11  
Description: The user has requested to protect a flash that is already protected. This is just a warning to the user that their operation did not make any changes and was not necessary.

\*\*\*\*\*

Return Name: Flash\_FUNCTION\_NOT\_SUPPORTED  
Notes: This condition should not occur when using this library.  
Return Value: -12  
Description: The user has attempted to make use of functionality not available on this flash device (and thus not provided by the software drivers). This is simply a warning to the user.

\*\*\*\*\*

Error Name: Flash\_VPP\_INVALID  
Notes: Applies to M28 series Flash only. This error condition should not occur when using this library.  
Return Value: -13  
Description: A Program or a Block Erase has been attempted with the Vpp supply voltage outside the allowed ranges. This command had no effect since an invalid Vpp has the effect of protecting the whole of the flash device.  
Solution: The (hardware) configuration of Vpp will need to be modified to make programming or erasing the device possible.

\*\*\*\*\*

Error Name: Flash\_ERASE\_FAIL  
Return Value: -14  
Description: This indicates that the previous erasure of one block, many blocks or of the whole device has failed.  
Solution: Investigate this failure further by attempting to erase each block individually. If erasing a single block still causes failure, then the Flash sadly needs replacing.

\*\*\*\*\*

Error Name: Flash\_TOGGLE\_FAIL  
Return Value: -15  
Notes: This applies to M29 series Flash only.  
Description: The Program/Erase Controller algorithm has not managed to complete the command operation successfully. This may be because the device is damaged.  
Solution: Try the command again. If it fails a second time then it is likely that the device will need to be replaced.

\*\*\*\*\*/

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If you have any questions or suggestion concerning the matters raised in this document please send them to the following electronic mail address:

*ask.memory@st.com* (for general enquiries)

Please remember to include your name, company, location, telephone number and fax number.

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